

2. ALL CAPACITOR VALUES ARE IN MICROFARADS (μF).

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED

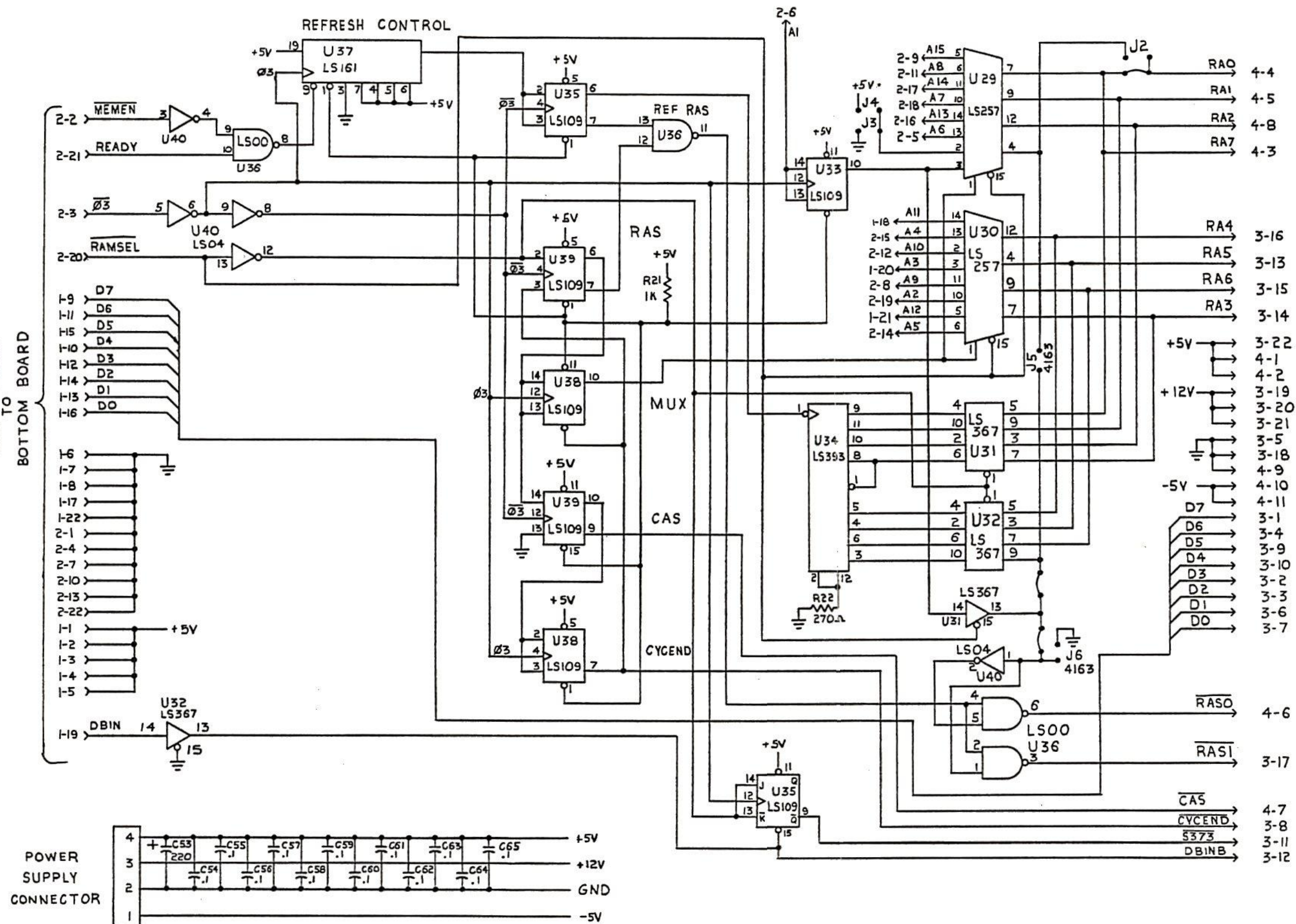


Retrocomputing

-2	-1	ITEM NO	PART OR IDENTIFYING NUMBER	NOMENCLATURE OR DESCRIPTION	REMARKS
QTY REQD					
PARTS LIST					
UNLESS OTHERWISE SPECIFIED • DIMENSIONS ARE IN INCHES • TOLERANCES ANGLES = 3 PLACE DECIMALS = 2 PLACE DECIMALS = • DIMENSIONING IN ACCORDANCE WITH ANSI Y14.5			DWN DATE <i>Rich Becking</i> 4-15-81 CDR DESIGNED BY <i>MA</i> 6-4-81 QA 6/15/81		 TEXAS INSTRUMENTS CALCULATOR PRODUCTS DIVISION
MATERIAL:			ARVD <i>Don't put in 11-7-1</i> PRODUCT ENG DESIGN ACTIVITY RELEASE <i>1-16-81</i> 8-15-81		
56 PRODUCT IN			SUP DWG SIZE DRAWING NO D 1041336		ELECTRONIC SCHEMATIC DIAGRAM RAM EXPANSION PRODUCT 156
			SCALE NONE REV LTR:		SHEET 1 OF 3

CLASS CODE: 40

A



NOTE: FOR 4163'S THREE ETCH CUTS
MUST BE DONE IN AREA INDICATED
AS CONNECTED JUMPER WIRES WITH
NO J#. (THESE ARE MARKED SMALL
ARROW ON ARTWORK) ALSO J2,5,6
MUST BE INSTALLED, J3 OR 4 MUST
BE INSTALLED FOR LOWER OR
UPPER HALF OF CHIP.

FLEX CABLE
TO
TOP BOARD
ONE 22 PIN
ONE 11 PIN

NOTE: FOR 4163'S THREE ETCH CUTS MUST BE DONE IN AREA INDICATED AS CONNECTED JUMPER WIRES WITH NO. J#. (THESE ARE MARKED WITH SMALL ARROW ON ARTWORK) ALSO J1 AND R20 MUST BE INSTALLED. 4163 GO IN PLACE OF U1-U8. C2,5,8,12,15,19,22,25,29,32,35,38,41,44,48,AND 51 MUST BE REMOVED.

1041336

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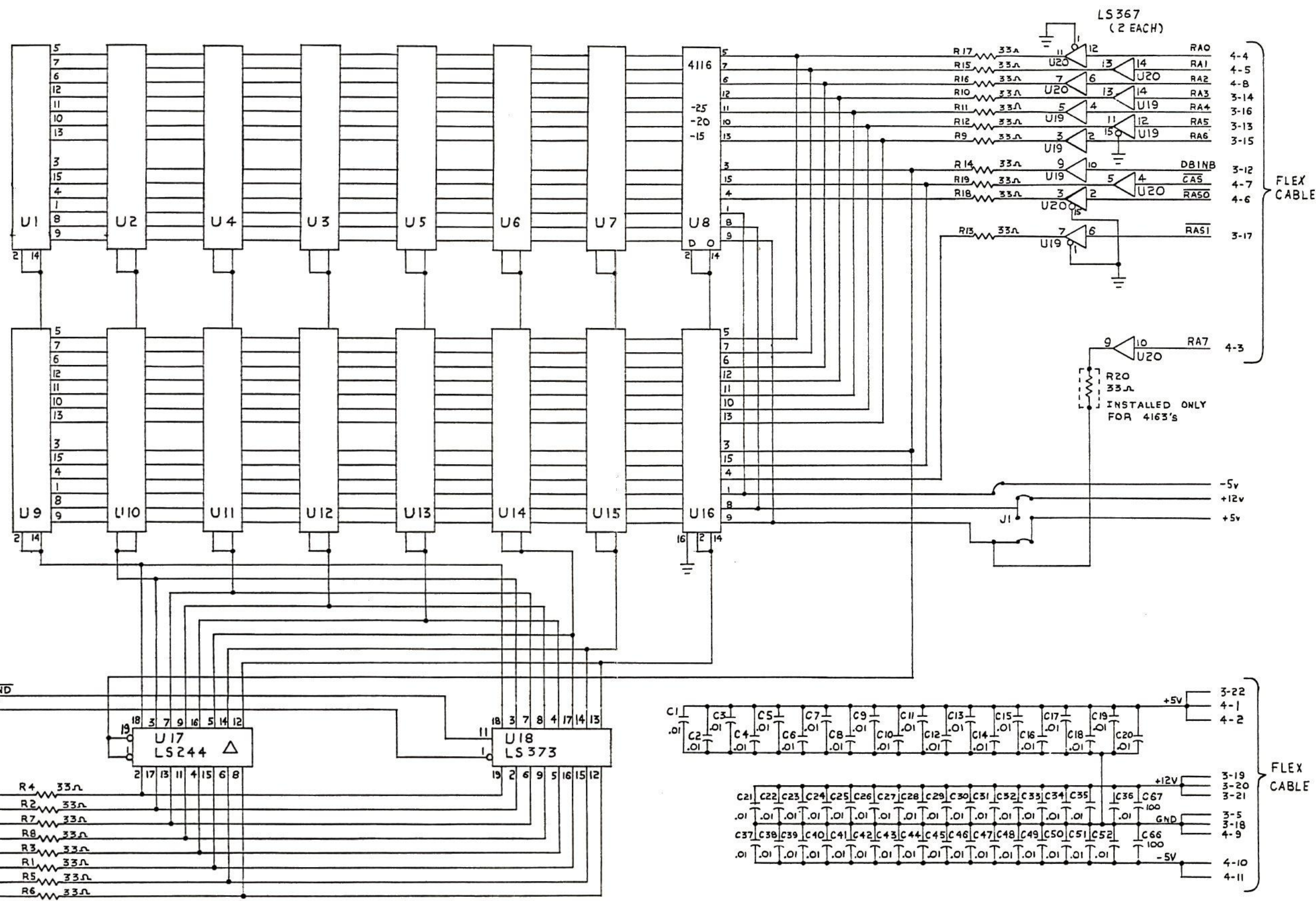
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D

C

B

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TOP BOARD