

# TECHNICAL MANUAL

TIMEX SINCLAIR  
2068  
PERSONAL  
COLOR COMPUTER



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TIMEX SINCLAIR 2068  
PERSONAL COLOR COMPUTER

TECHNICAL REFERENCE MANUAL

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## PREFACE

This manual is dedicated to the many individuals associated with the Timex Computer Corporation in the development and production of the TS2068. Our special thanks to Nan Parsons who prepared the TS2068 Schematic and other drawings used in this manual.

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The Second Edition of this Technical Manual has been re-edited by Tim Woods. Special thanks to Bob Orrfelt and Dave Clifford for technical assistance.

If you would like to receive information on a magazine and other publications for the Timex Sinclair 2068, direct your inquiry to: Time Designs Magazine Company, 29722 Hult Rd., Colton, OR 97017.

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## 1.0 INTRODUCTION

This manual provides detailed technical information on the Timex Sinclair 2068 Personal Color Computer. In conjunction with the TS2068 User Manual, it is intended to assist the reader in understanding the architecture, hardware and software features, programming techniques and I/O techniques pertaining to the TS2068.

### 1.1 TS2068 Overview

#### 1.1.1 Hardware Overview

Figure 1.1-1 is a block diagram of the TS2068 showing the major functional components and their logical connections. These components are:

Control Logic	- SCLD (Standard Cell Logic Device)
CPU	- Z80A Microprocessor
RAM	- 48K Random Access Memory
ROM	- 24K System Read-Only Memory (16K plus 8K Extension)

System Bus Connector  
Cartridge Connector  
Sound Generator/Speaker  
Video Circuits  
Cassette READ/WRITE  
Joystick Connectors

The TS2068 Cartridge Connector provides for the plug-in of cartridges containing programmed ROM's with up to 64K of addressable memory. The full 64K is not normally utilized (e.g., due to need for access to RAM for the machine stack). See Section 5.1 for details.

Figure 1.1-2 shows the standard TS2068 memory configuration comprised of the Home Bank, the ROM Extension Bank and the Dock (Cartridge) Bank. This memory is selectable as eight 8K 'chunks' with the Home Bank being enabled by default, i.e., any chunk not selected in the Extension or Dock Bank is automatically enabled in the Home Bank.

Memory selection and I/O are controlled via the I/O ports. These topics are covered in detail in later sections.

FIGURE 1.1-1

TS 2068 SYSTEM BLOCK DIAGRAM

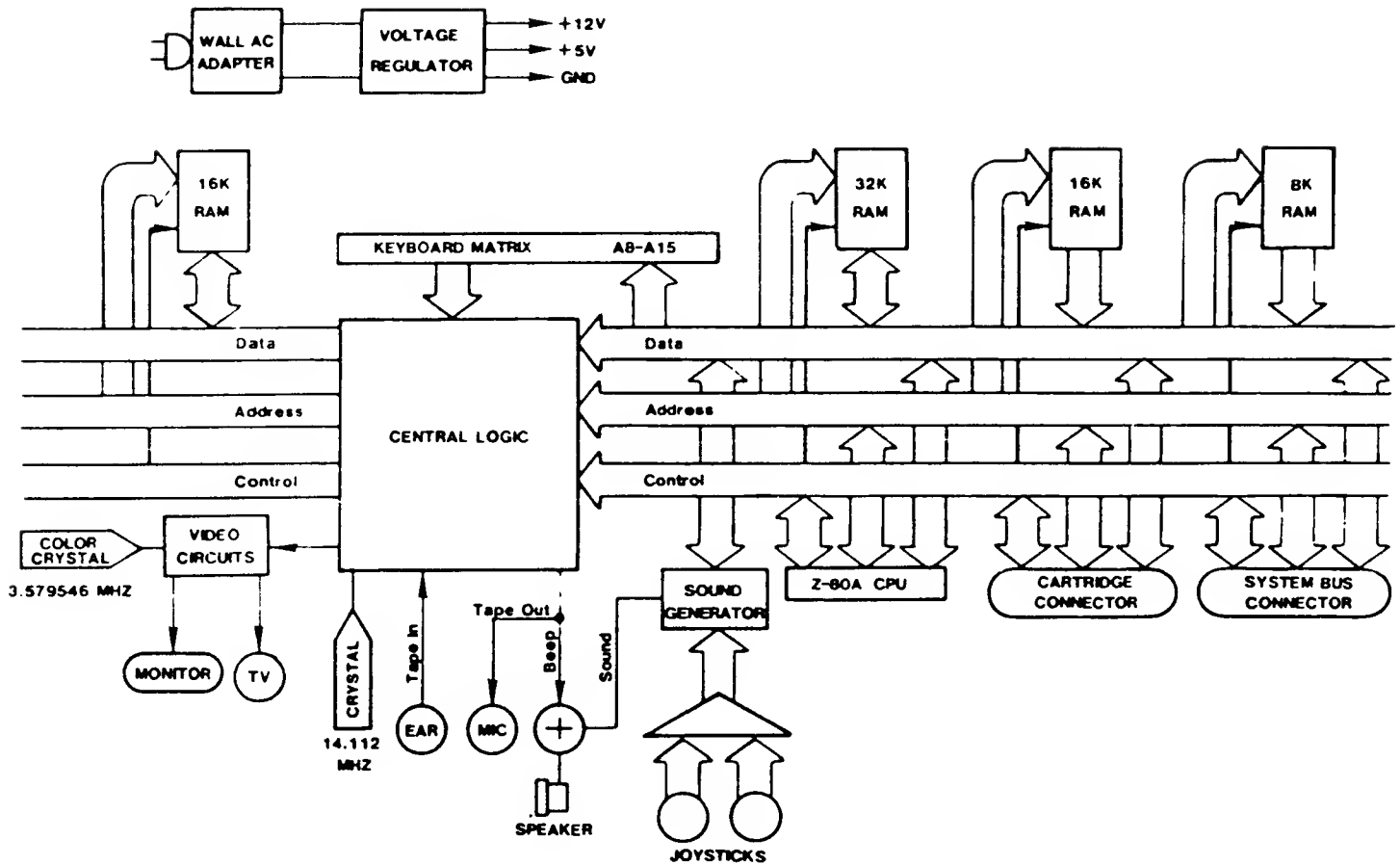
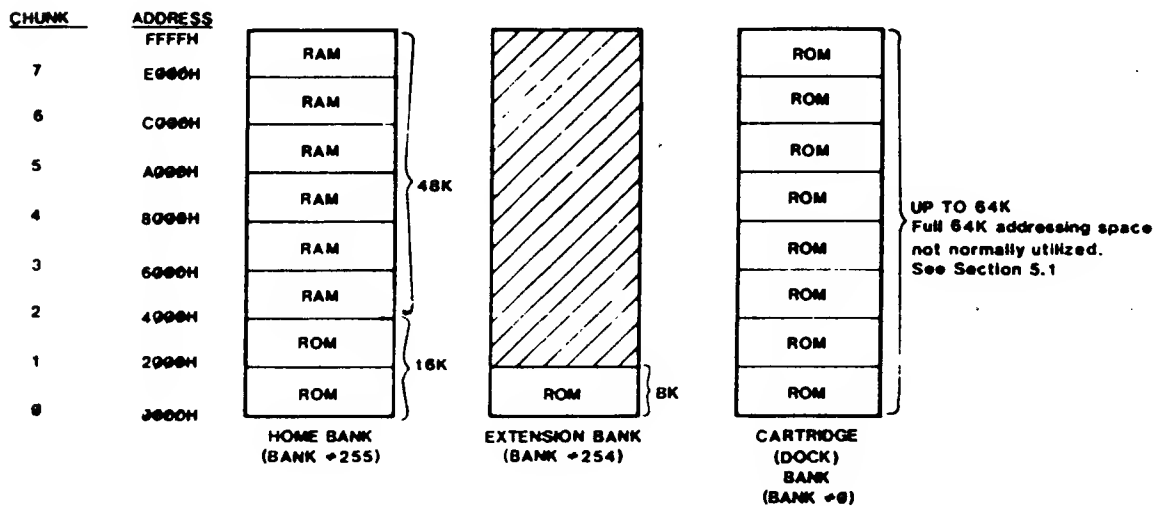


FIGURE 1.1-2

TS 2068 STANDARD MEMORY CONFIGURATION



### 1.1.2 System Software Overview

The TS2068 System Software resides in the Home ROM, the Extension ROM, and dedicated RAM. It supports the following functions:

- System Initialization
- BASIC Interpreter (including BASIC cartridge support)
- BASIC I/O for Standard Peripherals
  - o keyboard
  - o video screen
  - o 2040 32-col. dot matrix printer
  - o cassette tape
  - o joysticks
  - o software generated sound (BEEP)
  - o programmable sound chip (SOUND)
- Video Mode Change Service
- Interruption Servicing (Z80 Int. Mode 1)
- Bank Switching/Data Transfer Services
- Function Dispatcher (provides access to selected system routines via a Service Code input)

In addition, portions of the Home Bank RAM are used for the machine stack, the BASIC system variables, the Printer Buffer and the Display Files. Figure 1.1-3 shows the standard mapping of the Home Bank RAM and the mapping necessary when the second display file is to be used with the BASIC interpreter still functional. The Video Mode Change Service routine makes these memory modifications. Note that there is no direct support of the second display file via BASIC or in the system ROM I/O routines.

Figure 1.1-4 is a Flowchart of the System Initialization process.

**FIGURE 1.1-3**  
**STANDARD MAPPING OF**  
**HOME BANK RAM**

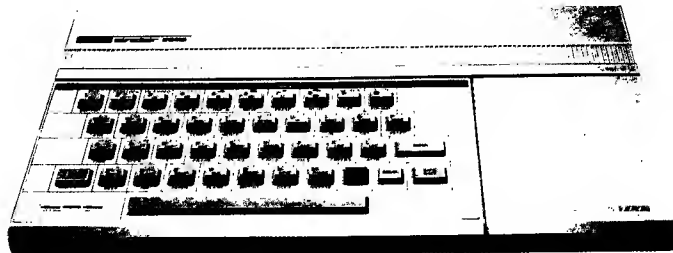
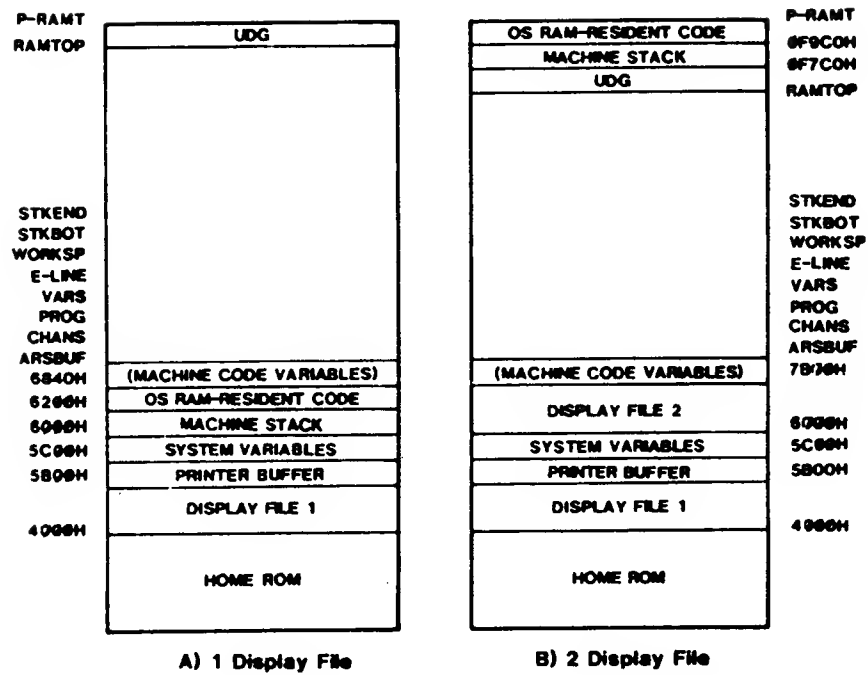
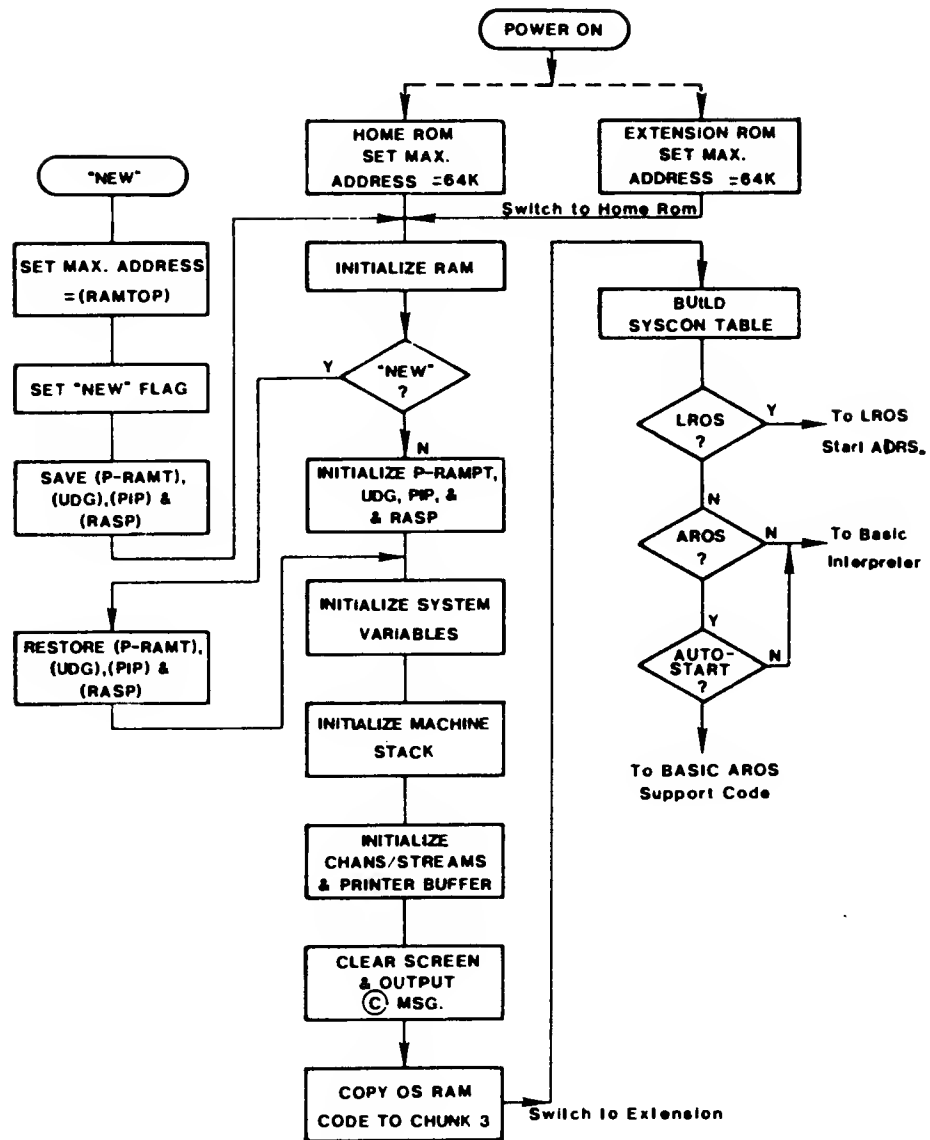


FIGURE 1.1-4  
SYSTEM INITIALIZATION



### 1.1.3 Cartridge Software Overview

The TS2068 supports two basic types of Cartridge or ROM-Oriented Software designated as LROS (Language ROM-Oriented Software) and AROS (Application ROM-Oriented Software) which plug into the cartridge connector. They are identified via overhead bytes at Location 0 for an LROS or 32768 (8000H) for an AROS. The fundamental difference is that an LROS contains Z80 machine code in memory chunk 0 and is in total control of the TS2068 hardware including the RESTART implementation and Interruption Mode setting and handling, while an AROS is dependent on the System ROM or an LROS for these functions if needed. An AROS written in BASIC, which may also include machine code accessed via the USR function, is supported from the System ROM BASIC Interpreter and is mapped beginning in memory chunk 4. An AROS may also be written entirely in Z80 machine code. An AROS written in any other high-level language would require an LROS supporting that language and would have to be integrated with the LROS in a single cartridge.

See Sections 3.2.1.2, BASIC AROS Support and 5.1, Cartridge Software/Hardware, for additional details.



## 2.0 HARDWARE GUIDE

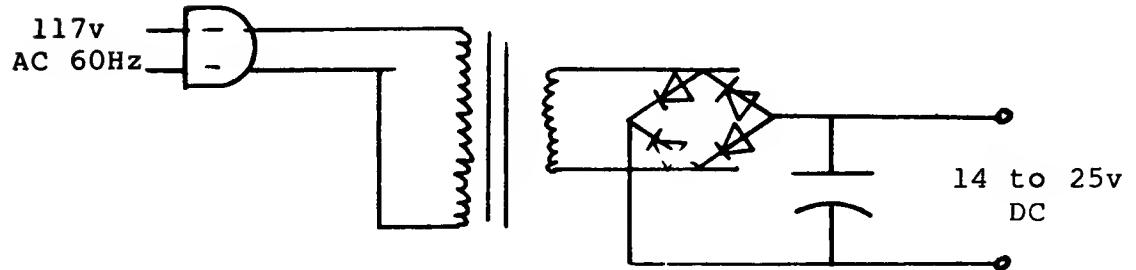
### 2.1 Description of Major Hardware Functions

Figure 1.1-1 shows a simplified block diagram of the TS2068. The following functional units are described in the following sections:

SECTION	FUNCTIONAL UNIT
2.1.1	AC Adapter
2.1.2	Voltage Regulation
2.1.3	Z-80A CPU
2.1.3.1	Address Bus
2.1.3.2	Data Bus
2.1.3.3	Control Signals
2.1.3.4	OP Code Fetch
2.1.3.5	Memory READ/WRITE
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2.1.11.2	RF Modulator
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2.1.13	Port Map

### 2.1.1 AC Adapter

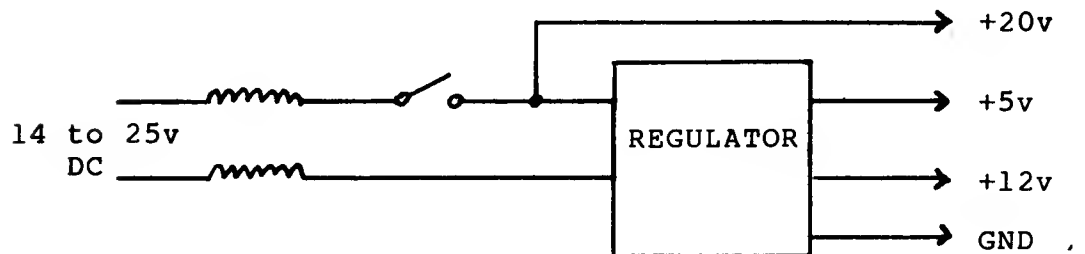
The AC Adapter transforms 117V AC (Nominal) to filtered DC via a step down transformer, full-wave bridge rectifier, and filter capacitor to supply from 14 to 25 volts at 1 amp over the AC voltage variation range of 105 to 130 V AC. Transformer isolation exceeds 1500 volts.



### 2.1.2 Voltage Regulation

Unregulated DC from the AC Adapter is supplied for regulation through a bi-filar torroidal inductor which reduces conducted line emanation for FCC compliance and through the power-ON/OFF switch located on the left side of the TS2068. This switch voltage is supplied to the System Bus Connector (see Section 2.4) and for regulation to the +12 V regulator and the +5 V regulator. Characteristics are as follows:

SUPPLY	VOLTAGE RANGE	CURRENT RANGE
5V	4.75 - 5.25V	200ma - 1.0 A
12V	11.5 - 12.5V	20ma - 100ma



The 12V regulator is a 78L12 series regulator while the 5V regulator is a switching supply utilizing the 78S40 circuit.

### 2.1.3 Z-80A CPU

The Z-80A CPU of the TS2068 operates at a clock frequency of 3.53 MHz. Primary features of this CPU are:

- 158 instructions
- Dual register set
- Two index registers
- On-chip refresh logic

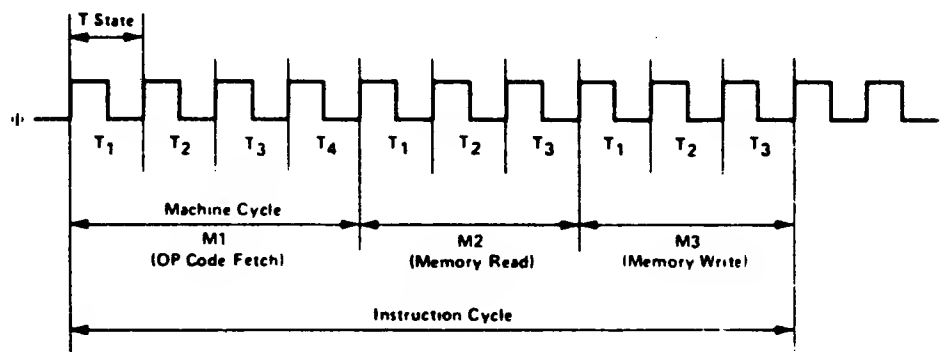
The Z-80 CPU executes instructions by proceeding through a sequence of operations that include:

- a) instruction Op code fetching
- b) READ or WRITE memory
- c) READ or WRITE I/O
- d) Acknowledge an interruption

The basic clock period is referred to as a T time or state and three or more T states make up a machine cycle. In the TS2068, each T-time is approximately 283 nanoseconds ( $2.83 \times 10^{-7}$  seconds). Figure 2.1.3-1 illustrates the basic timing.

FIGURE 2.1.3-1

### BASIC CPU TIMING EXAMPLE



#### 2.1.3.1 Address Bus

Output from the Z-80 are 16-bits of address information, A0 - A15, which are high-active tri-state signals and address for memory data and I/O device exchanges.

#### 2.1.3.2 Data Bus

These input/output signals from the Z-80, D0 - D7, constitute an 8-bit bi-directional, high-active, tri-state data bus used for data exchanges with memory and I/O devices.

#### 2.1.3.3 Control Bus

Associated with the Z-80 are 13 control lines which are provided by or used by the Z-80 to control system operation. These signals are detailed in Table 2-1.

#### 2.1.3.4 Op Code Fetch

The timing during an M1 cycle (Op Code Fetch) is shown in Figure 2.1.3-2. At the beginning of the M1 cycle the PC (Program Counter) is placed onto the address bus, then one-half clock time later the  $\overline{\text{MREQ}}$  signal goes active indicating that the memory address is stable. The  $\overline{\text{RD}}$  signal is activated to indicate that memory read data should be gated onto the data bus. At the rising clock edge during the T3 state, the CPU samples the data on the data bus and deactivates the  $\overline{\text{RD}}$  and  $\overline{\text{MREQ}}$  signals. During the T3 and T4 states, the CPU decodes and executes the fetched instruction and the CPU places on the lower 7 bits of the address bus a memory refresh address and activates the  $\overline{\text{RFSH}}$  signal indicating a refresh read is to begin when  $\overline{\text{MREQ}}$  is activated.

#### 2.1.3.5 Memory READ/WRITE

Memory read or write cycles other than Op Code Fetches are 3 clock periods long with the  $\overline{\text{MREQ}}$  and  $\overline{\text{RD}}$  signals used as in the fetch cycle. During a write cycle the  $\overline{\text{WR}}$  signal is activated when the write data is stable on the data bus. The address and data bus contents remain stable for one-half T state after the  $\overline{\text{WR}}$  signal goes active. Figure 2.1.3-3 illustrates.

FIGURE 2.1.3-2

# INSTRUCTION OP CODE FETCH

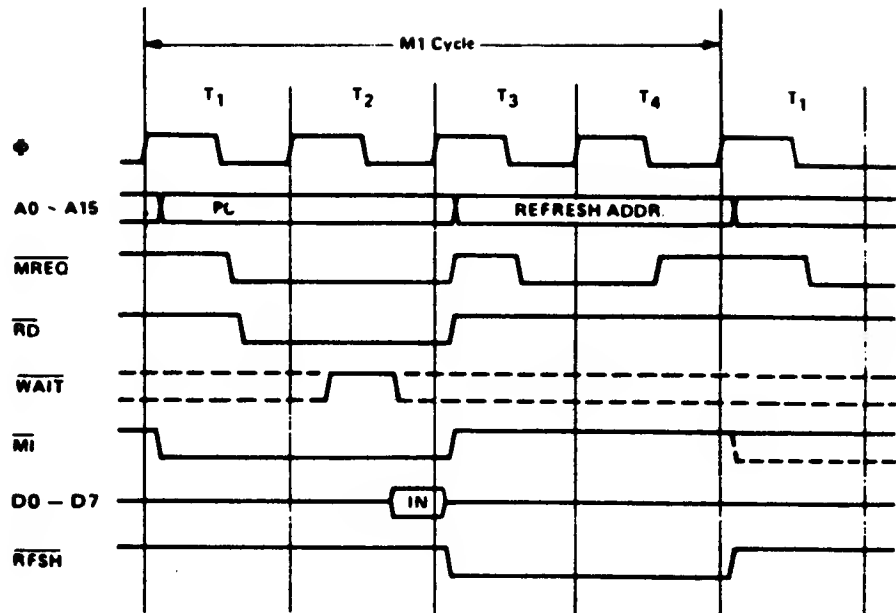
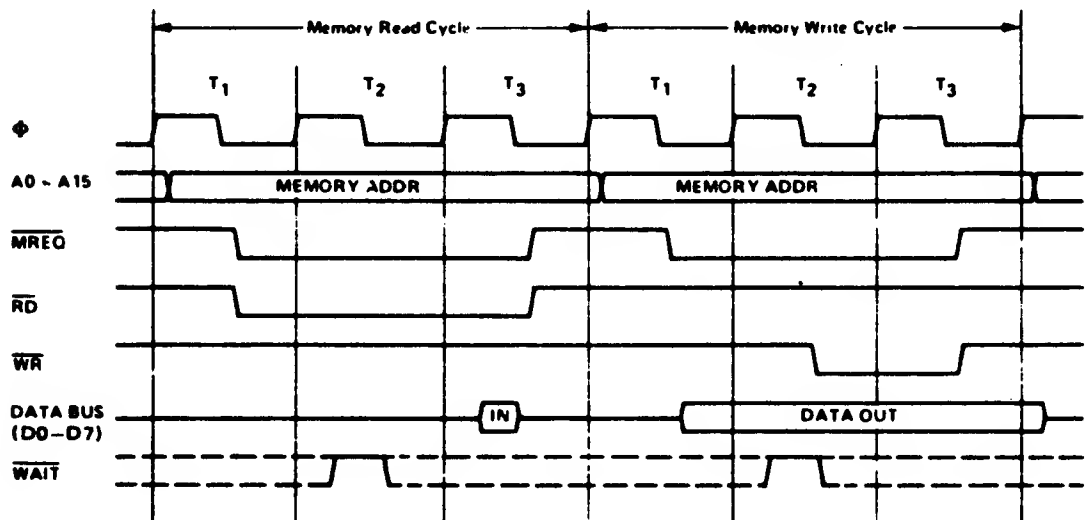


FIGURE 2.1.3-3

# MEMORY READ OR WRITE CYCLES

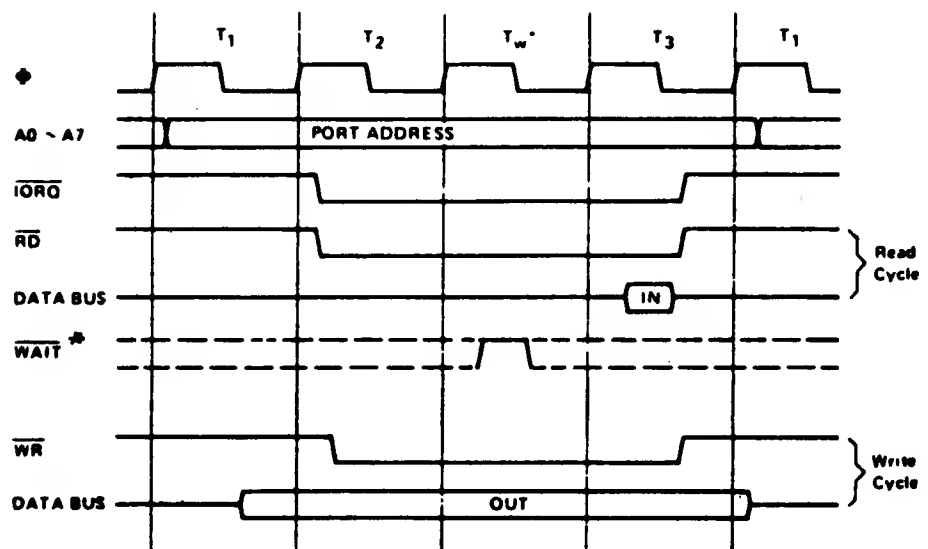


### 2.1.3.6 I/O READ/WRITE

During I/O operations  $\overline{IORQ}$  and  $\overline{RD}$  or  $\overline{WR}$  are activated on the leading edge of the T2 clock and a single  $\overline{Wait}$  state is automatically inserted as illustrated in Figure 2.1.3-4. The  $\overline{RD}$  and  $\overline{WR}$  signals are used to enable data from the addressed port onto the data bus and to, on the rising edge of  $\overline{WR}$ , clock data to the I/O port, respectively. Note that external I/O may stretch the activation period of the  $\overline{WAIT}$  line to extend the I/O cycles.

FIGURE 2.1.3-4

### INPUT OR OUTPUT CYCLES



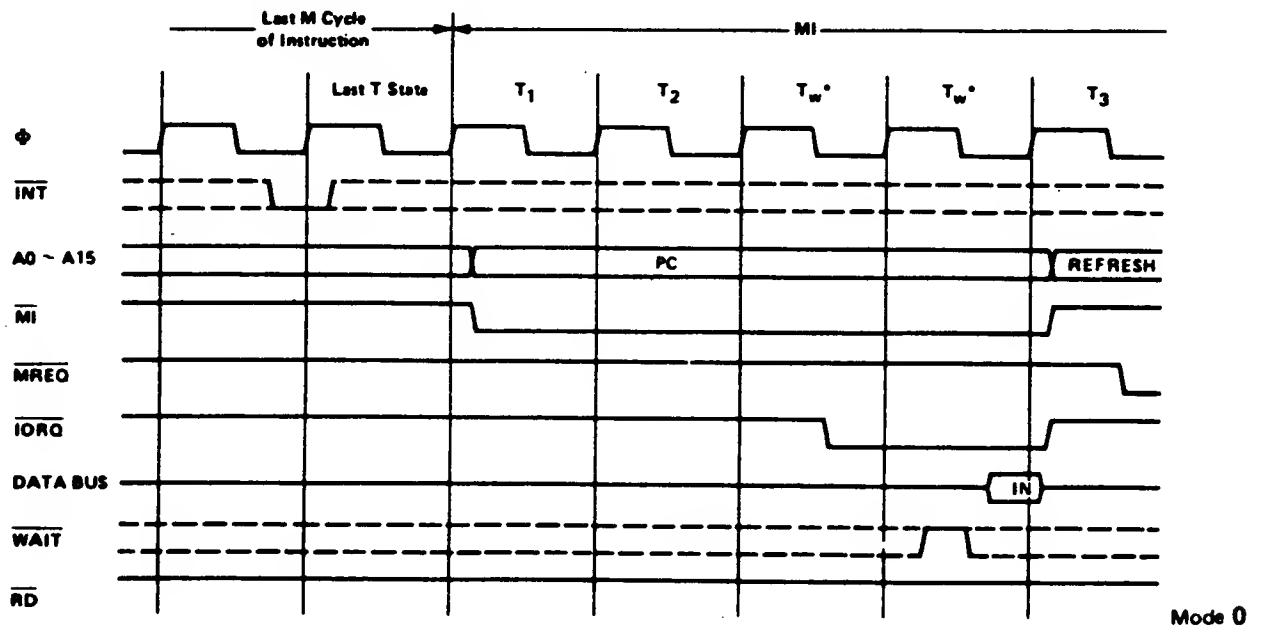
\*Inserted by Z80 CPU

### 2.1.3.7 Maskable Interruption

When enabled by software, when  $\overline{\text{BUSRQ}}$  is not active and when  $\overline{\text{INT}}$  is active at the rising edge of the last clock of any instruction, a maskable interruption occurs during the subsequent M1 cycle, as illustrated in Figure 2.1.3-5.

FIGURE 2.1.3-5

#### INTERRUPT REQUEST/ACKNOWLEDGE CYCLE



In Interruption Mode 0, the interrupting I/O device places any instruction on the data bus during the  $\overline{\text{IORQ}}$  activation and the CPU executes that instruction. The RESTART instruction is commonly used for this purpose. RESET will automatically set Interruption Mode 0.

In Interruption Mode 1, the CPU executes a RESTART to Location 0038H. This is the mode normally used by the TS 2068 software.

In Interruption Mode 2, the CPU concatenates the 8-bit argument, which must be a 2-byte boundary address, with the 8-bit I Register contents to form a 16-bit pointer to a memory table entry containing the 16-bit service routine address - the first byte in the table

being the low order portion of the address. Once the interrupting device supplies the lower portion of the pointer (for concatenation), the CPU automatically pushes the PC onto the stack, obtains the starting address from the table, and does a jump to that address. 19 clock periods are required to complete this sequence.

#### 2.1.3.8 Non-Maskable Interruption (NMI)

A pulse on the ~~NMI~~ input to the Z80 sets the internal latch which is tested by the CPU at the end of each instruction. The NMI has priority over the maskable interruption and its response is identical to the maskable interruption (Mode 1) except that the call location is 0066H instead of 0038H.

- NOTES: 1. The NMI is not used by the TS 2068.
2. Comments in the ROM listing claiming to "mask the NMI" via the DI instruction are incorrect. The DI instruction masks only the maskable interruption.



TABLE 2-1

## Z-80 CONTROL SIGNALS

	<u>ACRONYM</u>	<u>DEFINITION</u>
SYSTEM CONTROL	<u>MT</u>	<u>Machine Cycle 1</u> - Output, active low. This signal indicates that the current machine cycle is the OP code fetch cycle. During execution of instructions having a 2-byte OP code, this signal is generated as each OP code byte is fetched. <u>MT</u> is also used with <u>IORQ</u> to indicate an interrupt acknowledge cycle.
	<u>MREQ</u>	<u>Memory Request</u> - Tri-state output, active low. This signal indicates that the Address Bus holds a valid address for a memory read or write operation.
	<u>IORQ</u>	<u>I/O Request</u> - Tri-state output, active low. This signal indicates that the lower half of the Address Bus holds a valid I/O address for an I/O read or write operation. This signal is also used with <u>MT</u> in connection with acknowledging an interruption, indicating that an interrupt response vector can be placed on the data bus. I/O operations never occur during <u>MT</u> time.
	<u>RD</u>	<u>Memory Read</u> - Tri-state output, active low. This signal indicates that the CPU wants to read data from memory or an I/O device. The addressed memory or device should use this signal to gate the requested data onto the CPU data bus.
	<u>WR</u>	<u>Memory Write</u> - Tri-state output, active low. This signal indicates that the CPU data bus holds valid data to be stored in the addressed memory or I/O device.
	<u>RFSH</u>	<u>Refresh</u> - Output, active low. This signal indicates that the lower 7 bits of the Address Bus contain a refresh address for dynamic memories and the current <u>MREQ</u> signal should be used to do a refresh read to all dynamic memories. A7 is a logic zero and the upper 8 bits of the Address Bus contain the contents of the I Register.

TABLE 2-1  
Z80 CONTROL SIGNALS  
(continued)

	<u>ACRONYM</u>	<u>DEFINITION</u>
CPU CONTROL	<u>HALT</u>	<u>Halt State</u> - Output, active low. This signal indicates that the CPU has executed a HALT instruction. CPU operations are suspended until a Non-Maskable or a Maskable Interruption (with the mask enabled) occurs. While halted, the CPU executes NOP's to maintain memory refresh.
	<u>WAIT</u>	<u>Wait</u> - Input, active low. This signal indicates to the CPU that the addressed memory or I/O device is not ready for a data transfer. The CPU will continue to enter wait states as long as this signal is active. This allows for synchronization of the CPU to external devices of varying speeds.
	<u>INT</u>	<u>Interrupt Request</u> - Input, active low. This signal is generated by external devices and is honored at the end of the current instruction if the interrupt is not masked by the software and if the <u>BUSRQ</u> signal is not active. When the CPU accepts the interruption, an acknowledge signal is sent out at the beginning of the next instruction cycle ( <u>IORQ</u> at M1 time). There are three interruption modes selectable by the software.
	<u>NMI</u>	<u>Non-Maskable Interruption</u> - Input, negative edge triggered. This signal has a higher priority than <u>INT</u> and is always recognized at the end of the current instruction (cannot be masked). The CPU is forced to restart to location 0066H with the program counter saved in the external stack. NOTE: The NMI is not used in the TS2068 ROM software design.

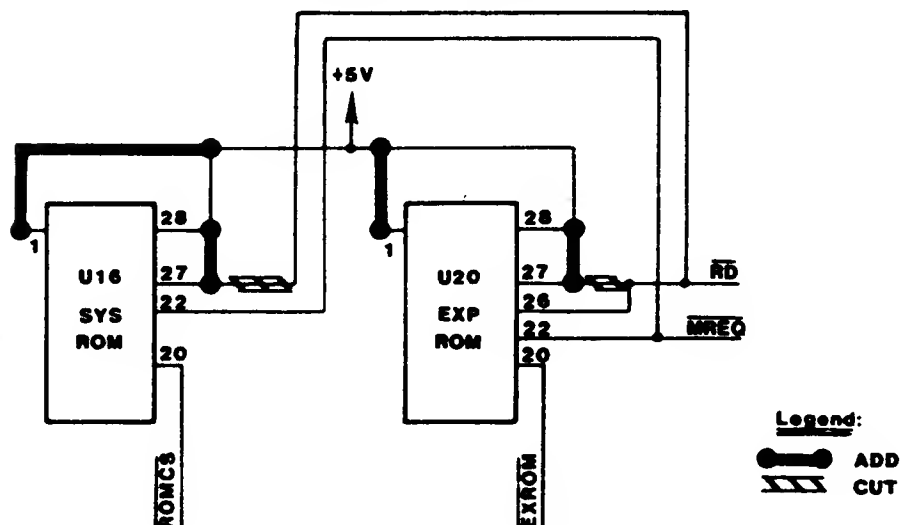
TABLE 2-1

Z80 CONTROL SIGNALS  
(continued)

<u>ACRONYM</u>	<u>DEFINITION</u>
<u>RESET</u>	<u>Reset</u> - Input, active low. This signal forces the program counter to zero and initializes the CPU. Address and data buses go to their high impedance state and control output signals to their inactive state. No refresh occurs. Initialization includes: Disable the interrupt enable flip-flop and set Register I, Register R and the Interrupt Mode all to Zero.
CPU BUS CONTROL	
<u>BUSRQ</u>	<u>Bus Request</u> - Input, active low. This signal is used to request the CPU address bus, data bus and tri-state output control signals to go to a high impedance state permitting other devices to control these buses. The CPU sets these buses to a high impedance state at the termination of the current Machine cycle.
<u>BUSAK</u>	<u>Bus Acknowledge</u> - Output, active low. This signal is used to indicate to the requesting device that the CPU has set its address, data and control bus signals to a high impedance state in response to <u>BUSRQ</u> .

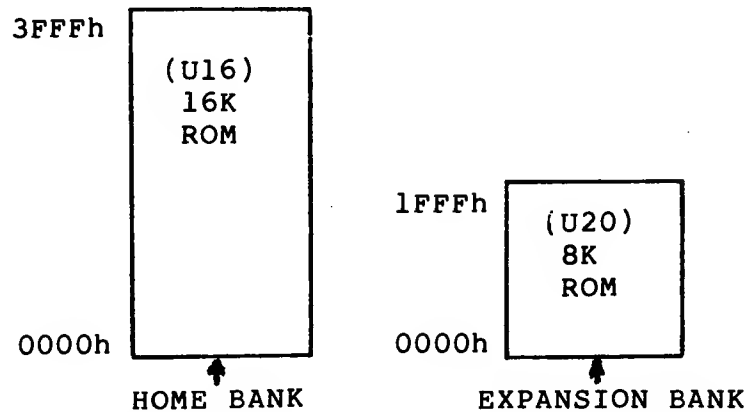
Figure 2.1.4-1

REWORK TO REPLACE ROM's with EPROM's



#### 2.1.4 ROM

The system includes both a 16K byte ROM and an 8K byte ROM mapped into the address space as shown below.



Section 2.1.8.1 describes the selection of the Home Bank and Expansion Bank via the control logic.

The devices involved are a 23128 and a 2364 for the 16K byte (128K-bit) and the 8K byte (64K-bit) ROM's respectively. Direct replacement of these devices with 27128 and 2764 EPROM's is not possible since pins 1 and 27 must be maintained in the high state for those devices (see schematic in Section 2.2). To replace U16 and U20 with 27128 and 2764 EPROM's requires the rework shown in Figure 2.1.4-1.

- (1) Cut input to pin 27 on each chip.
- (2) Wire +5V to pins 1 and 27 on each chip to pull high.

If U20 is to be a 27128, then replace the RD input to pin 26 with address A13 from pin 26 on U16.

#### 2.1.5 32K RAM (Address 8000-FFFFH)

The upper 32K of RAM is composed of four 200ns 4416's (16K x 4 dynamic RAMs).

## 2.1.6 Sound Generator

The Programmable Sound Generator (GI 8912) is accessed via Ports 0F5H (Address) and 0F6H (Data). The basic registers in the PSG which produce the programmed sounds include:

Tone Generators: Produce the basic square wave tone frequencies for each channel (A, B, C).

Noise Generator: Produces a frequency modulated pseudo-random pulse width square wave output.

Mixers: Combine the outputs of the Tone Generators and the Noise Generator. One for each channel (A, B, C).

Amplitude Control: Provides the D/A Converters with either a fixed or variable amplitude pattern. The fixed amplitude is under direct CPU control; the variable amplitude is accomplished by using the output of the Envelope Generator.

Envelope Generator: Produces an envelope pattern which can be used to amplitude modulate the output of each Mixer.

D/A Converters: The three D/A Converters each produce up to a 16-level output signal as determined by the Amplitude Control.

An additional register is shown in the PSG Block Diagram (Figure 2.1.6-1) which has nothing directly to do with the production of sound -- this is the I/O Port (A). Data to/from the CPU may be read/written to/from the 8-bit I/O Port without affecting any other function of the PSG. The TS 2068 uses the I/O Port to access the joysticks.

### 2.1.6.1 Tone Generator Control (Registers R0-R5)

The frequency of each square wave generated by the three Tone Generators (one each for Channels A, B, and C) is obtained in the PSG by first counting down the input clock by 16, then by further counting down the result by the programmed 12-bit Tone Period value. Each 12-bit value is obtained in the PSG by combining the contents of the relative Coarse and Fine Tune registers, as illustrated by Figure 2.1.6-2.

Note that the 12-bit value programmed in the combined Coarse and Fine Tune registers is a period value -- the higher the value in the registers, the lower the resultant tone frequency.

Note also that due to the design technique used in the Tone Period countdown, the lowest period value is 000000000001 (divide by 1) and the highest period value is 111111111111 (divide by 4095).

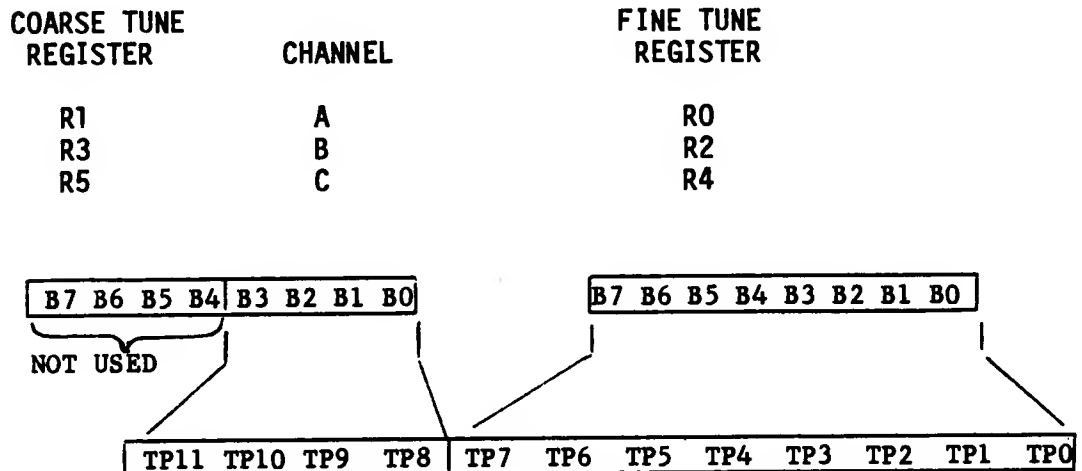
FIGURE 2.1.6-1

PSG REGISTER BLOCK DIAGRAM

REGISTER				B I T							
DEC	HEX	OCT		B7	B6	B5	B4	B3	B2	B1	B0
R0	R0	R0	Channel A	8 Bit Fine Tune							
R1	R1	R1	Tone Period	//////////////////// 4 Bit Coarse Tune							
R2	R2	R2	Channel B	8 Bit Fine Tune							
R3	R3	R3	Tone Period	//////////////////// 4 Bit Coarse Tune							
R4	R4	R4	Channel C	8 Bit Fine Tune							
R5	R5	R5	Tone Period	//////////////////// 4 Bit Coarse Tune							
R6	R6	R6	Noise Period	//////////////////// 5 Bit Period Control							
			Enable	IN/OUT		NOISE		TONE			
R7	R7	R7		IOB	IOA	C	B	A	C	B	A
R8	R8	R10	Ch.A Amplitude	////////////////////			M	L3	L2	L1	L0
R9	R9	R11	Ch.B Amplitude	////////////////////			M	L3	L2	L1	L0
R10	RA	R12	Ch.C Amplitude	////////////////////			M	L3	L2	L1	L0
R11	RB	R13	Envelope	8 Bit Fine Tune E							
R12	RC	R14	Period	8 Bit Coarse Tune E							
			Envelope	////////////////////							
R13	RD	R15	Shape/Cycle	////////////////////				CONT.	ATT.	ALT.	HOLD
R14	RE	R16	I/O Port A Data Store	8 Bit Parallel I/O on Port A							

FIGURE 2.1.6-2

12-BIT TONE PERIOD (TP) TO TONE GENERATOR



### 2.1.6.1 (continued)

The equations describing the relationship between the desired output tone frequency and the input clock frequency and Tone Period value are:

$$(a) \quad fT = \frac{fCLOCK}{16TP_{10}}$$

$$(b) \quad TP_{10} = 256CT_{10} + FT_{10}$$

Where:

- $fT$  = Desired tone frequency
- $fCLOCK$  = Input clock frequency
- $TP_{10}$  = Decimal equivalent of the Tone Period bits TP11 to TP0
- $CT_{10}$  = Decimal equivalent of the Coarse Tune register bits B3 to B0 (TP11 to TP8)
- $FT_{10}$  = Decimal equivalent of the Fine Tune register bits B7 to B0 (TP7 to TP0)

From the above equations, it can be seen that the tone frequency can range from a low of:

$$fCLOCK/65520 \text{ (wherein } TP_{10} = 4095 \text{ )}$$

to a high of:

$$fCLOCK/16 \text{ (wherein } TP_{10} = 1 \text{ ).}$$

The TS 2068 uses a 1.76475 MHz input clock, so it can produce a range of 26.9 Hz to 110 kHz.

### 2.1.6.1 (continued)

To calculate the values for the contents of the Tone Period Coarse and Fine Tune registers, given the input clock and the desired output tone frequencies, we simply rearrange the above equations, yielding:

$$(a) \quad TP_{10} = \frac{fCLOCK}{16 fT}$$

$$(b) \quad CT_{10} + \frac{FT_{10}}{256} = \frac{TP_{10}}{256}$$

Example 1:  $fT = 1 \text{ kHz}$   $fCLOCK = 1.76475 \text{ MHz}$

$$TP_{10} = \frac{1.76475 \times 10^6}{16(1 \times 10^3)} = 110.3$$

Substituting this result into equation (b):

$$CT_{10} + \frac{FT_{10}}{256} = \frac{110.3}{256}$$

resulting in:

$$CT_{10} = 0 = 0000 \text{ (B3-B0)}$$

$$FT_{10} = 110 = 01101110 \text{ (B7-B0)}$$

Example 2:  $fT = 100 \text{ Hz}$   $fCLOCK = 1.76475 \text{ MHz}$

$$TP_{10} = \frac{1.76475 \times 10^6}{16(100)} = 1103$$

Substituting this result into equation (b):

$$CT_{10} + \frac{FT_{10}}{256} = \frac{1103}{256} = 4 + \frac{79}{256}$$

resulting in:

$$CT_{10} = 4 = 0100 \text{ (B3-B0)}$$

$$FT_{10} = 79 = 01001111 \text{ (B7-B0)}$$

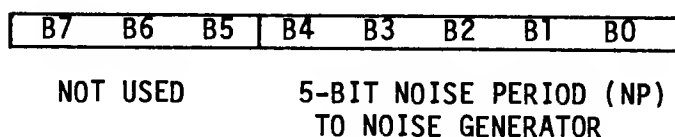


### 2.1.6.2 Noise Generator Control (Register R6)

The frequency of the noise source is obtained in the PSG by first counting down the input clock by 16, then by further counting down the result by the programmed 5-bit Noise Period value. This 5-bit value consists of the lower 5 bits (B4-B0) of Register R6 as illustrated by Figure 2.1.6-3.

FIGURE 2.1.6-3

#### NOISE PERIOD REGISTER R6



Note that the 5-bit value in R6 is a period value -- the higher the value in the register, the lower the resultant noise frequency. Note also that, as with the Tone Period, the lowest period value is 00001 (divide by 1); the highest period value is 11111 (divide by 31<sub>10</sub>).

10

The noise frequency equation is:

$$fN = \frac{fCLOCK}{16 \cdot NP_{10}}$$

Where:

fN	=	Desired noise frequency
fCLOCK	=	Input clock frequency
NP <sub>10</sub>	=	Decimal equivalent of the Noise Period register bits B4-B0.

From the above equation it can be seen that the noise frequency can range from a low of fCLOCK/496 (wherein NP<sub>10</sub> = 31<sub>10</sub>)

to a high of fCLOCK/16 (wherein NP<sub>10</sub> = 1). Using a 1.76475 MHz

clock, for example, would produce a range of noise frequencies from 3.6 kHz to 110.3 kHz.

To calculate the value for the contents of the Noise Period register, given the input clock and the desired output noise frequencies, we simply rearrange the above equation, yielding:

$$NP_{10} = fCLOCK / 16fN$$

### 2.1.6.3 Mixer Control I/O Enable (Register R7)

Register 7 is a multi-function Enable register which controls the three Noise/Tone Mixers and the two general purpose I/O ports.

The Mixers, as previously described, combine the noise and tone frequencies for each of the three channels. The determination of combining neither/either/both noise and tone frequencies on each channel is made by the state of bits B5 thru B0 of R7.

The direction (input or output) of the two general purpose I/O ports (IOA and IOB) is determined by the state of bits B7 and B6 of R7. Note that in the TS 2068 there is no second I/O Port B.

These functions are illustrated by Figure 2.1.6-4 and Tables 2.1.6-1 and 2.1.6-2 below.

FIGURE 2.1.6-4

MIXER CONTROL - I/O ENABLE REGISTER R7

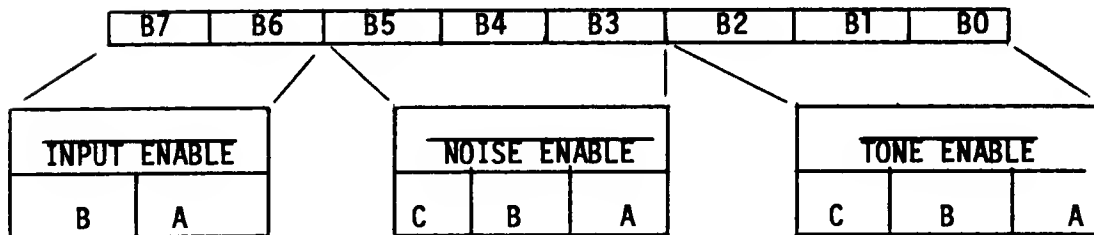


TABLE 2.1.6-1

I/O ENABLE TRUTH TABLE

NOISE ENABLE TRUTH TABLE				TONE ENABLE TRUTH TABLE			
R7 BITS B5 B4 B3			Noise Enabled on Channel	R7 BITS B2 B1 B0			Tone Enabled on Channel
0	0	0	C B A	0	0	0	C B A
0	0	1	C B -	0	0	1	C B -
0	1	0	C - A	0	1	0	C - A
0	1	1	C - -	0	1	1	C - -
1	0	0	- B A	1	0	0	- B A
1	0	1	- B -	1	0	1	- B -
1	1	0	- - A	1	1	0	- - A
1	1	1	- - -	1	1	1	- - -

TABLE 2.1.6-2  
I/O PORT TRUTH TABLE

R7 BITS	I/O Port Status
B6	IOA
0	Input
1	Output

NOTE

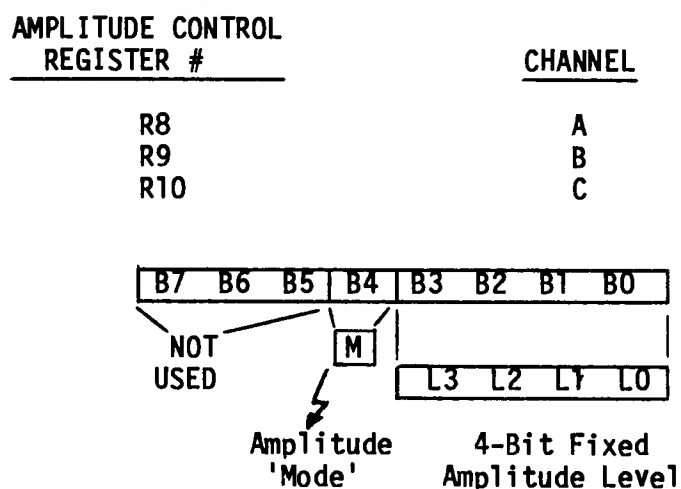
Disabling noise and tone does not turn off a channel. Turning a channel off can only be accomplished by writing all zeroes into the corresponding Amplitude Control register, R8, R9 or R10 (refer to Paragraph 2.1.6.4).

2.1.6.4 Amplitude Control (Registers R8, R9, R10)

The amplitudes of the signals generated by each of the three D/A Converters (one each for Channels A, B, and C) is determined by the contents of the lower 5 bits (B4-B0) of Registers R8, R9 and R10 as illustrated by Figure 2.1.6-5.

FIGURE 2.1.6-5

D/A CONVERTER SIGNAL GENERATION



#### 2.1.6.4 (continued)

The amplitude 'mode' (Bit M) selects either fixed level amplitude (M=0) or variable level amplitude (M=1). It follows then that Bits L3-L0 defining the value of a 'fixed' level amplitude, are only active when M=0. When fixed level amplitude is selected, it is 'fixed' only in the sense that the amplitude level is under the direct control of the system processor (via bits L3-L0). Varying the amplitude when in this 'fixed' amplitude mode requires in each instance the direct intervention of the system processor via an address latch/write data sequence to modify the L3-L0 data.

When M=1 (select 'variable' level amplitudes), the amplitude of each channel is determined by the envelope pattern as defined by the Envelope Generator's 4-bit output E3-E0 (refer to Paragraph 2.1.6.5).

The amplitude 'mode' (Bit M) can be thought of as an 'envelope enable' bit, i.e. when M=0 the envelope is not used, and when M=1 the envelope is enabled.

Figure 2.1.6-6 illustrates all combination of the 5-bit Amplitude Control.

FIGURE 2.1.6-6

#### AMPLITUDE CONTROL REGISTERS

AMPLITUDE CONTROL REGISTER #								CHANNEL			
R8								A			
R9								B			
R10								C			

B7	B6	B5	B4	B3	B2	B1	B0				
NOT USED			M	L3	L2	L1	L0	Amplitude Control Output			

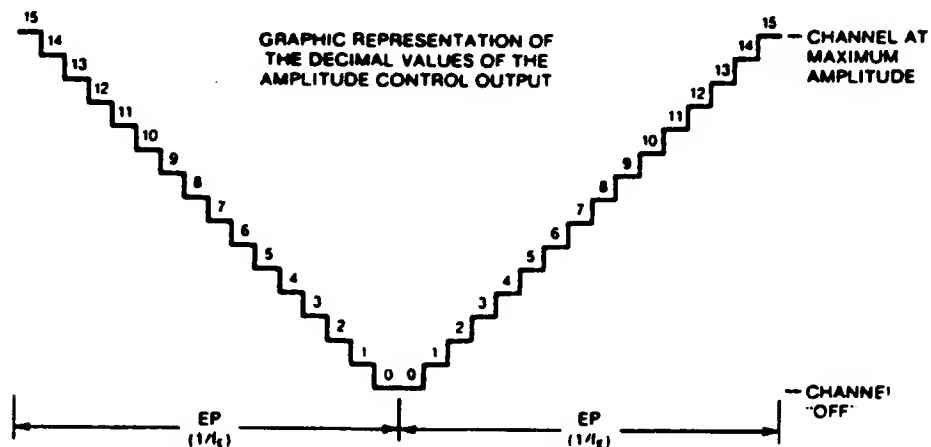
0	0	0	0	0	*0	0	0	0	The amplitude is fixed at 1 of 16 levels as determined by L3-L0.
0	.	.	.	.	.	.	.	.	
0	.	.	.	.	.	.	.	.	
0	.	.	.	.	.	.	.	.	
0	.	.	.	.	.	.	.	.	
0	1	1	1	1	1	1	1	1	
1	X	X	X	X	E3	E2	E1	E0	The amplitude is variable at 16 levels as determined by the output of the Envelope Gen.
(X=Don't Care)									

\*The all zeros code is used to turn a channel "off".

#### 2.1.6.4 (continued)

Figure 2.1.6-7 graphically illustrates a selection of variable level (envelope-controlled) amplitude where the 16 levels directly reflect the output of the Envelope Generator. A fixed level amplitude would correspond to only one of the levels shown, with the level directly determined by the decimal equivalent of Bits L3-L0.

FIGURE 2.1.6-7  
VARIABLE AMPLITUDE CONTROL (M=1)



#### 2.1.6.5 Envelope Generator Control (Registers R11, R12, R13)

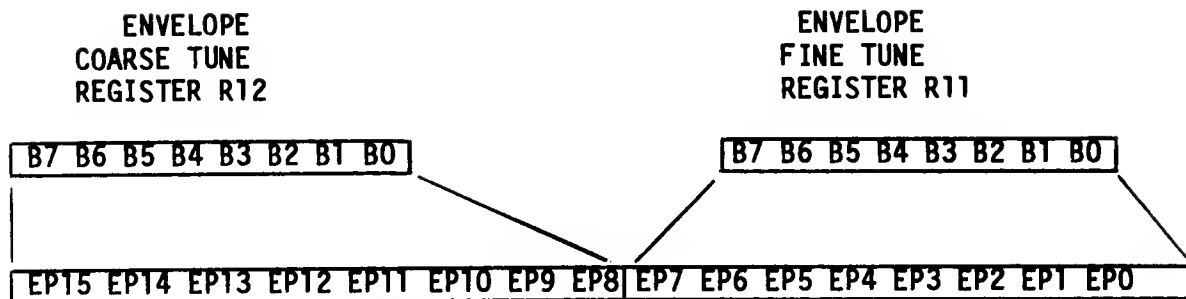
To accomplish the generation of fairly complex envelope patterns, two independent methods of control are provided in the PSG; first, it is possible to vary the frequency of the envelope using registers R11 and R12; and second, the relative shape and cycle pattern of the envelope can be varied using register R13. The following paragraphs explain the details of the envelope control functions, describing first the envelope period control and then the envelope shape/cycle control.

##### 2.1.6.5.1 Envelope Period Control (Registers R11, R12)

The frequency of the envelope is obtained in the PSG by first counting down the input clock by 256, then by further counting down the result by the programmed 16-bit Envelope Period value. This 16-bit value is obtained in the PSG by combining the contents of the Envelope Coarse and Fine Tune registers, as illustrated by Figure 2.1.6-8.

FIGURE 2.1.6-8

16-BIT ENVELOPE PERIOD (EP) TO  
ENVELOPE GENERATOR



Note that the 16-bit value programmed in the combined Coarse and Fine Tune registers is a period value - the higher the value in the registers, the lower the resultant envelope frequency.

Note also that, as with the Tone Period, the lowest period value is 0000000000000001 (divide by 1); the highest period value is 1111111111111111 (divide by 65,535<sub>2</sub><sup>10</sup>).

The envelope frequency equations are:

$$(a) \quad fE = \frac{fCLOCK}{256 \cdot EP_{10}}$$

$$(b) \quad EP_{10} = 256 \cdot CT_{10} + FT_{10}$$

Where:

fE = Desired envelope frequency  
fCLOCK = Input clock frequency  
EP<sub>10</sub> = Decimal equivalent of the Envelope Period bits EP15-EP0  
CT<sub>10</sub> = Decimal equivalent of the Coarse Tune register bits B7-B0 (EP15-EP8)  
FT<sub>10</sub> = Decimal equivalent of the Fine Tune register bits B7-B0 (EP7-EP0)

From the above equation it can be seen that the envelope frequency can range from a low of  $fCLOCK / 16,766,960_{10}$  (wherein  $EP_{10} = 65,535_{10}$ ) to a high of  $fCLOCK / 256_{10}$  (wherein  $EP_{10} = 1_{10}$ ). Using a 1.76475 MHz clock, for example, would produce a range of envelope frequencies from 0.105 Hz to 6893.6 Hz.

To calculate the values for the contents of the Envelope Period Coarse and Fine Tune registers, given the input clock and the desired envelope frequencies, we rearrange the above equations, yielding:

$$(a) \quad \frac{EP}{10} = \frac{fCLOCK}{256fE} \qquad (b) \quad \frac{CT}{10} + \frac{FT}{10} = \frac{EP}{256}$$

Example:

$$\begin{aligned} fE &= 0.5 \text{ Hz} \\ fCLOCK &= 1.76475 \text{ MHz} \end{aligned}$$

$$\frac{EP}{10} = \frac{1.76475 \times 10^6}{256(0.5)} = 13787$$

Substituting this result into equation (b):

$$\frac{CT}{10} + \frac{FT}{10} = \frac{13787}{256} = 53 + \frac{219}{256}$$

$$\frac{CT}{10} = 53 = 00110101_2 \quad (B7-B0)$$

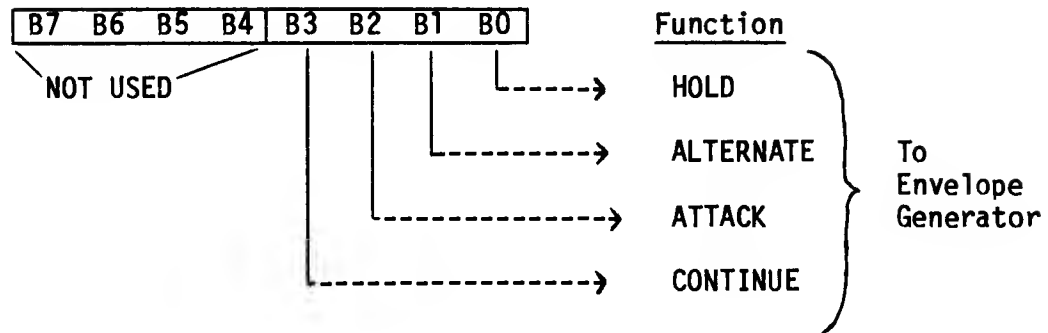
$$\frac{FT}{10} = 219 = 11011011_2 \quad (B7-B0)$$

#### 2.1.6.5.2 Envelope Shape/Cycle Control (Register R13)

The Envelope Generator further counts down the envelope frequency by 16, producing a 16-state per cycle envelope pattern as defined by its 4-bit counter output, E3-E0. The particular shape and cycle pattern of any desired envelope is accomplished by controlling the count pattern (count up/count down) of the 4-bit counter and by defining a single-cycle or repeat-cycle pattern. This envelope shape/cycle control is contained in the lower 4 bits (B3-B0) of register R13. Each of these 4 bits controls a function in the envelope generator, as illustrated in Figure 2.1.6-9.

FIGURE 2.1.6-9

ENVELOPE SHAPE/CYCLE CONTROL REGISTER (R13)



The definition of each function is as follows:

**HOLD** When set to logic "1", limits the envelope to one cycle, holding the last count of the envelope counter (E3-E0 = either 0000 or 1111, depending on whether the envelope counter was in countdown or countup mode respectively).

**ALTERNATE** When set to logic "1", the envelope counter reverses count direction (up-down) after each cycle.

NOTE

When both the Hold bit and the Alternate bit are ones, the envelope counter is reset to its initial count before holding.

**ATTACK** When set to logic "1", the envelope counter will count up (attack) from E3-E0 = 0000 to E3-E0 = 1111; when set to logic "0", the envelope counter will count down (decay) from 1111 to 0000.

**CONTINUE** When set to logic "1", the cycle pattern will be as defined by the Hold bit; when set to logic "0", the envelope generator will reset to 0000 after one cycle and hold at that count.



To further describe the above functions, numerous charts of the binary count sequence of E3-E0 could be used, showing each combination of Hold, Alternate, Attack and Continue. However, since these outputs are used (when selected by the Amplitude Control registers) to amplitude modulate the output of the Mixers, a better understanding of their effect can be accomplished via a graphic representation of their value for each condition selected, as illustrated in Figures 2.1.6-10 and 2.1.6-11.

FIGURE 2.1.6-10  
ENVELOPE GENERATOR OUTPUT

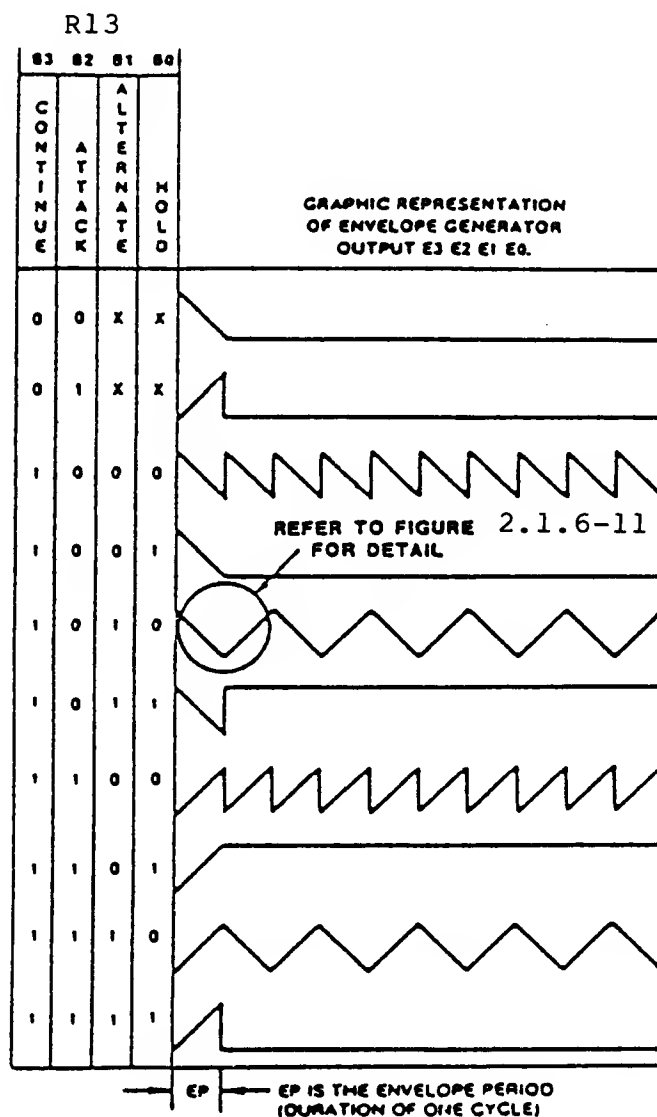
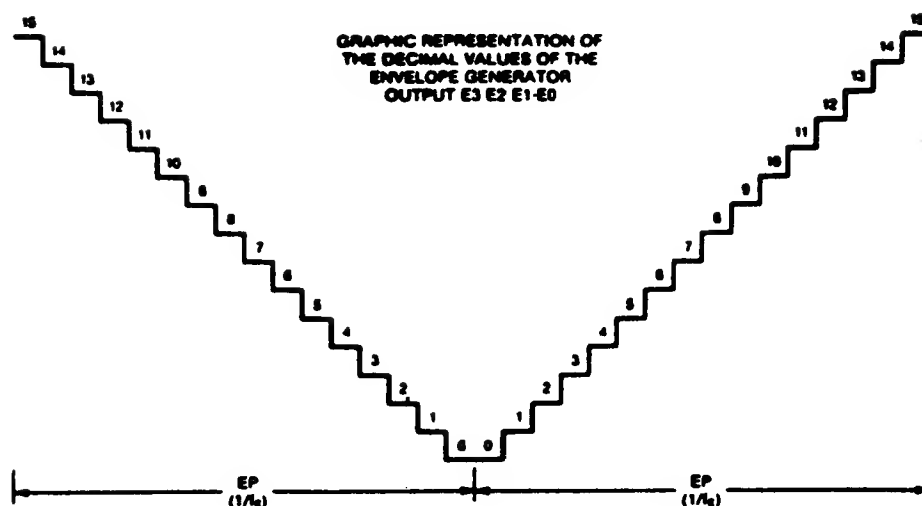


FIGURE 2.1.6-11

DETAIL OF TWO CYCLES OF FIGURE 2.1.6-10



#### 2.1.6.6 I/O Port Data Store (Register R14)

Register R14 functions as an intermediate data storage register between the PSG/CPU data bus (DA7-DA0) and the I/O Port (IOA7-IOA0). This port is available for reading the joysticks. Using register R14 for the transfer of I/O data has no effect at all on sound generation.

To output data from the CPU bus to a peripheral device connected to I/O Port A would require the following steps:

1. Latch address R7 (select Enable register)
2. Write data to PSG (setting R7, B6=1)
3. Latch address R14 (select IOA register)
4. Write data to PSG (data to be output on I/O Port A)

To input data from I/O Port A to the CPU bus would require the following:

1. Latch address R7 (select Enable register)
2. Write data to PSG (setting R7, B6=0)
3. Latch address R14 (select IOA register)
4. Read data from PSG (data from I/O Port A)

Note that once loaded with data in the output mode, the data will remain on the I/O port until changed either by loading different data, by applying a reset (grounding the Reset pin), or by switching to the input mode.

Note also that when in the input mode, the contents of register R14 will follow the signals applied to the I/O port. However, transfer of this data to the CPU bus requires a "read" operation as described above.

### 2.1.7 Joystick Port Operation

The joystick port (Register 14 of the Sound Chip - Section 2.1.6.6) is read via an IN-instruction directed at port F6H with selection of activating data from the left (player 1) or right (player 2) determined by Address bits 8 and 9 as shown in Figure 2.1.7-1. In order to address Register 14, a 0EH must be written to port F5H (Sound Generator Address) prior to reading joystick data. Section 4.4 describes the software sequence necessary to control this hardware.

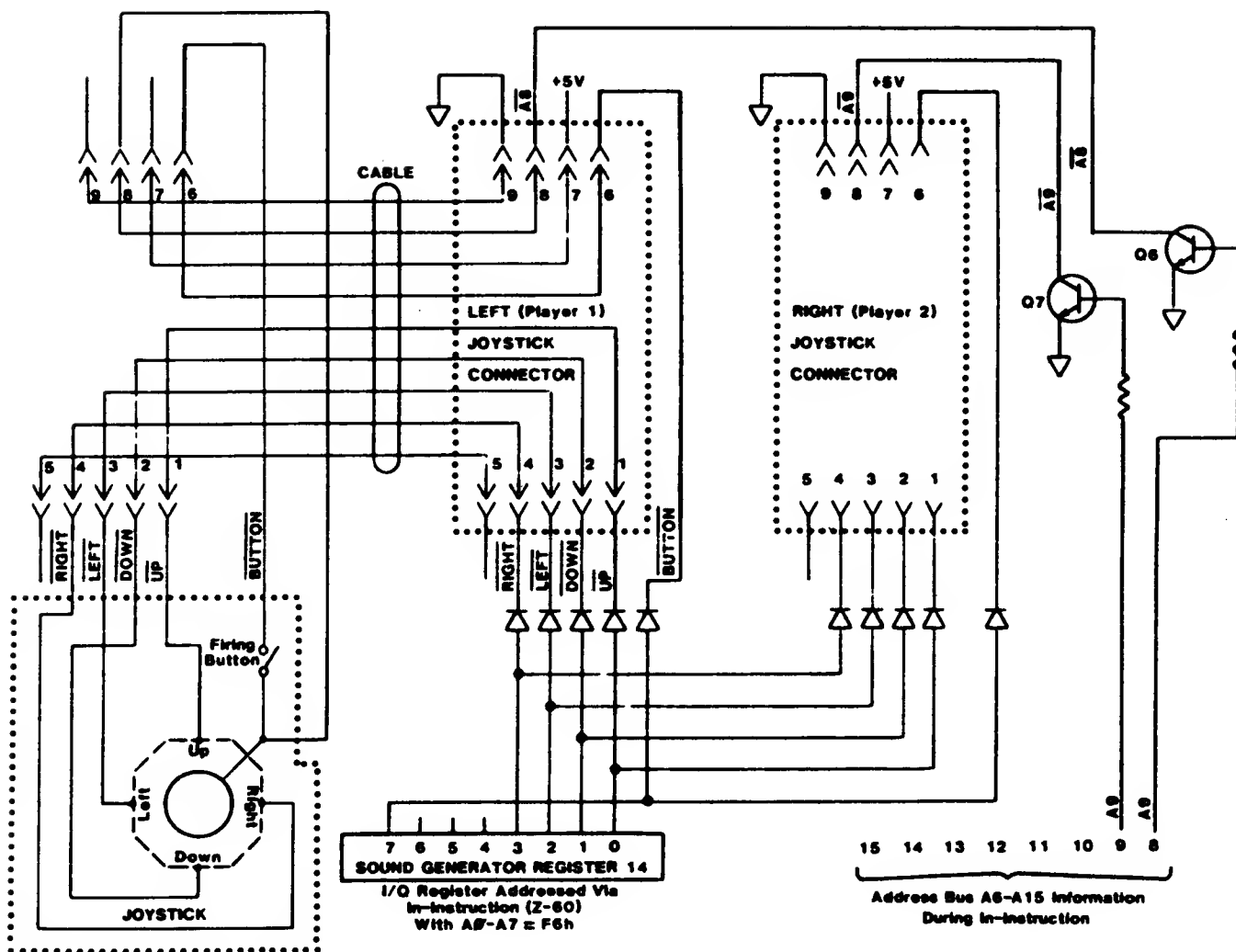
In the example of Figure 2.1.7-1, the joystick, shown schematically in the lower left of the drawing, is composed of a movable center stick which is pushed up to touch the up-contact and, therefore, electronically connects pin-8 to pin-1. In this state, a read of port F6H with address bit A8 high, causes actions as follows:

- (1) Address A8 high turns on transistor Q8
- (2) Q8 drives cable pin-8 low
- (3) The movable center stick of the joystick in contact with the up-contact results in a conductive path from cable pin-8 to cable pin-1.
- (4) Pin-1 low results in a 0 in bit position 0 of the I/O register via the isolation diode.

The various positions of the stick similarly result in various bits being read from the I/O register.

Note that +5 volts and ground are available on the connector so +5V logic could be attached to the joystick port.

FIGURE 2.1.7-1  
JOYSTICK PORT OPERATION



## 2.1.8 Control Logic

The control logic of the TS2068 is primarily a Standard Cell Logic Device in a 68-pin JEDEC leaded carrier package and includes the following major functions:

SECTION	FUNCTION
2.1.8.1	Bank Selection Logic
2.1.8.2	Z-80 Clock Generation
2.1.8.3	Display Timing, DMA Display File Access, Attribute Control, and Pixel Data Serial Shift
2.1.8.4	Interruption Generation
	BEEP Output (See Section 2.1.13.2)
	CASSETTE I/O (See Section 2.1.12).

Additionally, Table 2.1.8-1 provides a description of the function of each SCLD I/O pin. See the System Schematic in Appendix D for pin numbering.

### 2.1.8.1 Bank Selection Logic

The TS2068 is a Z-80 based computer, therefore it can directly address only 64K bytes of memory via its 16-bit address. Additionally, since the Z-80 has no relocation or indirection capability, the conventional technique of extending the memory space available to the Z-80 is bank switching. The TS2068 provides extended bank switching by allowing selection of memory in 8K "chunks" which are identified by bank number and chunk number as illustrated in Figure 2.1.8-1 for the internal bank selection logic. The externally sourced  $\overline{BE}$  (Bank Enable) signal can be used by external logic to disable the internally controlled memories.

As shown in Figure 2.1.8-1:

- (1) The cartridge is selected on a memory access with:
  - a. Port FF bit 7 = 0
  - b. The HSR at port F4h has a "1" in the bit selected by a decode of Address bits A13-A15. and
  - c.  $\overline{BE}$  is highcausing activation of  $\overline{ROSCS}$  (ROS Chip Select).

(2) The EXROM bank is selected on a memory access with:

- a. Port FF bit 7 = 1
- b. The HSR at port F4H has a "1" in the bit selected by a decode of Address bits A13 - A15.
- c.  $\overline{BE}$  is high

causing the activation of  $\overline{EXROM}$  (Ext. ROM Enable)

(3) The Home Bank is selected on a memory access with

- a. The HSR at Port F4H has a "0" in the bit selected by a decode of Address bits A13 - A15.
- b.  $\overline{BE}$  is high.

causing the activation of the appropriate enable signal as detailed below.

To understand the details of the schematic of Section 2.2 (Appendix D):

- (1) SELECT CARTRIDGE of Figure 2.1.8-1 involves activating  $\overline{ROMCS}$  to its low active state
- (2) SELECT EXROM of Figure 2.1.8-1 involves activating  $\overline{EXROM}$  to its low active state
- (3) SELECT HOME BANK of Figure 2.1.8-1 involves
  - a. Activating  $\overline{ROMCS}$  to its low active state when A15=0 and A14=0
  - b. Activating  $\overline{CAS1}$  to its low active state when A15=0 and A14=1
  - c. Activating  $\overline{CAS2}$  to its low active state when A15=1 and A14=0
  - d. Activating  $\overline{CAS3}$  to its low active state when A15=1 and A14=1.

FIGURE 2.1.8-1  
BANK SELECTION LOGIC

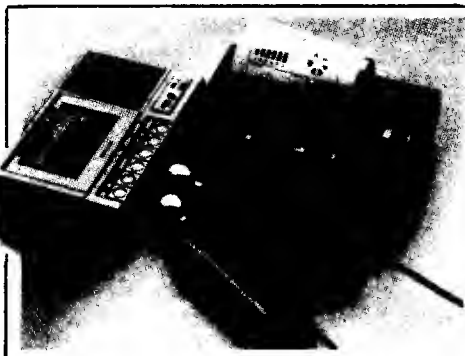
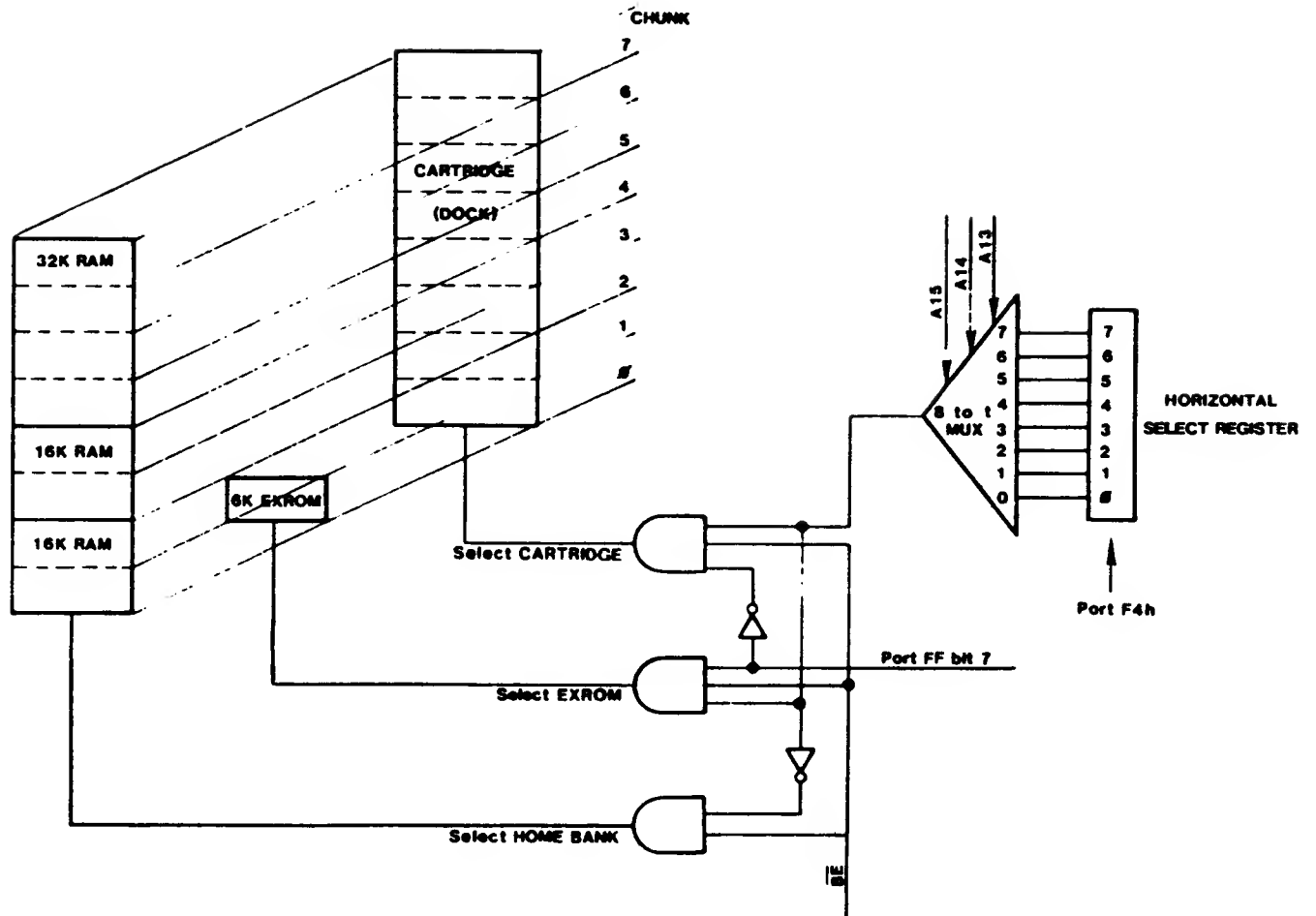


TABLE 2.1.8-1

## SCLD I/O PIN FUNCTION DEFINITIONS

SYMBOL	NAME	DIRECTION OF SCLD IN/OUT	FUNCTION
A0-A7 A13-A15	Address Bus	In	Address Bus lines Input from Z80A
D0-D7	Data Bus	In/Out	Data Bus inputs/outputs from/to Z80A through U9-74LS245 or inputs from display RAM (16K) - U6 and U7
KB0-KB4	Keyboard Outputs	In	Inputs from 5 lines of keyboard matrix - goes low at one of 8 address line (active low) sequences on I/O Request
A7R	A7+Refresh	Out	To refresh and address 8th bit address line input of RAM memory (not display) of 32K of 4416 RAM's (Home Bank 8000H to FFFFH)
MA0-MA7	Muxed Adrs.Bus	Out	Display memory muxed address bus and refresh
TS	Tri-State Display Memory Ctl.	Out	Tri-State control for address and data buffers when CPU is addressing display memory at same time display controller is addressing the display memory
OCPU	Clock to CPU	Out	CLK - Clock to Z80A CPU which is interrupted to stop CPU when CPU wants to address display RAM at same time as display controller
RD <sup>̄</sup>	Read Direction Control to SCLD	Out	To control read/write direction of 74LS245 Data Bus Buffer between CPU and SCLD
ROMCS	Home ROM Chip Select	Out	To activate the 16K Home ROM (first 16K) when memory selection (MS) is set to Home Bank
RAST <sup>̄</sup>	Row Address Strobe #1	Out	To activate row address strobe for display memory only during memory read/write, refresh and display read



TABLE 2.1.8-1

SCLD I/O PIN FUNCTION DEFINITIONS  
(continued)

SYMBOL	NAME	DIRECTION OF SCLD IN/OUT	FUNCTION
<u>CAS1</u>	Column Address Strobe #1	Out	To activate column address strobe for display memory only (2nd 16K) during memory read/write and display read
<u>CAS2</u>	Column Address Strobe #2	Out	To activate column address strobe for Home Bank RAM (3rd 16K)
<u>CAS3</u>	Column Address Strobe #3	Out	To activate column address strobe for Home Bank RAM (4th 16K)
<u>DRAMWE</u>	Dynamic RAM Write Enable	Out	When active low, enables a write into the display RAM only
<u>MUX</u>	Mux Control of RAM Address	Out	Mux control to 74LS157 (U10 & U11 to multiplex the row and column addresses to all dynamic RAM's
<u>V</u>	Chroma Vector V	Out	Color vector level for quadrature (R-Y) input to video modulator
<u>Y</u>	Luminance Y	Out	Luminance (brightness) control level
<u>RD</u>	Read to CPU	In	CPU is reading from a memory or I/O location
<u>WR</u>	Write from CPU	In	CPU is writing to a memory or I/O location
<u>MREQ</u>	Memory Request	In	CPU is requesting access to a memory location to read or write
<u>IORQ</u>	I/O Request	In	CPU is requesting access to an I/O location to read or write

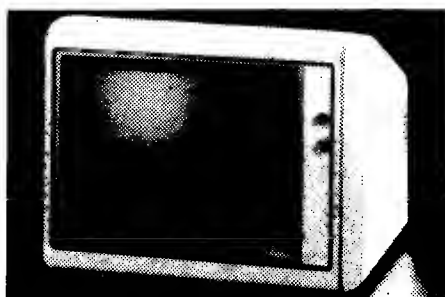
TABLE 2.1.8-1  
SCLD I/O PIN FUNCTION DEFINITIONS  
(continued)

SYMBOL	NAME	DIRECTION OF SCLD <u>IN/OUT</u>	FUNCTION
<del>RFSH</del>	Refresh	In	CPU is generating a refresh address to refresh dynamic RAM's
Tape In	Tape Input	In	Magnetic tape signal input
<del>BE</del>	Bank Enable	In	When active low, indicates that internal memory is disabled (Home, Extension and Dock Banks) and an external memory is in use
<del>EXROM</del>	Extension ROM Select	Out	Active low chip select signal for Extension ROM
VCC	+5 Volt Power	In	Power (+5V) input to SCLD
INT	Interrupt to CPU	Out	Interrupts CPU to handle keyboard strobing and timer for PAUSE command. Open drain N channel with internal pull-up
<del>ROSCS</del>	ROS Chip Select	Out	ROM-Oriented Software (Cartridge Bank) Chip Select
SPKR/TAPE OUT	Speaker and Tape Output	Out	Digital output to magnetic tape and to sound amplifier for speaker output
OC	Clock "C"	Out	Clock for sound chip @1.764 MHz.
BDIR	Bus Direction to Sound Chip	Out	A bus direction control signal to the PSG. When high the sound chip either receives a write to PSG or latches addresses from the data bus
BC1	Bus Control to Sound Chip	Out	A bus control signal to the PSG. When high the sound chip either is read to data bus or latches addresses from the data bus

TABLE 2.1.8-1

SCLD I/O PIN FUNCTION DEFINITIONS  
(continued)

SYMBOL	NAME	DIRECTION OF SCLD IN/OUT	FUNCTION
OSC Out	Oscillator Out	Out	Xtal Oscillator amplifier output to drive crystal
OSC In	Oscillator In	In	Xtal Oscillator amplifier input to sense crystal signal
U	Chroma Vector U	Out	Color vector level for quadrature (B-Y) input to video modulator
GND	Ground	In	Ground return of SCLD
$\bar{O}$	Buffered Clock	Out	Buffered CPU clock to outside (J1 - connector)
R	Red Color Output	Out	Produce color signals to RGB monitor (TTL level)
G	Green Color Output	Out	Produce color signals to RGB monitor (TTL level)
B	Blue Color Output	Out	Produce color signals to RGB monitor (TTL level)



### 2.1.8.2 Z-80 Clock Generation

The oscillator circuit utilizes an AT-cut quartz crystal at 14.112 MHz. This oscillator feeds a divide by 4 chain to generate the 3.528 MHz clock for the CPU (0 CPU). This clock runs continuously except when the CPU addresses the 16K bytes of RAM containing the video display file at the same time the video display processor logic requires access to that same RAM. For this contention case the CPU clock is stopped in the high state until the video display processor access has been completed, then the CPU clock continues in its normal manner.

### 2.1.8.3 Display File H/W Control and Timing

The 14.112 MHz oscillator is also used to drive the counter chain deriving video timing. By dividing the 14.112 MHz. signal by 896 a 15.75 KHz horizontal sweep frequency is generated. The 15.75 KHz signal feeds a 9-stage counter which counts from 0 to 106H (262 decimal) developing the 60.1145 Hz vertical sync. See Figure 2.1.8-2.

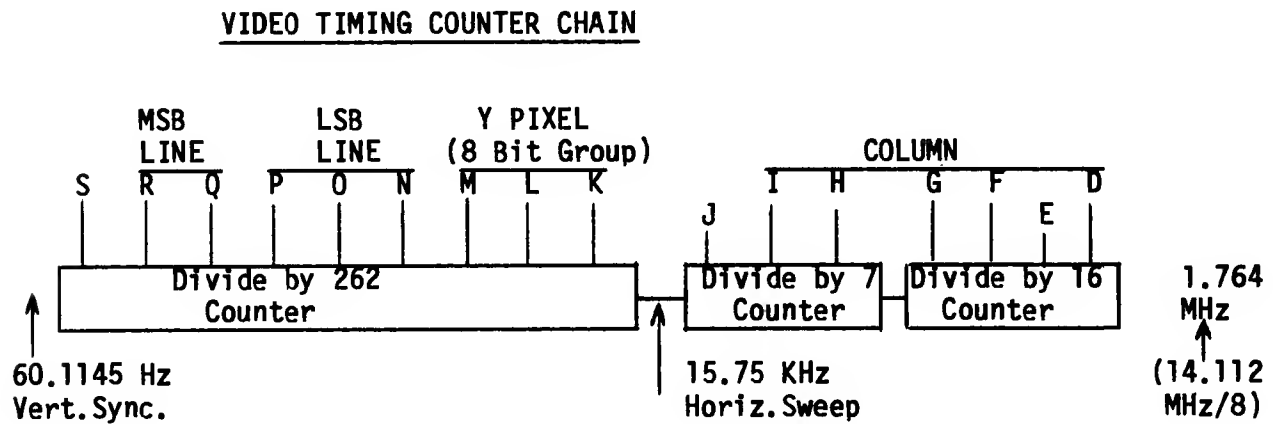
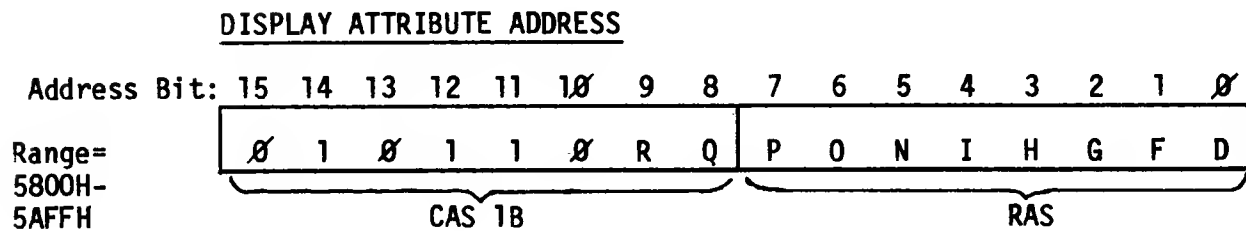
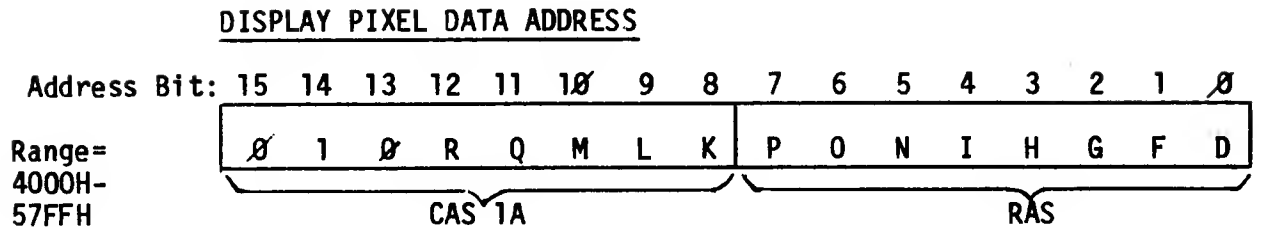
During each horizontal scan the video display processor accesses, in the standard video mode, 32 bytes of pixel data plus 32 bytes of attributes by 32 memory accesses reading 2 bytes per access in RAM page mode, i.e. the low order address bits are provided to the RAM once via RAS activation, then the data byte is read during the first activation of CAS and the attribute byte is read during the second activation of CAS. The page mode operation is completed by deactivating RAS. (See Fig. 2.1.8-2.)

The accessed pixel data is serially shifted out to the video generation circuitry at a rate of 1 bit each 142 nanoseconds (7.056 MHz) resulting in the need to fetch a new data/attribute pair each 1.134 microseconds during the horizontal scan time. The shifted out pixel information is used to control the selection of the 3 paper color (pixel=0) or 3 ink color (pixel=1) bits to be gated out as the R, G, and B signals. When FLASH is enabled by the attribute byte, the INK and PAPER field information is swapped at the 1.879 Hz. flash rate. The R, G, and B signals control the D-to-A converter which generates the proper U, V, and  $\overline{Y}$  outputs for use by the 1889 to create composite video.

The address information provided to the RAM's during RAS and CAS times is as shown in Figure 2.1.8-2. This address generation logic explains the non-sequential nature of the video display as described in Section 2.1.10.

FIGURE 2.1.8-2

VIDEO DISPLAY PROCESSOR RAM ADDRESS GENERATION  
(Normal Video Mode)



#### 2.1.8.4 Interruption Generation (17 ms)

During the vertical blanking interval (once each 15.635 ms) the SCLD, if enabled by the INTEN bit (Bit 6) of I/O Port FFH, activates the INT signal which directly connects to the INT input to the Z80. A CPU maskable interruption can then occur, as described in Section 2.1.3.7, if enabled.

#### 2.1.9 Keyboard

The keyboard for the TS 2068 has forty-two (42) hard keys (typewriter style) with tactile feel utilizing an over-dead-center type of rubber spring pad and a carbon pill that hits the P.C. board, just under the keyboard, to short-out a pair of closely placed precious metal contacts. The read-out matrix is an eight by five cross point switching as shown in Figure 2.1.9-1.

Each switch closure connects one of the eight high order address lines (by going low through a diode) to one of the five input lines to the SCLD (KB0 through KB4).

Scanning is by software algorithm as described in Section 4.1.1. During the IN instruction, address bits A0-A7=FEH select the Keyboard I/O port while bits A8-A15 select the particular 5 keys to be sampled during the particular IN instruction execution. For example, an IN instruction directed at the keyboard I/O port with address bit A8 low and A9-A15 high will supply 0's on KB0, KB1, KB2, KB3, and/or KB4 if the CAP SHIFT, Z, X, C, and/or V keys are respectively depressed.

Note that when reading the I/O port FEH, data bits D5-D7 are not part of the keyboard information.

Section 2.4.7 details the connection of the keyboard to the main P.C. board.

#### 2.1.10 16K Video Display RAM

The 16K-byte video display RAM, composed of two 4416's, is isolated from the Z80A CPU by the SCLD control logic and buffers to allow the video display processor to access pixel and attribute data from the display files independent of the CPU (see Section 2.1.8.3).

The Video Display RAM is located in Chunks 2 and 3 of the Home Bank, beginning at 4000H and 6000H respectively. Figure 2.1.10-1 illustrates the organization of the Primary Display File located at 4000H. The second display file utilizes the same organization. Based on the video mode set via Port FFH, the video hardware accesses the RAM for pixel data and attribute control information.

Figure 2.1.9. KEYBOARD SCHEMATIC

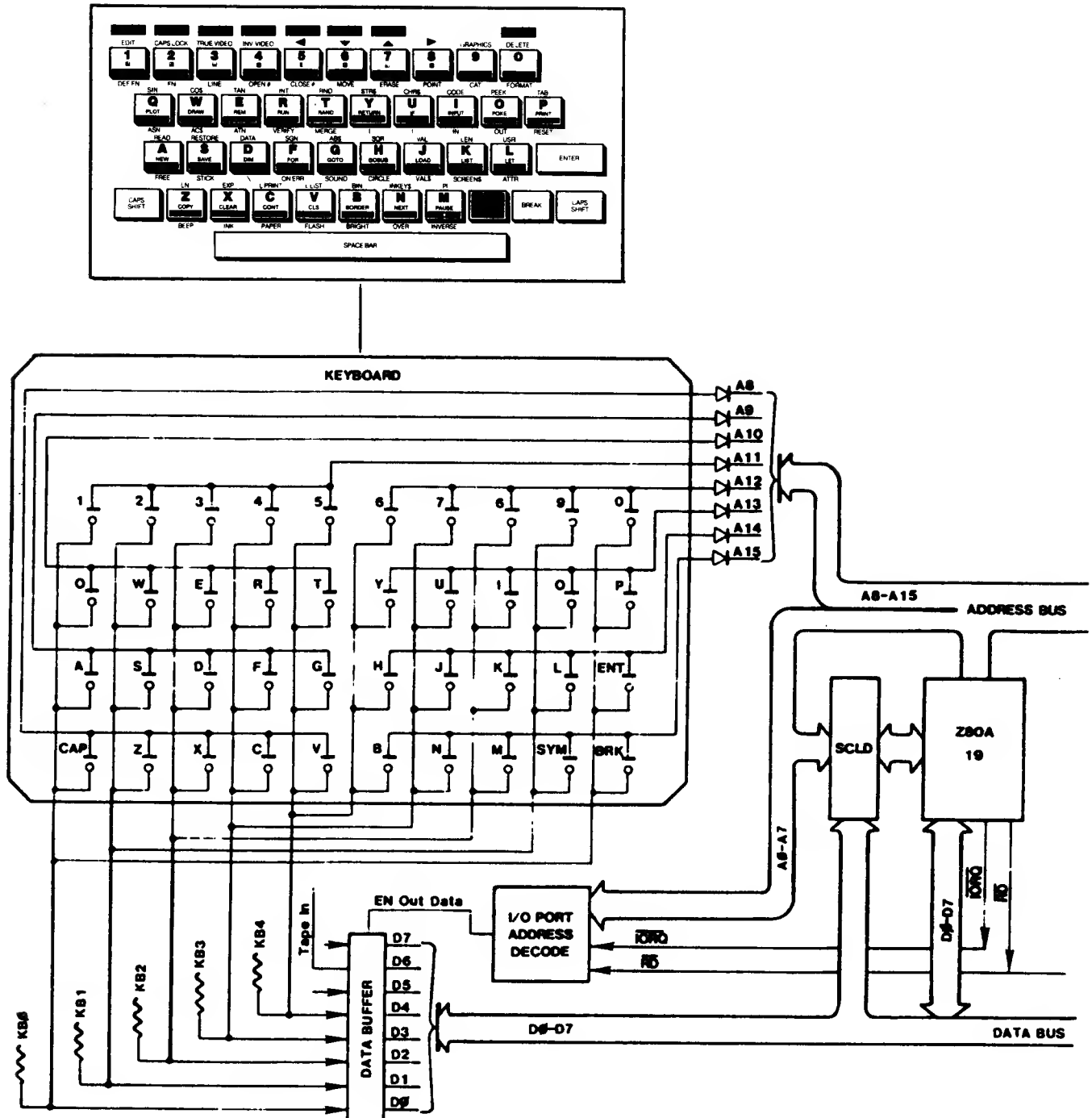


FIG. 2.1.10-1  
DISPLAY FILE ORGANIZATION (NORMAL MODE)

		32 BYTES			32 BYTES			32 BYTES		
		LINE 0			LINE 1			LINE 7		
BLOCK 0	Scan 0	4000	4001.....401F	4020.....403F	.....	40E0	40E1	.....40FF		
	1	4100	4101.....411F	4120.....413F	.....	41E0	41E1	.....41FF		
	2	4200	4201.....421F	4220.....423F	.....	42E0	42E1	.....42FF		
	3	4300	4301.....431F	4320.....433F	.....	43E0	43E1	.....43FF		
	4	4400	4401.....441F	4420.....443F	.....	44E0	44E1	.....44FF		
	5	4500	4501.....451F	4520.....453F	.....	45E0	45E1	.....45FF		
	6	4600	4601.....461F	4620.....463F	.....	46E0	46E1	.....46FF		
	7	4700	4701.....471F	4720.....473F	.....	47E0	47E1	.....47FF		
		CHAR.	CHAR.	CHAR.		CHAR.	CHAR.	CHAR.		
		POS.	POS.	POS.		POS.	POS.	POS.		
		0/0	0/1	0/31		7/0	7/1	7/31		

		32 BYTES			32 BYTES			32 BYTES		
		LINE 8			LINE 9			LINE 15		
BLOCK 1	Scan 0	4800	4801.....481F	4820.....483F	.....	48E0	48E1	.....48FF		
	1	4900	4901.....491F	4920.....493F	.....	49E0	49E1	.....49FF		
	2	4A00	4A01.....4A1F	4A20.....4A3F	.....	4AE0	4AE1	.....4AFF		
	3	4B00	4B01.....4B1F	4B20.....4B3F	.....	4BE0	4BE1	.....4BFF		
	4	4C00	4C01.....4C1F	4C20.....4C3F	.....	4CE0	4CE1	.....4CFF		
	5	4D00	4D01.....4D1F	4D20.....4D3F	.....	4DE0	4DE1	.....4DFF		
	6	4E00	4E01.....4E1F	4E20.....4E3F	.....	4EE0	4EE1	.....4EFF		
	7	4F00	4F01.....4F1F	4F20.....4F3F	.....	4FE0	4FE1	.....4FFF		
		CHAR.	CHAR.	CHAR.		CHAR.	CHAR.	CHAR.		
		POS.	POS.	POS.		POS.	POS.	POS.		
		8/0	8/1	8/31		15/0	15/1	15/31		

		32 BYTES			32 BYTES			32 BYTES		
		LINE 16			LINE 17			LINE 23		
BLOCK 2	Scan 0	5000	5001.....501F	5020.....503F	.....	50E0	50E1	.....50FF		
	1	5100	5101.....511F	5120.....513F	.....	51E0	51E1	.....51FF		
	2	5200	5201.....521F	5220.....523F	.....	52E0	52E1	.....52FF		
	3	5300	5301.....531F	5320.....533F	.....	53E0	53E1	.....53FF		
	4	5400	5401.....541F	5420.....543F	.....	54E0	54E1	.....54FF		
	5	5500	5501.....551F	5520.....553F	.....	55E0	55E1	.....55FF		
	6	5600	5601.....561F	5620.....563F	.....	56E0	56E1	.....56FF		
	7	5700	5701.....571F	5720.....573F	.....	57E0	57E1	.....57FF		
		CHAR.	CHAR.	CHAR.		CHAR.	CHAR.	CHAR.		
		POS.	POS.	POS.		POS.	POS.	POS.		
		16/0	16/1	16/31		23/0	23/1	23/31		

ATTRIBUTE FILE:

BLOCK 0	LINE 0	LINE 1	LINES 2 - 6	LINE 7
	5800.....581F	5820.....583F	5840.....58DF	58E0.....58FF
BLOCK 1	LINE 8	LINE 9	LINES 10-14	LINE 15
	5900.....591F	5920.....593F	5940.....59DF	59E0.....59FF
BLOCK 2	LINE 16	LINE 17	LINES 18-22	LINE 23
	5A00.....5A1F	5A20.....5A3F	5A40.....5ADF	5AE0.....5AFF



## 2.1.11 Video Generation

### 2.1.11.1 Composite Video

The U, V, and  $\overline{Y}$  signals from the SCLD are supplied to the LM1889 and associated circuitry to produce composite video and modulated RF. This circuitry produces color vectors at approximately the following angles:

PHASE	TS 2068 (Degrees)	NTSC STANDARD (Degrees)
Blue	350	350
Magenta	64	62
Red	116	112
Green	242	240
Cyan	284	284
Yellow	170	170
Reference	224	180

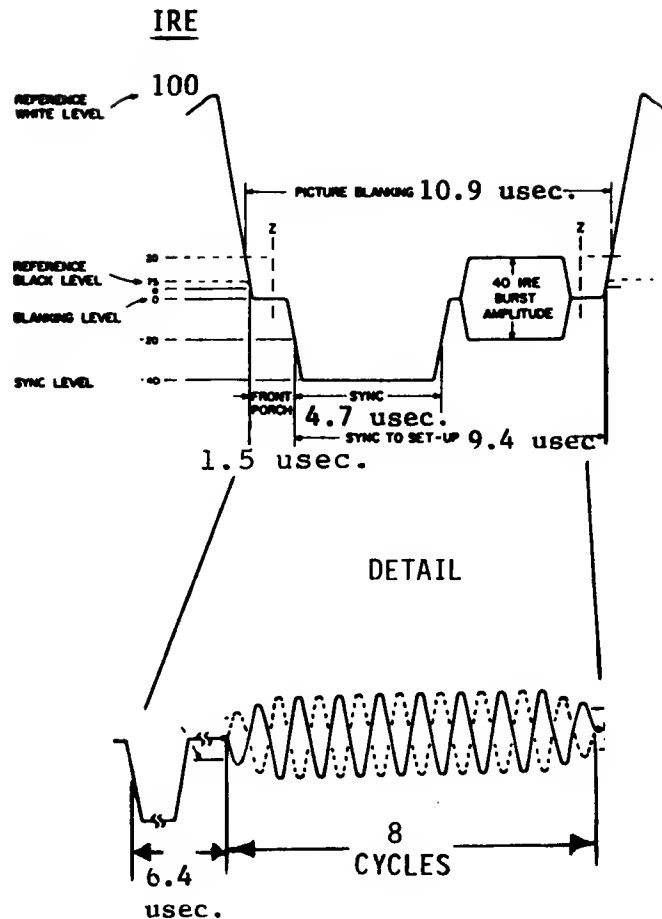
The Front Porch, Sync Pulse, Back Porch, and Color Burst portions of the composite video signal are illustrated in Figure 2.1.11-1. In proper adjustment the following should be observed:

Sync Pulse = 40 +/- 2 IRE units  
Color Burst = 35 to 45 IRE units  
Color Burst Freq. = 3.579545 MHz. +/- 70 Hz

The following three facts may aid in understanding problems with certain monitors.

1. The color burst is not synchronous with the waveform since it is generated from the 3.579545 MHz crystal and the waveform is derived from the 14.112 MHz crystal. The result is observed ripples at color boundaries, e.g. green to magenta.
2. The color burst duration is 8 cycles while standard TV broadcast stations provide 9 cycles. This "short" burst is a problem for some monitors.
3. The color burst starts 6.4 microseconds from the leading edge of sync. Many monitors are designed to expect this start as early as 5.3 microseconds, thus these monitors may not produce color when attached to the TS 2068.

FIGURE 2.1.11-1  
COMPOSITE VIDEO SIGNALS



#### 2.1.11.2 RF Modulator

The composite video information is used to AM modulate the selected channel frequency via the LM1889 and associated Channel 2/3 tank circuitry. The modulated output is filtered through the output filter network to reduce harmonic generation to comply with FCC requirements. The RF circuitry is physically contained inside the RF-can at the rear left corner of the PCB (at the RF output jack). 75 ohms is the output impedance.

## 2.1.12 Cassette I/O

See Sections 2.1.13.2, 2.4.3 and 4.2.

## 2.1.13 Port Map

Table 2.1.13-1 summarizes the I/O addressing of ports utilized by the TS 2068. Details of the data bits of each of these ports is provided by the following sections.

### 2.1.13.1 Display Enhancement Control (Port FFH)

The display enhancement control register within the SCLD controls:

- a) Selection of Enhanced Video Modes
- b) Ink selection for 64-Column Mode
- c) Enable/Inhibit the 17 ms interruption to the Z80
- d) Selection of Extension ROM or Cartridge (see Section 2.1.8.1)

D7	D6	D5	D4	D3	D2	D1	D0
		64-Column Mode <u>Ink/Paper Selection</u>			Video Mode <u>Selection</u>		
		000	-	Black/White	000	-	Normal (Primary
		001	-	Blue/Yellow			Display File)
		010	-	Red/Cyan	001	-	Second Display File
		011	-	Magenta/Green	010	-	High Res. Graphics
		100	-	Green/Magenta	110	-	64-Column Mode
		101	-	Cyan/Red	Other combinations may produce unpredictable results.		
		110	-	Yellow/Blue			
		111	-	White/Black			
		Inhibit 17 ms Interruption (0 to Enable)					
		EXROM/Cartridge Select (See 2.1.8.1)					

TABLE 2.1.13-1

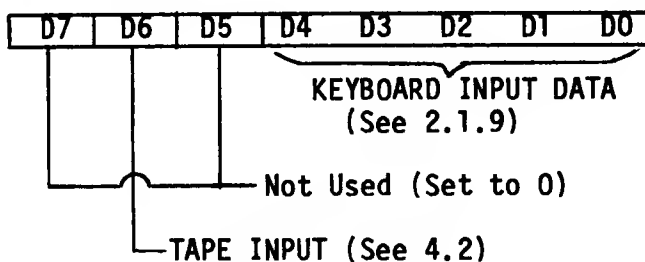
## I/O PORT MAP

FUNCTION	PORT ADDRESS			OPERATION	REFERENCE
	(HEX)	(DECIMAL)	(BINARY)		
Display Enhancement Control	FF	255	11111111	R/W	2.1.10, 2.1.13.1, 3.2.2.3, 5.2
Keyboard/Tape I/O	FE	254	11111110	R/W	2.1.9, 2.1.13.2, 2.4.3, 4.1.1, 4.2
Reserved	FD	253	11111101		
Reserved	FC	252	11111100		
TS 2040 Printer	FB	251	11111011	R/W	2.1.13.3, 4.1.3
Sound Chip & Joystick Data	F6	246	11110110	R/W	2.1.6, 2.1.7, 2.1.13.4, 2.4.4, 4.3, 4.5
Sound Chip Address	F5	245	11110101	W	Same
Horizontal Select Register	F4	244	11110100	R/W	2.1.8.1

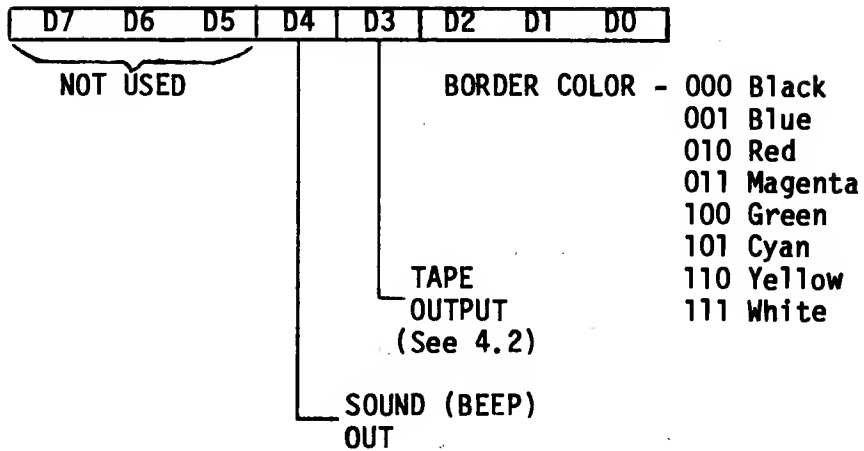
## 2.1.13.2 Keyboard/Tape I/O (Port FEH)

Port FEH is used to input Keyboard and Tape data and to output Border color, Tape data, and Sound (BEEP) tones.

READ (IN)



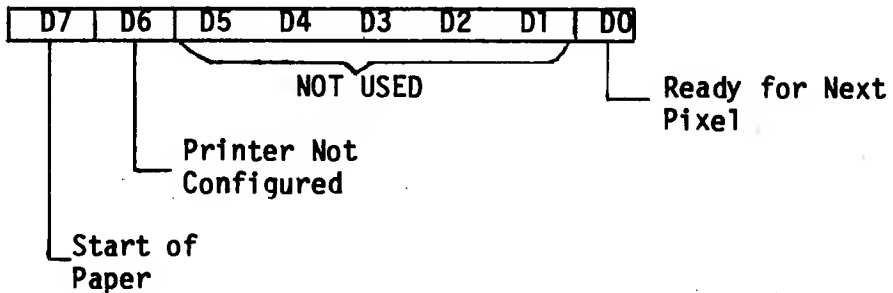
# WRITE (OUT)



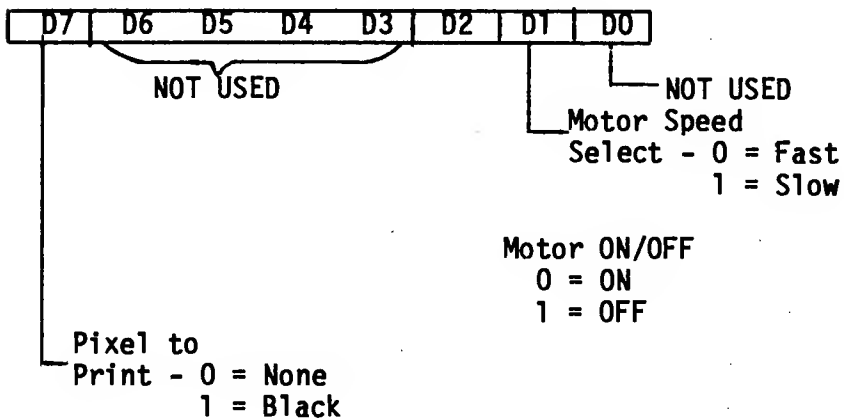
## 2.1.13.3 TS 2040 Printer (Port 1XXXX0XX)

The TS 2040 Printer peripheral is written to and status read from via OUT and IN instructions with Bit 7 = 1 and Bit 2 = 0 (other bits are not decoded by the printer).

# READ (IN)



# WRITE (OUT)



#### 2.1.13.4 Sound Chip & Joystick (Ports F5H and F6H)

Ports F5H and F6H are used to control and access the Sound Generator and the Joysticks. Details of the registers available via these ports is contained in Sections 2.1.6 and 2.1.7.

#### 2.1.13.5 Horizontal Select Register (Port F4H)

The HSR addressed via Port F4H is used in the control of the Bank Switching logic as detailed in Section 2.1.8. Each bit, when set, enables the corresponding 8K memory "chunk" in either the Dock Bank (Port FF, Bit 7=0) or the Extension ROM Bank (Port FF, Bit 7=1). The HSR must be set to all zeroes in order to enable the entire Home Bank.

### 2.2 Schematic Diagram

Appendix D contains a detailed schematic diagram of the TS 2068.

### 2.3 Unit Absolute Ratings

FUNCTION	DESCRIPTION	MIN	MAX
TS	Storage Temperature	-40°C	+65°C
VAC	AC Line Voltage	105V	130V
Ta	Operating Ambient Temp	0°C	40°C
Vin	Voltage on any Logic Pin	-0.3V	+5.3V
Vin (EAR)	EAR input Peak AC	-2.0V	+5.0V
Vdc (IN)	Input DC Voltage	14.75V	26V

### 2.4 Interfaces and Connectors

The TS2068 has a number of specialized interfaces that are accessible via the following connectors:

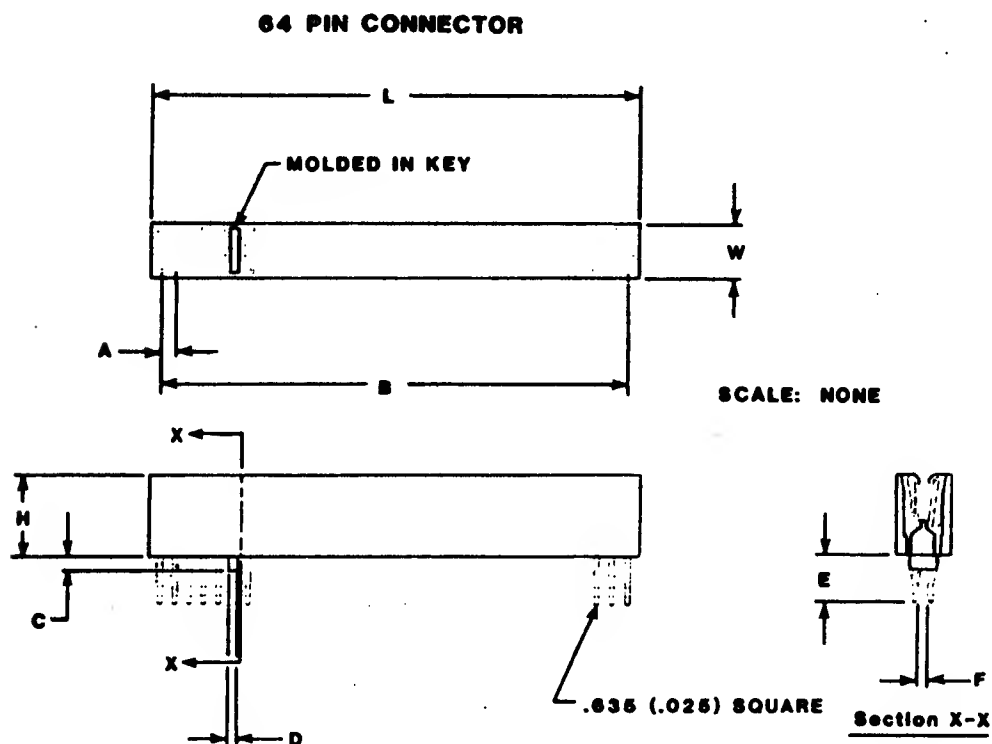
CONNECTOR	TYPE	LOCATION
System Bus	2X32 Card Edge	Right Rear
Cartridge	2X18 Card Edge	Under TCC door
MIC	1/8" Mini Phone	Rear
EAR	1/8" Mini Phone	Rear
Player 1 Joystick	9-pin "D"	Left Side
Player 2 Joystick	9-pin "D"	Right Side
Monitor	RCA Phono	Rear
TV	RCA Phono	Rear
Keyboard	14-pin SIP	Inside-Left Rear
AC Adapter		Rear

### 2.4.1 System Bus Connector - P1

The TS2068 provides a 2 X 32 pin connector, which is designated as P1, at the right rear corner of the console. The mechanical, functional, and electrical requirements of the system buss connector are detailed in the following tables and figures:

FIGURE/TABLE	TITLE
Figure 2.4.1-1 P1	Mating Connector Mechanical Requirements
Figure 2.4.1-2 P1	Signal Layout
Table 2.4.1 - 1 P1	Signal Definition
Table 2.4.1 - 2 P1	Signal Electrical Characteristics

FIGURE 2.4.1-1  
P1 MATING CONNECTOR MECHANICAL REQUIREMENTS



LTR	DIMENSION *
L	82.55 (3.25)
W	8.826 ± 0.127 (3.78 ± .006)
H	13.87 ± 0.254 (.550 ± .010)
A	2.54 (.100)
B	31 EQUAL SPACES AT 2.54 (.100) = 78.74 (3.100)
C	2.54 (.10)
D	1.727 (.088) MAX
E	8.382 ± 0.305 (.330 ± .020)
F	FOR 1.578 (.082) BOARD

1. **INSULATOR MATERIAL:** Insulator body shall be 30% glass-filled polyester and shall meet UL94V-0 requirements.
2. **CONTACT MATERIAL:** Contact material shall be phosphor bronze.
3. **CONTACT FINISH:** Contacts shall be selectively plated with gold, 0.00038 (.000015) thick over nickel on contact surfaces.
4. **INSERTION FORCE:** Insertion forces shall be 170.1-263.5 grams (6-10 oz) per contact pair using a 1.675 (.062) flat steel test blade.
5. **WITHDRAWAL FORCE:** Withdrawal forces shall be 226.6-340.2 grams (8-12 oz) per contact pair using a 1.875 (.062) flat test blade.
6. **NORMAL FORCE:** Normal force shall be 85.05 grams (3 oz) minimum when mated with a 1.37 (.064) thick test board.
7. **PURCHASE FROM:** San Diego Microtronics INC. San Diego, CA 92123.

FIGURE 2.4.1-2

P1 CONNECTOR SIGNAL LAYOUT





TABLE 2.4.1 - 1

## P1 SIGNAL DEFINITION

PIN #	SIGNAL NAME	DESCRIPTION
1A	GND	Signal Ground
1B	GND	Signal Ground
2A	EAR	EAR Input
2B	SPKR/TAPE OUT	Speaker/Tape Output
3A	A7RB	Refresh Address Bit 7 Buffered
3B	+15V	+15 Volts DC
4A	D7	Data Bus Bit 7
4B	+5V	+5 Volts
5A	DZIN	Daisy In (Not Connected)
5B	Not Used	--
6A	Slot	--
6B	Slot	--
7A	D0	Data Bus Bit 0
7B	GND	Power Ground
8A	D1	Data Bus Bit 1
8B	GND	Power Ground
9A	D2	Data Bus Bit 2
9B	0	CPU Clock (Inverted)
10A	D6	Data Bus Bit 6
10B	A0	Address Bus Bit 0
11A	D5	Data Bus Bit 5
11B	A1	Address Bus Bit 1
12A	D3	Data Bus Bit 3
12B	A2	Address Bus Bit 2
13A	D4	Data Bus Bit 4
13B	A3	Address Bus Bit 3
14A	INT	Interrupt Request (Active Low)
14B	A15B	Address Bus Bit 15, Buffered
15A	NMI	Non-Maskable Int.(Active Low)
15B	A14B	Address Bus Bit 14, Buffered
16A	HALT	CPU HALT Indicator (Active Low)
16B	A13B	Address Bus Bit 13, Buffered
17A	MREQB	Memory Request (Active Low), Bfrd.
17B	A12	Address Bus Bit 12
18A	IORQB	I/O Request (Active Low), Bfrd.
18B	A11	Address Bus Bit 11
19A	RDB	Read (Active Low), Buffered
19B	A10	Address Bus Bit 10
20A	WFB	Write (Active Low), Buffered
20B	A9	Address Bus Bit 9
21A	BUSAK	Bus Acknowledge (Active Low)
21B	A8	Address Bus Bit 8
22A	WAIT	CPU WAIT (Active Low)
22B	A7	Address Bus Bit 7
23A	BUSRQ	Bus Request (Active Low)
23B	A6	Address Bus Bit 6
24A	RESET	CPU Reset (Active Low)
24B	A5	Address Bus Bit 5
25A	M1	CPU M1 State (Active Low)
25B	A4	Address Bus Bit 4
26A	RFSHB	Refresh (Active Low), Buffered
26B	DZOUT	Daisy Out (Not Connected)
27A	EXROM	Extension ROM Enable (Active Low)
27B	R	Color Signal - Red
28A	ROSCS	ROS Chip Select (Active Low) (Dock Bank Enable)
28B	G	Color Signal - Green
29A	BE	Bank Enable (Active Low)
29B	B	Color Signal - Blue
30A	IOA5	
30B	BUSISO	
31A	SOUND	Analog Sound Signal Output(0-5V)
31B	VIDEO	Composite Video Signal Output
32A	GND	Signal Ground
32B	GND	Signal Ground

NOTE: All A Pins are on component side of board  
 All B Pins are on non-component (soldering) side of board

TABLE -2.4.1-2

## P1 SIGNAL ELECTRICAL CHARACTERISTICS

MNEMONIC	----- OUTPUTS FROM TS2068 -----				----- INPUTS TO TS2068 -----			
	CAPACITIVE LOADING MAX (PF)	V(OL) MAX VOLTS	I(LOAO) MAX (MA)	V(OH) MIN VOLTS	V(IL) MAX VOLTS	V(IH) MIN VOLTS	I IN (MAX) $\mu$ A	INPUT CAPACITIVE LOADING MAX (PF)
A158	30	0.5	1.8	2.4	0.8	2.0	1800	40
A148	30	0.5	1.8	2.4	0.8	2.0	1800	40
A138	30	0.5	1.8	2.4	0.8	2.0	1800	40
A12	30	0.4	1.8	2.4	0.8	2.0	1800	74
A11	30	0.4	1.8	2.4	0.8	2.0	1800	74
A10	30	0.4	1.8	2.4	0.8	2.0	1800	74
A9	30	0.4	1.8	2.4	0.8	2.0	1800	76
A8	30	0.4	1.8	2.4	0.8	2.0	1800	76
A7	30	0.4	1.8	2.4	0.8	2.0	1800	72
A6	30	0.4	1.8	2.4	0.8	2.0	1800	72
A5	30	0.4	1.8	2.4	0.8	2.0	1800	72
A4	30	0.5	1.8	2.4	0.8	2.0	1800	72
A3	30	0.4	1.8	2.4	0.8	2.0	1800	72
A2	30	0.4	1.8	2.4	0.8	2.0	1800	72
A1	30	0.4	1.8	2.4	0.8	2.0	1800	72
A0	30	0.4	1.8	2.4	0.8	2.0	1800	98
A7RB	30	0.5	0.35	2.7	0.8	2.0	----	120
IORQB	30	0.5	12	2.4	0.8	2.0	20	10
WRB	30	0.5	12	2.4	0.8	2.0	20	10
RFSHB	30	0.5	12	2.4	0.8	2.0	20	10
EXROM	30	0.5	12	2.4	---	---	----	--
ROSCS	30	0.5	12	2.4	---	---	----	--
MREQB	30	0.5	12	2.4	0.8	2.0	20	10
ROB	30	0.5	12	2.4	0.8	2.0	20	10
MI	30	0.4	1.8	2.4	0.8	2.0	20	10
8E	--	---	---	---	0.8	2.0	10	12
BUSAK	30	0.4	1.8	2.4	---	---	----	--
WAIT	--	---	---	---	0.8	2.0	----	10
HALT	30	0.4	1.8	2.4	0.8	2.0	----	10
NMI	--	---	---	---	0.8	2.0	----	10
INT	----- OPEN COLLECTOR WITH PULL-UP -----							
R	50	0.4	1.8	2.4	---	---	----	--
G	50	0.4	1.8	2.4	---	---	----	--
B	50	0.4	1.8	2.4	---	---	----	--
VIDEO	----- TO 75 ohm COAX -----							
D0	30	0.4	1.8	2.4	0.8	2.0	20	120
D1	30	0.4	1.8	2.4	0.8	2.0	20	120
D2	30	0.4	1.8	2.4	0.8	2.0	20	120
D3	30	0.4	1.8	2.4	0.8	2.0	20	120
D4	30	0.4	1.8	2.4	0.8	2.0	20	120
D5	30	0.4	1.8	2.4	0.8	2.0	20	120
D6	30	0.4	1.8	2.4	0.8	2.0	20	120
D7	30	0.4	1.8	2.4	0.8	2.0	20	120
SPKP/TAPE OUT	500	0.2	0.04	0.3-0.5	---	---	----	--
EAR	15	0.5	1.6	2.4	+/- 1.3	+/- 5.0	----	--
SOUNO	100	0	---	2.5	-0.3	+5.0	----	--
BUSRQ	--	---	---	---	0.8	2.0	----	10
RESET	----- 1 $\mu$ F WITH 220K PULL-UP -----							
IOA5	--	---	---	---	---	---	----	--

### 2.4.1.1 Attachment of an RGB Monitor

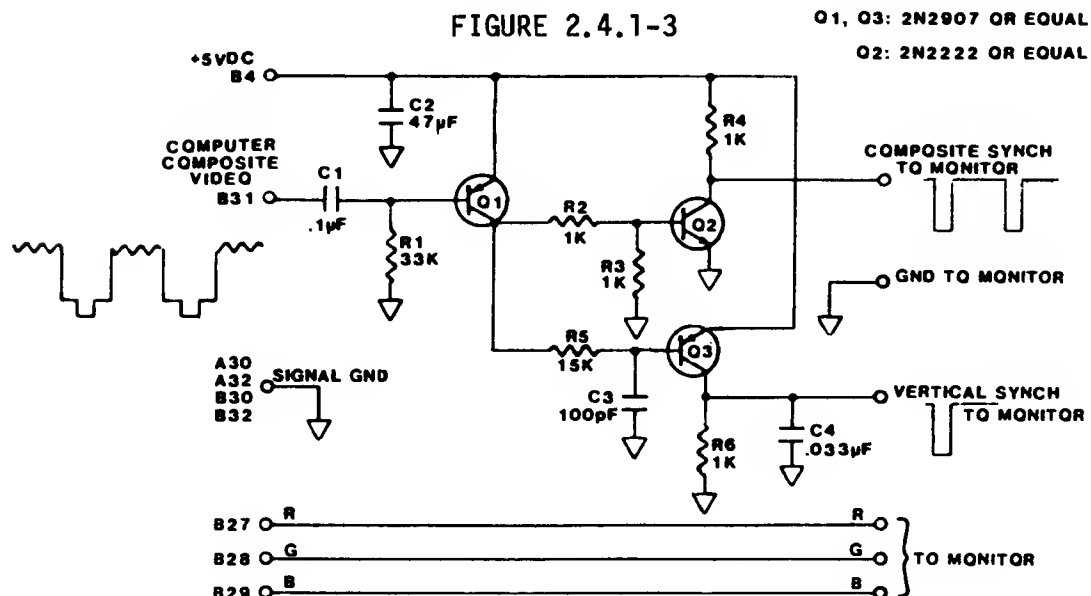
The TS 2068 provides via the P1 rear-edge connector the ability to attach an RGB monitor for excellent picture clarity and resolution. The TTL-level logic signals appear directly on the rear-edge connector of the TS 2068 -- the necessary synch signals can be derived from the simple synch stripper/separator circuit described here.

The Schematic of Figure 2.4.1-3 shows the required connections and electronics. Attachment is via the 64-pin keyed P1 connector. Shielding should not normally be required, but ferrite beads are recommended on each wire to minimize EMI, TVI, etc.

Circuit Operation - R1 and the base-emitter junction of Q1 operate as a DC restoration circuit with current flowing only when the composite video input signal from connector pin B31 is at the synch level. With the charge maintained on C1, Q1 conducts only during the synch pulse interval (not during the color burst time). During this conduction interval, the composite synch signal appears in inverted form on the collector of Q1. The Q2 stage simply re-inverts the signal, providing at its collector a composite synch signal for the connected monitor.

To provide a separated Vertical synch pulse, R5 and C3 filter the output of Q1 to partially eliminate the Horizontal synch pulses which are shorter than the Vertical synch pulses. The partially filtered inverted signal is re-inverted by Q3, then R6 and C4 complete the elimination of the Horizontal synch pulses so that a separate Vertical synch pulse is supplied for the attached monitor.

Signals R, G, and B from connector pins B27, B28, and B29 can be supplied directly to the attached monitor.



## 2.4.2 Cartridge Connector - J4

The TS2068 provides a 2 X 18 pin connector (designated J4 on the schematic) under the door at the front right of the console. The table and figures listed below detail the mechanical, functional, and electrical requirements and limits of the J4 Cartridge Connector.

FIGURE/TABLE	TITLE
Figure 2.4.2-1	J4 Mating PCB Mechanical Requirements
Figure 2.4.2-2	J4 Signal Layout
Table 2.4.2-1	J4 Signal Definition
Table 2.4.2-2	J4 Signal Electrical Characteristics

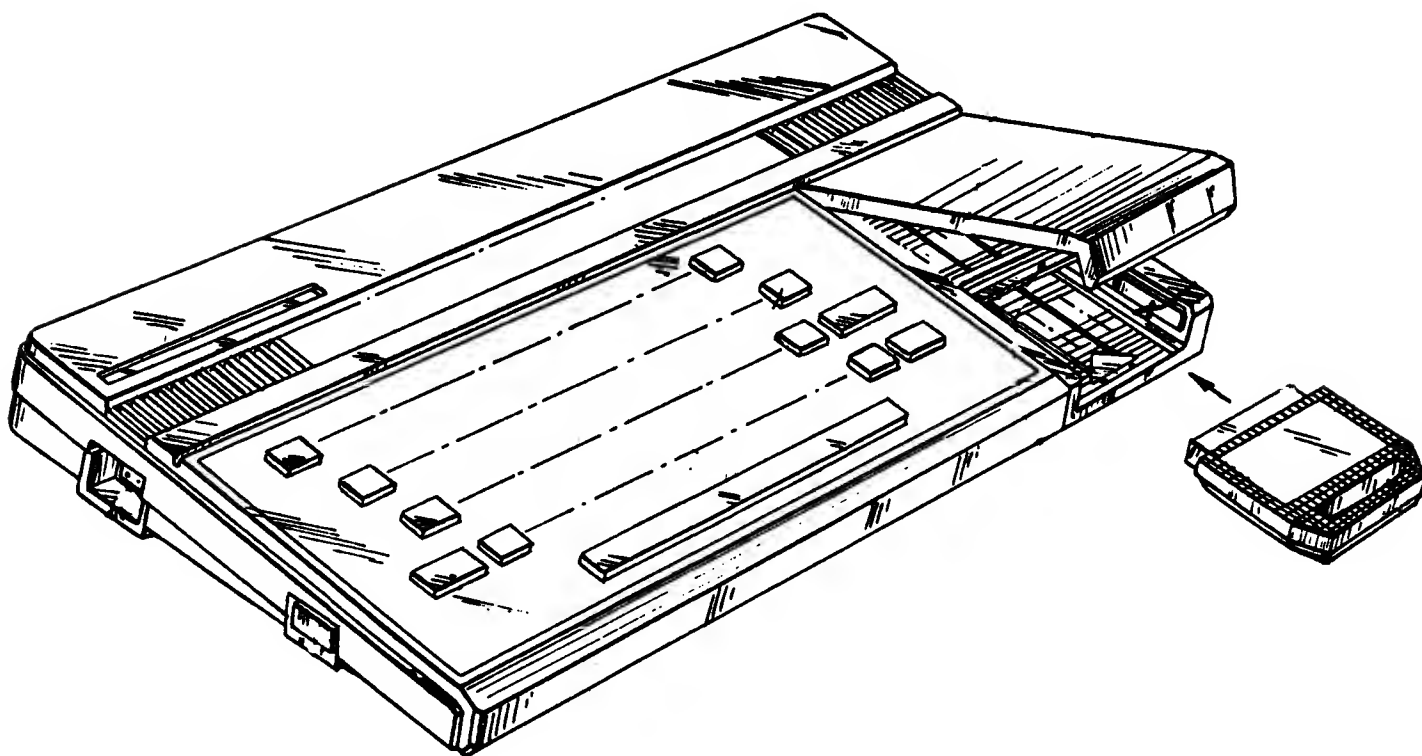
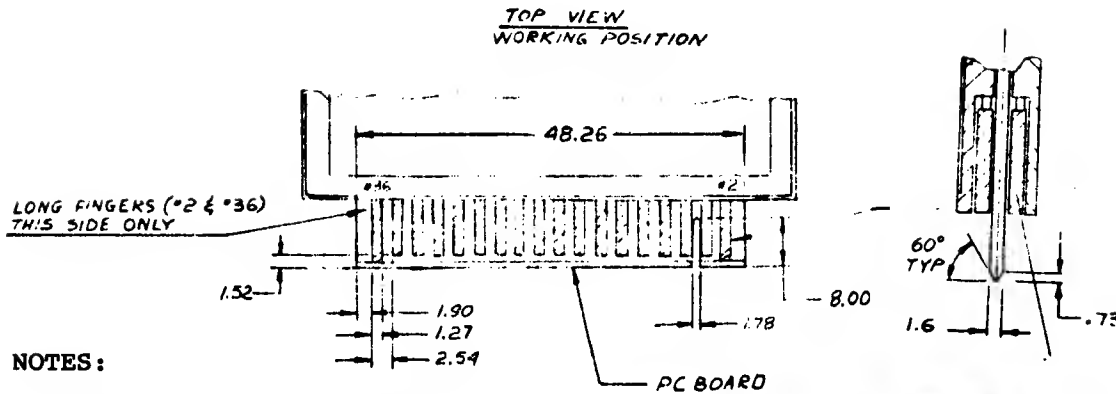


FIGURE 2.4.2-1

J4 MATING PCB MECHANICAL REQUIREMENTS



NOTES:

- (1) Circuit Board Material:  
FLGPN C62  
C1/1A2A (94V-0)  
Copper 1 or 2 sides
- (2) Contact Fingers: Min. 10  
millionth MIL-G - 45204 Gold over  
.00005 to .00010 inch low stress  
nickel.
- (3) Contact Fingers 2 and 36  
should be longer than other  
fingers to latch-up when inserted  
with power on.

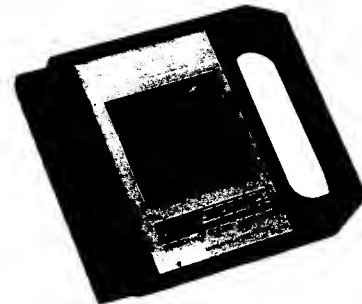


FIGURE 2.4.2-2

J4 SIGNAL LAYOUT

(View from Front)

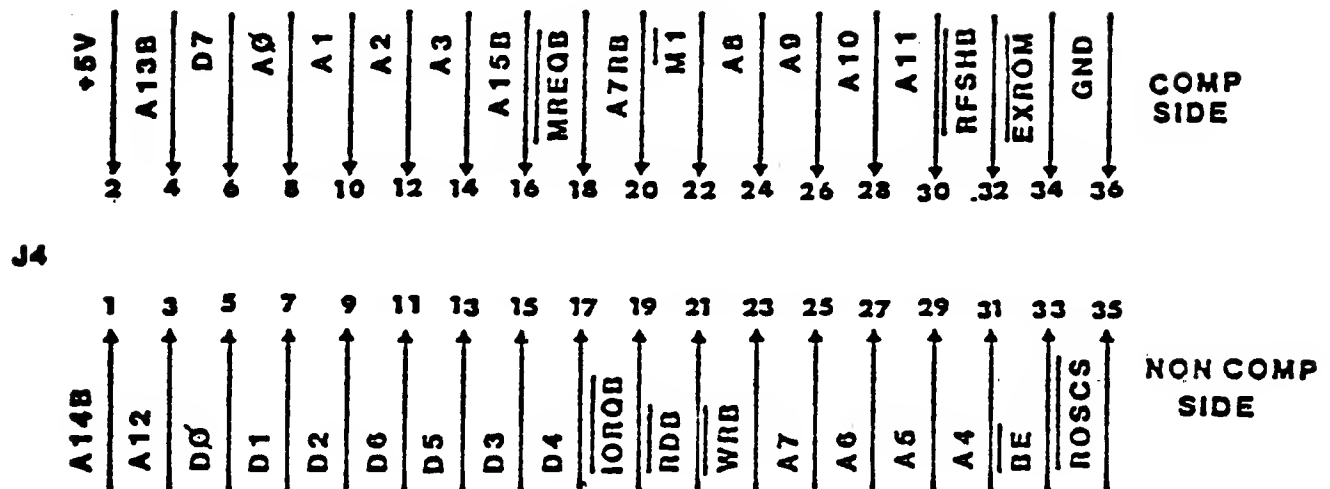


TABLE 2.4.2-1

## J4 CONNECTOR SIGNAL DEFINITIONS

PIN #	SIGNAL NAME	DESCRIPTION
1	A14B	Address Bus Bit 14, Buffered
2	+5V	+5 volts DC
3	A12	Address Bus Bit 12
4	A13B	Address Bus Bit 13, Buffered
5	D0	Data Bus Bit 0
6	D7	Data Bus Bit 7
7	D1	Data Bus Bit 1
8	A0	Address Bus Bit 0
9	D2	Data Bus Bit 2
10	A1	Address Bus Bit 1
11	D6	Data Bus Bit 6
12	A2	Address Bus Bit 2
13	D5	Data Bus Bit 5
14	A3	Address Bus Bit 3
15	D3	Data Bus Bit 3
16	A15B	Address Bus Bit 15, Buffered
17	D4	Data Bus Bit 4
18	<del>MREQB</del>	Memory Request (Active Low), Bfrd.
19	<del>IORQB</del>	I/O Request (Active Low), Buffered
20	A7RB	Refresh Address Bit 7, Buffered
21	<del>RDB</del>	Read (Active Low), Buffered
22	<del>MT</del>	CPU M1 State (Active Low)
23	<del>WRB</del>	Write (Active Low), Buffered
24	A8	Address Bus Bit 8
25	A7	Address Bus Bit 7
26	A9	Address Bus Bit 9
27	A6	Address Bus Bit 6
28	A10	Address Bus Bit 10
29	A5	Address Bus Bit 5
30	A11	Address Bus Bit 11
31	A4	Address Bus Bit 4
32	<del>RFSHB</del>	Refresh (Active Low), Buffered
33	<del>BE</del>	Bank Enable (Active Low)
34	<del>EXROM</del>	Extension ROM Enable (Active Low)
35	<del>ROSCS</del>	ROS Chip Select (Active Low) (Dock Bank Enable)
36	GND	Ground

TABLE 2.4.2-2

## J4 SIGNAL ELECTRICAL CHARACTERISTICS

MNEMONIC	----- OUTPUTS FROM TS2068 -----					----- INPUTS TO TS2068 -----			
	CAPACITIVE LOADING MAX (PF)	V(OL) MAX VOLTS	I(LOAD) MAX (MA)	V(OH) MIN VOLTS	I(LOAO)*V(IL) MIN (uA)	V(IH) MIN VOLTS	I IN (MAX)  μA	INPUT CAPACITIVE LOADING MAX (PF)	
A158	30	0.5	1.8	2.4	10				
A148	30	0.5	1.8	2.4	10				
A138	30	0.5	1.8	2.4	10				
A12	30	0.4	1.8	2.4	10				
A11	30	0.4	1.8	2.4	10				
A10	30	0.4	1.8	2.4	10				
A9	30	0.4	1.8	2.4	10				
A8	30	0.4	1.8	2.4	10				
A7	30	0.4	1.8	2.4	10				
A6	30	0.4	1.8	2.4	10				
A5	30	0.4	1.8	2.4	10				
A4	30	0.4	1.8	2.4	10				
A3	30	0.4	1.8	2.4	10				
A2	30	0.4	1.8	2.4	10				
A1	30	0.4	1.8	2.4	10				
A0	30	0.4	1.8	2.4	10				
A7RB	30	0.5	0.35	2.7					
ROSCS	30	0.4	1.8	2.4	10				
MREQB	30	0.5	1.8	2.4	10				
RDB	30	0.5	1.8	2.4	10				
IORB	30	0.5	12	2.4	10				
WRB	30	0.5	12	2.4	10				
RFSH8	30	0.5	12	2.4	10				
EXROM	30	0.5	12	2.4	10				
MI	30	0.5	12	2.4	10				
D0	30	0.4	1.8	2.4		0.8	2.0	15	
D1	30	0.4	1.8	2.4		0.8	2.0	15	
D2	30	0.4	1.8	2.4		0.8	2.0	15	
D3	30	0.4	1.8	2.4		0.8	2.0	15	
D4	30	0.4	1.8	2.4		0.8	2.0	15	
D5	30	0.4	1.8	2.4		0.8	2.0	15	
D6	30	0.4	1.8	2.4		0.8	2.0	15	
D7	30	0.4	1.8	2.4		0.8	2.0	15	
Vcc (+5V)	--	5.25	300	4.75					
GND	--	--	---	---					

### 2.4.3 Cassette I/O

The EAR and MIC connectors provided on the rear of the TS2068 are 1/8" mini-phone jacks requiring 1/8" mini-phone plugs as mating connectors.

The MIC output is filtered by a low-pass filter with a breakpoint of 2.5KHz and provides a signal output of 0.15 to 0.67 V p-p.

The EAR input is filtered by a low-pass filter with a breakpoint of 23 KHz. Input voltages should be between 4.0 and 10.0 V p-p.

### 2.4.4 Joystick

The joystick input connectors, one on each side of the TS2068 case, are standard 9-pin "D" type connectors for use with 5-switch type joysticks.

Connector layout and the function of each pin is given in Figure 2.4.4-1 and Table 2.4.4-1, respectively.

FIGURE 2.4.4-1

JOYSTICK CONNECTOR

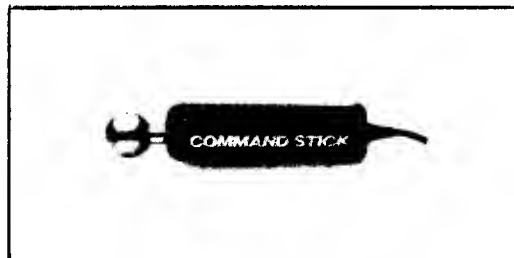
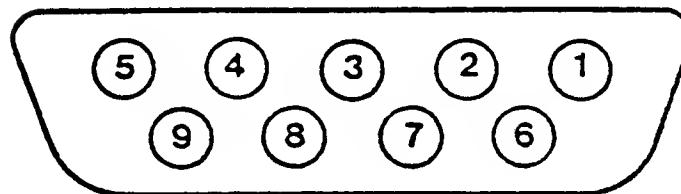




TABLE 2.4.4-1

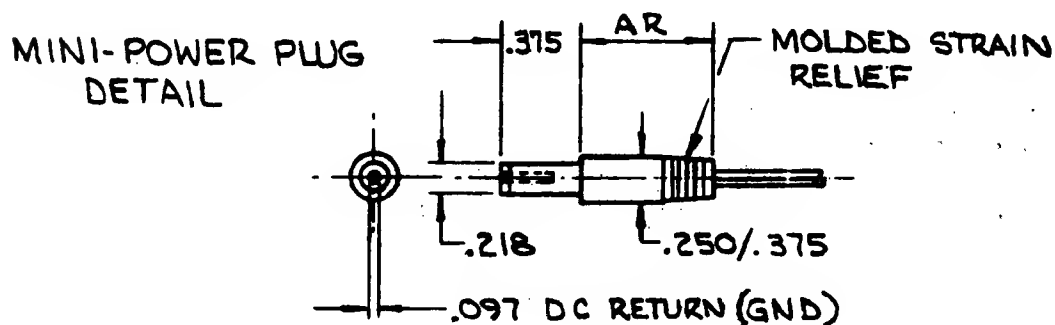
## JOYSTICK CONNECTOR SIGNAL ASSIGNMENT

P/N	SIGNAL NAME	I/O PORT BIT	FUNCTION
1	DIR1	0	STICK UP
2	DIR2	1	STICK DOWN
3	DIR3	2	STICK LEFT
4	DIR4	3	STICK RIGHT
5	---	----	not used
6	BUTTON	7	PUSH BUTTON
8	5V	---	5 VOLT POWER
8	READ STROBE	---	ADDRESS BIT 8 OR 9*
9	GND	---	POWER GROUND

\*When Address Bit 8 is high, the READ strobe to the left joystick is driven low. When address Bit 9 is high, the READ strobe to the right joystick is driven low.

## 2.4.5 AC Adapter Power Plug

The AC Adapter provided with the TS 2068 provides unregulated DC to the unit as described in Section 2.1.1 Mechanical details of the plug which mates to the TS 2068 are shown below:



#### 2.4.6 Composite Monitor Output

The MONITOR output on the rear of the TS2068 provides a 1 V p-p (+/- 20%) composite color video signal output to an RCA phono jack which is mated by a standard phono plug into a 75 ohm coax cable. See Section 2.1.11.1.

#### 2.4.7 RF Output

The TV output on the rear of the TS2068 provides a modulated color video signal on VHF Channel 2 or Channel 3 as selected by the channel select switch on the bottom of the unit. Connection to the RCA phono jack output should be via a standard phono plug and 75 ohm coax cable. See Section 2.1.11.2.

Channel frequencies provided are

Channel 2      55,250 +/- 100 KHz  
Channel 3      61,250 +/- 100 KHz

Output levels are less than 3 milliwatts as limited by the Federal Communications Commission.

#### 2.4.8 Keyboard Interface - J9 Connector

Located on the PCB inside the TS 2068 is a 14-pin single-in-line flex cable connector (AMP TRIO-MATE P/N 1-520315-4 or equivalent). Signals are as listed below:

<u>PIN</u>	<u>SIGNAL</u>
0	GND
1	KB0
2	KB1
3	KB2
4	KB3
5	KB4
6	CR6/A11
7	CR7/A10
8	CR8/A9
9	CR9/A12
10	CR10/A13B
11	CR11/A8
12	CR12/A14B
13	CR13/A15B

Any modification to or replacement of the keyboard supplied must consider the following:

- (1) Contact resistance less than 200 ohms.
- (2) Bounce less than 10 ms.
- (3) Capacitance per line less than 20 pF (0 or 1 key depressed); less than 40 pF (more than 1 key depressed).

## 3.0 SYSTEM SOFTWARE GUIDE

### 3.1 Identifier

Location 19 (13H) of the Home Bank ROM is used to identify the revision level of the System Software. The initial version is identified by this location having a value of 255 (FFH). Any subsequent versions will decrement this value by 1, e.g., the first revision would be identified by a value of 254 (FEH). This identifier should be used to conditionally apply patches or execute "work-arounds" identified as necessary with a particular version of the System Software.

### 3.2 ROM Organization and Services

#### 3.2.1 Home ROM

##### 3.2.1.1 Fixed Entry Points

Home ROM Location 0 is the entry to the system initialization code upon power-up (Ref. Figure 1.1-4). Locations 8 through 48 (8H through 30H) are the Z80 RESTART entry points for the following functions:

RESTART	FUNCTION
8	ERROR - Error exit from BASIC (Address on Stack points to Error Number)
16	WRCH - Write Character (Code in A) to Current Output Channel as established by SELECT (Address of output routine pointed to by System Variable CURCHL). (See Section 4.0).
24	IGN SP - Return in A the current significant character in the Program Line (Address in System Variable CH_ADD) skipping over spaces and control characters except End-of-Line (ODH=ENTER)

- 32            NXT\_IS - Like IGN\_SP but returns  
                 in A the Next Significant  
                 Character.
- 40            CALCTR - Entry to Calculator  
                 Routines .
- 48            COPYUP - Make room for BC Bytes  
                 of temporary workspace just  
                 before address in System Variable  
                 STKBOT by copying up memory  
                 between there and the address in  
                 STKEND, adjusting affected  
                 pointers. Returns DE=1st Byte of  
                 Space; HL=Last.

Location 56 (38H) is the entry to service the hardware generated interruption which occurs approximately every 1/60 of a second (16.67 ms). Z80 Int. Mode 1 is used. This interruption is used to scan the keyboard (call to routine UPD K - see Section 4.1.1). It is also used to update the Frame Counter (3 bytes pointed to by the System Variable FRAMES) used by the RANDOMIZE instruction.

Location 102 (66H) is the entry point for the NMI interruption, but this interruption is not used in the TS2068 design. (See Section 2.1.3.8 NMI Interruption.)

### 3.2.1.2 BASIC AROS Support

BASIC Application Cartridges are supported by special code in the Home ROM. A program line is copied from the cartridge to a buffer in the Home RAM (ARSBUF) and is then executed from there by the BASIC Interpreter. When a READ command is executed, the line containing the appropriate DATA statement is also copied from the cartridge to the RAM. The cartridge memory is enabled only for search and copy operations for both program lines and DATA statements, and when executing a USR function, otherwise the entire Home Bank is enabled while executing in the BASIC Interpreter. There is no support for User-Defined Functions which insert the expanded definition parameters directly into the program and then require search of the program area to find these parameters whenever a function is invoked.

See Section 5.1, Cartridge Software/Hardware, for additional details on BASIC AROS.

### 3.2.1.3 General

The balance of the Home ROM contains the BASIC Interpreter and standard I/O routines with the exception of the cassette I/O which is in the Extension ROM. The bit map table for the standard character set is located at the end of the Home ROM from location 15616 to 16383 (3D00H to 3FFFH). The address of this table minus 256 (100H) is contained in the System Variable CHARS (=3C00H).

The Home ROM routines accessible via the Function Dispatcher are described in Table 3.3.4-2. See Appendix A for the ROM Maps giving the ROM addresses of these routines.

## 3.2.2 Extension ROM

### 3.2.2.1 Fixed Entry Points

Extension ROM Location 0 contains code to pass control to the initialization code in the Home ROM. (Figure 1.1-4).

Extension ROM Location 56 (38H) is the interruption fielder. Control is passed to the System RAM code (See Section 3.3.3) to bank switch to the Home Bank and call the interruption service routines after which the state of the machine is restored and control returns to the interrupted process. Figure 3.2.2-1 shows the Extension ROM Interruption Fielder code.

### 3.2.2.2 General

The balance of the Extension ROM contains the following major components:

- Final Phase of System Initialization  
(See Figure 1.1-4)
- Cassette tape I/O (see Section 4.2)
- Change Video Mode Service
- OS RAM routines including the Function Dispatcher (copied to RAM at System Initialization) (see Section 3.3.3)
- Function Dispatcher Jump Table

FIGURE 3.2.2-1

## Extension ROM Interruption Fielder

<u>LOCATION</u>	<u>OBJECT CODE</u>	<u>SOURCE CODE</u>	<u>COMMENTS</u>
0038	F5	PUSH AF	Save AF
0039	F3	DI	Disable Ints.
003A	3AC25C	LD A,(VIDMOD)	Test Vidmod
003D	A7	AND A	
003E	00	NOP	
003F	2804	JR Z,CHK3	Vidmod=0
0041	F1	POP AF	Restore AF
0042	C36EFA	JP INT7	Chunk 7 if Vidmod not 0
0045	F1	CHK3 POP AF	Restore AF
0046	C3AE62	JP INT3	Chunk 3 if Vidmod = 0

## 3.2.2.3 Video Mode Change Service

The routine CHNG VID takes as input a single byte in Register A which designates the desired video mode as shown in Table 3.2.2-1. All non-zero values involve access to the second display file located at 6000H-7AFFH. When the mode change requires remapping of the RAM (see Figure 1.1-3), the necessary relocation (BASIC program, machine stack, OS RAM code, UDG area, etc.) and modifications (system variables, RAM code internal addresses, stack pointer, etc.) are done by this service. The desired video mode is written to Port 0FFH, Bits 0-5, and the System Variable VIDMOD (5CC2H) is updated. The second display file is cleared to zeros on initial access (for Dual Screen Mode and High Resolution Graphics Mode, this results in a black screen since 0 yields attributes of black ink on black paper). If there is not enough free memory to do the necessary remapping, Error 4, Out of Memory is given.

Access to this service via the Function Dispatcher cannot be made consistently for various reasons. An Interface Routine is given in Section 3.2.2.4, to be executed from the Home RAM, which provides access to the Video Mode Change Service as well as other Extension ROM routines.

See Sections 4.1.2 and 5.2 for discussion of video screen support software. See Section 6.4 for details on known problems and corrections related to the Video Mode Change Service.

TABLE 3.2.2-1

## INPUT TO VIDEO MODE CHANGE SERVICE

<u>VALUE IN A</u>	<u>VIDEO MODE</u>	<u>DESCRIPTION</u>
0	Normal	Primary Display File Only(Close 2nd Display File if Open)
128 (80H)	Dual Screen	Two Display Files Available. Primary Display File Active at Screen.
1	Dual Screen	Two Display Files Available. Second Display File Active at Screen
2	High Resolution Graphics	Primary Display File contains data for 256X192 pixels. Second Display File contains 6144 Attribute Bytes, each one controlling 8X1 pixels. NOTE 1.
	<div>64-Column</div> <div> <div><u>Ink</u></div> <div><u>Paper</u></div> </div>	
6	Black	White
14 (0EH)	Blue	Yellow
22 (16H)	Red	Cyan
30 (1EH)	Magenta	Green
38 (26H)	Green	Magenta
46 (2EH)	Cyan	Red
54 (36H)	Yellow	Blue
62 (3EH)	White	Black

NOTE 1: The areas of memory normally used for Attribute Bytes are not accessed by the video hardware in this mode.

#### 3.2.2.4 Extension ROM Interface Routine

The Extension ROM routines W TAPE (Write from RAM to Tape), R-TAPE (Read from Tape to RAM) (see Section 4.2) and CHNG VID (see Section 3.2.2.2) may be of interest to the machine code programmer. Because of a conflict with the use of the IX Register, the tape routines cannot be successfully accessed via the Function Dispatcher. Because the Change Video Mode Service may involve relocating the OS RAM routines (including the Function Dispatcher), and for other reasons, it also cannot be consistently accessed using the Function Dispatcher. Figure 3.2.2-2 gives a sample routine, to be executed from the Home RAM, which can be used to bank switch to the Extension ROM and call directly to the desired service. Appendix A contains an Extension ROM Map giving the addresses of these and other routines.



FIGURE 3.2.2-2

## EXTENSION ROM INTERFACE ROUTINE

		1	:			:	EXTENSION ROM INTERFACE ROUTINE
		2	R_TAPE	EQU	00FCM	:	READ TAPE ROUTINE
		3	W_TAPE	EQU	0068M	:	WRITE TAPE ROUTINE
		4	CHNG_VID	EQU	0E8EM	:	CHANGE VIDEO MODE ROUTINE
		5	VIDMOD	EQU	5CC2M	:	VIDEO MODE SYSTEM VARIABLE
		6	:				
		7	:				
		8	:			:	CALL READTP WITH REGISTERS SET
		9	:			:	UP FOR R_TAPE ROUTINE
		10	:				
0000'	21 00FC	11	READTP	LD	HL,R_TAPE	:	ADDRESS TO HL
0003'	CC 0020'	12		CALL	IFRTN	:	ENABLE EXT./EXECUTE RTN
0006'	18 17	13		JR	EXIT	:	RESTORE HOME BANK AND RETURN
		14	:				
		15	:			:	CALL WRITTP WITH REGISTERS SET
		16	:			:	UP FOR W_TAPE ROUTINE
		17	:				
0008'	21 0068	18	WRITTP	LD	HL,W_TAPE	:	ADDRESS TO HL
000B'	CC 0020'	19		CALL	IFRTN	:	
000E'	18 0F	20		JR	EXIT		
		21	:				
		22	:			:	CALL CHGVID WITH DESIRED VIDEO
		23	:			:	MODE IN A
		24	:				
0010'	21 0E8E	25	CHGVID	LD	HL,CHNG_VID	:	ADDRESS TO HL
0013'	F5	26		PUSH	AF	:	SAVE VIDEO MODE
0014'	CD 0020'	27		CALL	IFRTN		
		28	:				
		29	:			:	COMPENSATE FOR "BUG" IN
		30	:			:	CHNG_VID RTN.WHICH SETS
		31	:			:	VIDMOD=0 INSTEAD OF 80
		32	:			:	WHEN BOTH DISPLAY FILES
		33	:			:	ARE OPEN
		34	:				
		35	:				
0017'	F1	36		POP	AF	:	TEST VIDEO MODE
0018'	FE 80	37		CP	80H	:	TEST IF 80
001A'	20 03	38		JR	NZ,EXIT		
001C'	32 5CC2	39		LD	(VIDMOD),A	:	SET VIDMOD=80H
001F'	3A 002C'	40	EXIT	LD	A,(HSSAVE)	:	GET PREV. HQR.SEL.
0022'	D3 F4	41		OUT	(0F4H),A	:	RESTORE
0024'	D8 FF	42		IN	A,(0FFH)	:	READ PORT FF
0026'	C8 BF	43		RES	7,A	:	TURN OFF RCM SEL.
0028'	03 FF	44		OUT	(0FFH),A		
002A'	F8	45		EI			
002B'	C9	46		RET			
		47	:				
002C'	00	48	HSSAVE	0EF8	0	:	SAVE HQR.SEL. (PORT 0F4H)
		49	:				
002D'	F3	53	IFRTN	OI		:	MASK INTERRUPTIONS
002E'	F5	54		PUSH	AF	:	PRESERVE REG. A
002F'	D8 FF	55		IN	A,(0FFH)	:	EXT.ROM SELECT BIT
0031'	C8 FF	56		SET	7,A	:	SEL. EXT.ROM
0033'	D3 FF	57		OUT	(0FFH),A		
0035'	08 F4	58		IN	A,(0F4H)	:	HORIZONTAL SELECT FOR DOCK/EXT
0037'	32 002C'	59		LD	(HSSAVE),A	:	SAVE
003A'	3E 01	60		LD	A,1	:	SELECT CHUNK 0 IN EXT.ROM
003C'	D3 F4	61		OUT	(0F4H),A	:	
003E'	F1	62		POP	AF	:	RESTORE REG. A
003F'	E9	63		JP	(HL)	:	EXECUTE TARGET ROUTINE AND
		64	:			:	RETURN TO CALLER OF IFRTN
		65		END			

### 3.3 RAM Organization and Services

#### 3.3.1 System Variables

RAM beginning at 23552 (5C00H) is dedicated to the BASIC System Variables as defined in Appendix D of the TS 2068 User Manual and in Appendix B of this document. The area from the end of the defined variables (STRMMN - 23755 (5CCB) ) to 24297 (5EE9H) is reserved for expansion of the System Variables, but is not used by the Operating System in the current TS 2068.

#### 3.3.2 System Configuration Table

The area from 24298 (5EEAH) to 24575 (5FFFH) is reserved for the System Configuration Table (SYSCON). This table is built at system initialization time and is comprised of an 8 byte entry for AROS, a 4 byte entry for LROS, followed by eleven 24-byte entries for proposed expansion banks and an End-of-Table marker. In the original TS 2068 the actual usage of this table is limited to the 12 bytes for software cartridge identification (see Section 5.1 for details of the LROS and AROS Overhead Bytes).

#### 3.3.3 Machine Stack

The TS 2068 reserves 512 (200H) bytes of RAM for the Machine Stack. The Machine Stack pointer is initialized to a value of 6200H (value also in System Variable MSTBOT); the pointer is decremented as items are pushed onto the stack (the pointer may also be modified directly by software). While the area reserved for the stack extends to 6000H, there is no actual check made to enforce this limit.

Note that the Machine Stack is located in the same memory area as the second display file. The CHNG VID routine relocates the stack to the memory area from 0F7COH to 0F8BFH, and modifies the Stack Pointer and MSTBOT (0F8COH), as well as other affected system variables, when initializing the second display file. (See Section 3.2.2.3.)

#### 3.3.4 OS RAM Routines

The code for the following Operating System functions is copied from the Extension ROM to Chunk 3 of the RAM at System initialization time. Since this is in the same memory area as the second display file, this code must be relocated, along with the machine stack, if the second display file is to be used. The CHNG VID routine does the necessary relocation and modifications. (Section 3.2.2.3.)

Because this code is not in a fixed location, access to the OS RAM routines is conditional on the current video mode. The standard technique employed is to test the value in the System Variable VIDMOD at location 23746 (5CC2H). A zero indicates that the second display file is not in use and that the OS RAM routines are therefore in Chunk 3; any non-zero value indicates that the routines are in Chunk 7.

NOTE: This design implies that Chunks 2, 3 and 7 are always enabled in the Home Bank RAM whenever the System ROM and/or RAM routines are being used.

The OS RAM routines are contained in Module "Dispatch" which is included in Appendix A.

#### 3.3.4.1 RAM Interruption Handler

Chunk 3 Entry: 62AEH

Chunk 7 Entry: FA6EH

The user must enter with bank status and Z80 registers intact, with address from point of interruption on the stack.

The RAM interruption handler saves state, including memory selection, enables the Home Bank, updates the Frame Counter, calls the keyboard scan routine in the Home ROM, restores state, and returns to the interrupted process.

The RAM Interruption handler is used whenever the interruption occurs while the Extension ROM is enabled. See Figure 3.2.2-1, Extension ROM Interruption Fielder. This same technique can be used for interruption processing in another bank, e.g. if an LROS wanted to use the standard system ROM keyboard scanning routines.

#### 3.3.4.2 RAM Service Routines

Table 3.3.4-1 lists the RAM service routines which are designed to facilitate communication between memory banks. Those with Service Codes are accessible via the Function Dispatcher.

TABLE 3.3.4-1  
OS RAM SERVICE ROUTINES

LABEL	SERVICE CODE (Decimal)	LOCATION		DESCRIPTION
		CH.3	CH.7	
GET_WORD	-	6316	FAD6	Returns in HL the word from the address in HL in the bank specified in B.
PUT_WORD	-	633B	FAFB	Writes the word in DE to the address in HL in the bank specified in B.
GET_STATUS	14	6405	FBC5	Returns current memory selection (Horizontal Select byte - low active) in C for the bank specified in B. Preserves Bank # in B for Home, Ext. or Dock.
GET_CHUNK	-	644D	FC0D	Returns a single byte mask in A with all bits 0 except the one corresponding to the chunk for the address in HL.
GET_NUMBER	15	645E	FC1E	Returns in Reg. A the bank number currently controlling the address in HL.
BANK_ENABLE	-	6499	FC59	Enables the memory selected (Horizontal Select byte - low active) in the specified bank. (Bank # in B; Mem.Sel.in C)
GOTO_BANK	-	6572	FD32	Transfers control to the specified address after enabling the memory selected in the specified bank. Parameters passed on stack by pushing target address, then Bank #/Mem.Select prior to calling GOTO_BANK. (Return address is discarded.).
CALL_BANK	-	65D0	FD90	Like GOTO_BANK except saves current bank status, calls target address, and restores status prior to returning to user. Two additional parameters are passed on stack prior to doing call to CALL_BANK. These are PRM OUT (16-bits) following by PRM IN (16 bits) as described for the Function Dispatcher.

TABLE 3.3.4-1

OS RAM SERVICE ROUTINES  
(continued)

LABEL	SERVICE CODE	LOCATION		DESCRIPTION
	(Decimal)	CH.3	CH.7	
XFER_BYTES	-	6722	FEE2	<p>Copies n byte(s) from specified source to specified destination in either ascending or descending order. Source and destination can be in the same or different banks and can be in shadowing chunks, but neither source nor destination can pass a "chunk" (8K) boundary since only the chunks containing the starting source and destination addresses are explicitly enabled.</p> <p>Parameters passed on stack by pushing:</p> <p>Source Bank/Dest.Bank Source Address Dest. Address Length 0/Direction:   (0=Ascending   -1=Descending)</p>

NOTE: See Appendix A for listing of these routines. See Section 6.0 for known corrections to the routines.

### 3.3.4.3 Function Dispatcher

Chunk 3 Entry: 6200H

Chunk 7 Entry: F9C0H

The Function Dispatcher provides a common interface to a number of system routines via a Service Code and Jump Flag parameter passed on the machine stack. Table 3.3.4-2 lists the routines in Service Code order. Codes for routines that are known to not be successfully accessible via the Function Dispatcher have been deleted (marked Reserved). However, there is no guarantee that those on the list can be accessed without problems. Some ROM routines require data in a particular format, e.g. BASIC floating point number(s), both standard and special integer format, on the Calculator Stack which is located between (STKBOT) and (STKEND) (see Appendix C of the TS 2068 User Manual). An effort has been made to include information on register usage and functionality, but some of the ROM routines are so tightly tied to the BASIC Interpreter that they would require analysis which is beyond the scope of this document. These have been flagged with an Asterisk, but included in the list for documentation purposes only. Most of the routines which are directly implementing a BASIC command or function have two different action sequences based on the INTPT Flag (Bit 7 of FLAGS) which distinguishes syntax checking (Flag=0) from actual execution (Flag=1).

In order to use the Function Dispatcher, first set up any memory and stack (both machine and/or calculator) locations as if invoking the desired service directly. Then push the parameter(s) for the Dispatcher on the machine stack in the order outlined below. Finally, set up the registers as if invoking the desired service directly and call the Dispatcher based on its current location (Chunk 3 if VIDMOD=0 or Chunk 7 if VIDMOD has a non-zero value).

1. PRM\_OUT 16 bits - Number of bytes of parameter data being passed on the stack to the specified Service (number of stack "pushes" \* 2). Zero if no parameters being passed. E.g., to pass 4 bytes:

LD HL,4  
PUSH HL

This parameter is passed to the Dispatcher only if the Jump Flag (SVC\_CODE) Bit 15) is not set. NOTE: This parameter refers to machine stack entries only, not to the Calculator Stack.

2.       PRM\_IN   16 bits - Number of bytes of parameter data to be passed back from the specified Service (number of stack "pushes" \* 2). Zero if no parameters to be passed back.

This parameter is passed to the Dispatcher only if the Jump Flag (SVC\_CODE Bit 15) is not set. NOTE: This parameter refers to machine stack entries only, not to the Calculator Stack.

3.       SVC\_CODE 16 bits - Bits 0-14 identify the Service to be invoked. Bit 15 (Jump Flag) is set if no return is desired (jump to Service rather than call). Bit 15 is zero if return is desired. E.g, to call K\_SCAN using Service Code 136:

LD HL,136       or       LD HL,88H  
PUSH HL         PUSH HL

**Addendum To TS 2068 Function Dispatcher Services:**  
On page 84, COLOR and HIFLSH (service codes 85 and 86) cannot always be accessed through the Function Dispatcher, due to resetting of the carry flag by the FD. COLOR may be accessed by setting the registers as described in the manual, and then coding CALL #23DE. HIFLSH can be accessed similarly by coding CALL #2410.

TABLE 3.3.4-2

## TS 2068 FUNCTION DISPATCHER SERVICES

SERVICE	SERVICE CODE	DESCRIPTION
	1 - 13 (1-0DH)	Reserved
GET_STATUS	14 (0EH)	Returns Memory Selection (Low Active) in C for Bank # in B
GET_NUMBER	15 (0FH)	Returns Bank # in A for Address in HL
	16-24 (10-18H)	Reserved
UPD_K	25 (19H)	Process Keyboard Input (See Section 4.1.1)
PARP	26 (1AH)	Generates DE+1 Cycles of a Tone having the Period 8N+236 to 8N+246 T-States. HL=N. (See 4.4)
BEEP	27 (1BH)	BEEP Command - processes parameters on Calculator Stack. Exits via PARP. (See 4.4)
K_DUMP	28 (1CH)	COPY Command. Dumps Primary Display File to Printer. (See 4.1.3)
SENDTV	29 (1DH)	Char.Output to Screen/Printer. Character Code in A. (See 4.1.2)
SETAT	30 (1EH)	Set Print Position to value in BC. B=Line No. (0-23); C=Column No. (0-31)
ATTBYT	31 (1FH)	Set Attribute Byte for Display File Adrs. in HL using ATTR_T, MASK_T and P_FLAG.
R_ATTS	32 (20H)	Permanent Attribute Info. to Temporary Attribute Variables
CLLHS	33 (21H)	Clear Lower Screen (Primary Display File)
CLS	34 (22H)	Clear Entire Screen (Primary Display File)
DUMPPR	35 (23H)	Print/Clear Print Buffer. (See 4.1.3)



TABLE 3.3.4-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
PRSCAN	36 (24H)	Send Scan (32 bytes) to Printer. Pixel Data Address in HL. Number of Scans remaining in B (=1-8). (See 4.1.3)
DESLUG	37 (25H)	Remove Number Slugs from Edit Line Buffer (Address in HL)
K_NEW	38 (26H)	NEW command. See Fig. 1.1-4
INIT	39 (27H)	Initialize: DE=Maximum RAM Address. A=0 for Power-On; = -1 (FFH) for NEW. (See Fig.1.1-4)
INCH	40 (28H)	Input Character to A from currently Selected Channel. Returns NC if no input.
SELECT	41 (29H)	Select Channel (Stream) - # in A. (See 4.1)
INSERT	42 (2AH)	Insert BC Bytes before byte whose address is in HL. Copies up all from HL to (STKEND) and updates affected system variables. Returns BC=0; DE=adrs.of last byte of inserted space; HL=adrs.of byte before first.
RESET	43 (2BH)	Reset Calculator Stack. Sets (STKEND) = (STKBOT) and (MEM)=MEMBOT (5C92H).
CLOSE	44 (2CH)	CLOSE # Command. Channel # on Calculator Stack.
CLCHAN	45 (2DH)	Close Channel. BC=Value from STRMS (Index into CHANS).
OPEN	46 (2EH)	OPEN # Command. Channel # and Device Spec. on Calculator Stack
OPCHAN	47 (2FH)	Open Channel. Device Spec. on Calculator Stack. DE=pointer into STRMS based on Ch.#.  (See 4.1 for more info. on OPEN and CLOSE)

TABLE 3.3.4-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
CAT	48 (30H)	CAT Command (Not Applicable)
ERASE	49 (31H)	ERASE Command (Not Applicable)
FORMAT	50 (32H)	FORMAT Command (Not Applicable)
MOVE	51 (33H)	MOVE Command (Not Applicable)
FLASHA	52 (34H)	Flash Char.in A to Screen. (Calls SENDTV; assumes Lower Screen selected. Used to Flash Cursor.)
FIND_L	53 (35H)	Find BASIC Program Line with the number in HL. If Line found, returns Z and Address of Line in HL, else returns NZ and HL contains either address of line with next larger line number or points to the Variables area if there is no larger line number. Requested Line No. returned in BC and Address of Preceding Line in DE (DE=HL if no preceding line).
SUBLIN	54 (36H)	Finds either the D'th statement (D=Statement #; E=0) or 1st statement whose keyword token matches E (D=0), in a line pointed to by HL. If the D'th statement is found, returns Z and HL and (CH_ADD) both point to 1 byte before statement. (If line contains exactly D-1 statements, then the next line counts as the D'th.). If match on E is found, then returns NZ,NC and both HL and (CH_ADD) point to keyword. D is decremented by the number of statements looked at (e.g. D= -2 if two statements). If no match on E then returns NZ,C with both HL and (CH_ADD) pointing to End-of-Line byte (ODH).

TABLE 3.3.4-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
RECLEN	55 (37H)	Returns in BC the length of the record pointed to by HL. Sets DE to HL+BC. The record can be a program line, or a string or numeric variable or array.
DELREC	56 (38H)	Delete record pointed to by HL having length BC from Program or Variables memory. Updates affected system variables.
PUT_BC	57 (39H)	Converts number in BC from binary to ASCII and outputs to currently selected channel. If BC less than 0, outputs a 0.
SYNTAX	58 (3AH)	Check syntax of command or program line in Edit Line Buffer (E_LINE). ERR_NR= -1 if no errors, otherwise contains Error Number-1.
EXCUTE	59 (3BH)	Execute command(s) from Edit Line buffer.
FOR	60 (3CH)	FOR command. *
STOP	61 (3DH)	STOP command. Does RESTART 8 with Error No. 9.
NEXT	62 (3EH)	NEXT command. *
READ	63 (3FH)	READ command. *
DATA	64 (40H)	DATA statement. *
RESTBC	65 (41H)	RESTORE command - Line No. in BC
RAND	66 (42H)	RANDomize command. Sets seed for Random Number Generator based on Parameter on Calculator Stack. If parameter is non-zero, value is loaded to SEED; if zero, value in FRAMES is loaded to SEED.

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
CON'T	67 (43H)	CONT command. Loads values from OLDPPC and OSPPC to NEWPPC and NSPPC and returns. Inside the BASIC Interpreter, this results in executing from Line No. in NEWPPC, Statement No. in NSPPC.
JUMP	68 (44H)	Jump to Line - Loads Line Number from Calculator Stack to NEWPPC and sets NSPPC to 0 and returns.
FIX_U1	69 (45H)	Converts Floating Point number on Calculator Stack to a single byte unsigned binary value in A (uses FP2A). Does RESTART 8 for Error B if number out of range.
FIX_U	70 (46H)	Converts Floating Point number on Calculator Stack to a 2-byte unsigned binary value in BC (uses FP2BC). Error B if number out of range.
CLEAR	71 (47H)	CLEAR command. Processes parameter on Calculator Stack to value in BC for CLR_BC.
CLR_BC	72 (48H)	Value in BC is new RAMTOP. Deletes Variables, clears screen, and Calculator Stack, etc.
GO_SUB	73 (49H)	GO SUB command. Inserts a 3-byte GO SUB Block into the machine stack above the 2 most recent entries. The Block consists of current Line No. (2 bytes) and Statement No. (1 byte) to be used when RETURN is executed. Then calls JUMP to process GO SUB parameter and returns. At return to caller, machine stack consists of top of stack at point GO SUB was called, followed by 3-byte entry (Line No. MSB/Line No. LSB/Statement No.).

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
CHK_SZ	74 (4AH)	Checks if room for BC + 80 (50H) bytes between (STKEND) and (RAMTOP). Addition of 80 bytes is "left-over" from Spectrum to guarantee minimum machine stack where the stack was at the top of RAM. Error 4 if not enough room.
RETURN	75 (4BH)	RETURN command. Retrieves most recent GO SUB Block from Machine Stack (SP+4), loads data to NEWPPC and NSPPC and returns. Error 7 if MSB Line No.=3EH (End of Stack Marker).
PAUSE	76 (4CH)	PAUSE command. Processes parameter on Calculator Stack to BC then waits BC frames or until key is depressed. (Uses HALT instruction, so interruptions must be enabled.)
BREAK?	77 (4DH)	Reads BREAK key. Returns NC if it is pressed and ON ERROR is not active.
DEF	78 (4EH)	Define Function.*
K_LPR	79 (4FH)	LPRINT - Selects Channel 3 and processes items in LPRINT statement for output via WRCH.
K_PRIN	80 (50H)	PRINT - Selects Channel 2 and processes items in PRINT statement for output via WRCH (same code used for K_LPR).
P_SEQ	81 (51H)	Code used by K_LPR and K_PRIN to process output data and controls in BASIC statement (address in CH ADD).
INPUT	82 (52H)	INPUT command. Selects Channel 1 and processes I/O for Keyboard/Lower Screen using a buffer at (WORKSP) for input. *

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
I_SEQ	83 (53H)	Code used by INPUT to process input items and controls in BASIC statement (address in CH_ADD).
NOTKB?	84 (54H)	Returns Z if current channel is Keyboard/Lower Screen (device specification="K").
COLOR	85 (55H)	Adjusts system variables ATTR_T, MASK_T and P_FLAG for color code in D (0-9). Enter with C set to set Ink or NC set to set Paper. Error K if D is invalid.
HIFLSH	86 (56H)	Adjusts system variables (ATTR_T and MASK_T) for Flash/Bright code in D (0, 1 or 8) else Error K. Enter with C for Flash or NC for Bright.
SCRMBL	87 (57H)	Returns in HL the primary display file address for the pixel with coordinates in BC (B=Y;C=X). Returns in A the bit no (0-7) where 0=lefthand or most significant bit. Error B if Y is greater than 175.
PLOT	88 (58H)	PLOT command. Processes X/Y parameters on the Calculator Stack to BC for plotting of pixel via PLOTBC.
PLOTBC	89 (59H)	Deals with pixel for coordinates in BC (B=Y; C=X). Processes using P_FLAG for Inverse and Over attributes. Updates Attribute File and sets COORDS=BC.
GET_XY	90 (5AH)	Converts a pair of numbers from the Calculator Stack to 2 single byte numbers. Top number goes to B and second to C. D=sign of B and E=sign of C (+1 or -1). Used by PLOT and other routines.

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
CIRCLE	91 (5BH)	CIRCLE command. Calculates successive plot positions from the parameters in the BASIC statement. *
DRAW	92 (5CH)	DRAW command. Calculates successive plot positions from the parameters in the BASIC statement. *
DRAW_L	93 (5DH)	Plots a straight line from current position (COORDS) based on parameters from Calculator Stack (X,Y). *
EXPRN	94 (5EH)	Evaluates expression in BASIC program line (CH_ADD), putting value on Calculator Stack. *
F_SCRN	95 (5FH)	SCREEN\$ function. Matches screen line/col. position (parameters on Calculator Stack) against standard ASCII character set. Returns BC=0 if no find. BC=1 and DE points to Char. Code byte if match found.
F_ATTR	96 (60H)	ATTR function. Returns attribute byte value controlling screen pixel position based on parameters on Calculator Stack (X,Y).
RND	97 (61H)	RND function. Uses value in SEED to generate a pseudo-random number which is placed on the Calculator Stack (Floating Point number).
F_PI	98 (62H)	PI function. Places value of PI on Calculator Stack.

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
F_INKY	99 (63H)	INKEY\$ function. Scans keyboard and puts character code byte in (WORKSP) if key detected. In any case, pushes Regs. AEDCB onto Calculator Stack - BC=0 if no input; =1 if char. code stored; DE=address of char. code byte.
FIND_N	100 (64H)	Find Variable. Searches Variables area for match against identifier pointed to by CH ADD. Adjusts bit NO of FLAGS (Bit 6) for type (1=numeric; 0=string). Also used to find formal parameters for User Defined Functions. *
PSHSTR	101 (65H)	Push String - Clears bit NO of FLAGS and pushes Regs. AEDCB onto Calculator Stack adjusting (STKNXT) upwards. DE contains address of string; BC contains length.
PAEDCB	102 (66H)	Same code as for PSHSTR but preserves state of bit NO of FLAGS (Bit 6).
LET	103 (67H)	LET command. Processes existing or creates new variables. *
POPSTR	104 (68H)	Pop String - Pops end of Calculator Stack ( (STKNXT)-1 through (STKNXT)-5 ) to Regs. BCDEA, adjusting (STKNXT) downwards.
DIM	105 (69H)	DIM statement. Creates or initializes numeric or string arrays. *
STKUSN	106 (6AH)	Stack Unsigned Number - inputs a floating point number onto the Calculator Stack from a series of ASCII characters addressed by (CH ADD). The first character is already in Reg. A (either decimal point, binary token or digit).



TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
STK_A	107 (6BH)	1-byte unsigned integer in A to top of Calculator Stack (binary to floating point). Loads O to B and A to C, then executes STK_BC.
STK_BC	108 (6CH)	2-byte unsigned integer in BC to top of Calculator Stack (binary to floating point).
ININT	109 (6DH)	Converts a series of ASCII digits pointed to by (CH_ADD) into an unsigned floating point integer on the Calculator Stack. First character is in A on entry. Terminates when non-digit found.
FP2BC	110 (6EH)	Pops top of Calculator Stack (floating point number) and puts in BC, rounded to nearest integer. Returns NZ if value is negative. Returns C if number exceeded maximum 2-byte value (65535). Range: -65535 to +65535.
FP2A	111 (6FH)	Pops top of Calculator Stack (floating point number) and puts in A, rounded to nearest integer. Returns NZ if value is negative. Returns C if number exceeded maximum 1-byte value (255). Range: -255 to +255.
OUTPUT	112 (70H)	Outputs number on top of Calculator Stack to currently selected channel via WRCH. (Converts from floating point to ASCII.)

Full explanation of the following Calculator Routines is beyond the scope of this document.

SUB	113 (71H)	Subtract floating point format numbers (HL) minus (DE). (DE) assumed to be (HL) + 5.
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TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
ADD	114 (72H)	Add (HL) + (DE). See SUB.
MULT	115 (73H)	Integer multiply HL * DE. Returns C if overflow.
TIMES	116 (74H)	Floating Point Multiply (HL) * (DE).
DIVIDE	117 (75H)	Floating Point Divide (HL)/(DE).
TRUNC	118 (76H)	Truncates a floating point number (HL) towards zero to an integer. Assumes (DE) = (HL) + 5.
FLOAT	119 (77H)	Converts number (HL) to floating point format. Assumes HL points to an integer in 5-byte format.
INTDIV	120 (78H)	Replaces top two numbers on Calculator Stack (X and Y) by X Mod Y and the integer quotient INT (X/Y). Returns with DE and HL = Calc.Stack Pointers.
INT	121 (79H)	Replaces the top of the Calculator Stack by its integer part. Returns with HL = top of Calc. Stack and DE = next free space.
EXP	122 (7AH)	Replaces the top of the Calculator Stack, X, by EXP(X). Returns with DE and HL = Calc.Stack Pointers.
LN	123 (7BH)	Replaces the top of the Calculator Stack by its natural logarithm. Returns DE and HL = Calc.Stack Pointers.
ANGLE	124 (7CH)	Replaces the top of the Calculator Stack (X) by Y where Y is greater than or equal to -1 and less than or equal to +1 and the $\sin X = \sin (\pi/2 * Y)$ .

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
COS	125 (7DH)	Replaces the top of the Calculator Stack by its COSINE.
SIN	126 (7EH)	Replaces the top of the Calculator Stack by its SINE.
TAN	127 (7FH)	Replaces the top of the Calculator Stack by its TANGENT.
ATN	128 (80H)	Replaces the top of the Calculator Stack by its inverse TANGENT.
ASN	129 (81H)	Replaces the top of the Calculator Stack by its inverse SINE.
ACS	130 (82H)	Replaces the top of the Calculator Stack by its inverse COSINE.
ROOT	131 (83H)	Replaces the top of the Calculator Stack by its Square Root.
TO_THE	132 (84H)	Replaces the top two numbers on the Calculator Stack (X, Y) by $X^{**}Y$ .
RDCH	133 (85H)	Wait for character from currently selected channel (calls INCH). Returns character code in A. See 4.1.1.
SENDCH	134 (86H)	Write character whose code is in A to currently selected output channel. See 4.1.2.
WRCH	135 (87H)	See 3.2.1.1, RESTART 16.
K_SCAN	136 (88H)	Keyboard Scan. See 4.1.1

TABLE 3.3.3-2

TS 2068 FUNCTION DISPATCHER SERVICES  
(continued)

SERVICE	SERVICE CODE	DESCRIPTION
P_LFT	137 (89H)	Backspace. Sets current column position back 1 for selected device. (System Variable updated is S_POSN, SPOSNL, or P_POSN for Screen, Lower Screen or Printer respectively.)
P_RT	138 (8AH)	Outputs a space to currently selected device.
P_NL	139 (8BH)	End-of-Line. Sets current position to start of next line if screen, or outputs printer buffer if printer.
PUTMES	140 (8CH)	Output message to currently selected device. DE points to base of message table which contains variable length ASCII coded messages. The first byte of the table and the last byte of each message must have the most significant bit set. Register A contains the message number, numbered from 0 upwards.
K_CLS	141 (8DH)	CLS command. Executes both CLS and CLLHS.
SCRL	142 (8EH)	Scrolls entire screen (primary display file) up 1 line.
F_PNT	143 (8FH)	POINT function. Processes X,Y parameters from Calculator Stack to BC. Returns unsigned integer value = 0 or 1 on Calculator Stack reflecting state of pixel at coordinates X/Y.
DRAWLN	144 (90H)	Same as DRAW L but enter with BC register containing coordinates, B=Y and C=X.
PUT_LN	145 (91H)	Output Line Number as 4 digits, right aligned and space filled to currently selected output channel. HL points to MSB of 2-byte Line Number.

## 4.0 SYSTEM I/O GUIDE

### 4.1 I/O Channels

The TS 2068 software architecture supports up to 19 I/O Channels or "Streams", numbered from -3 through 15. Those numbered less than 0 are "hidden" or reserved for system use; Channels 0 through 15 are available for assignment via the OPEN # command which has the following format:

OPEN # n,s

where n is the Channel number (0-15) and s is the Device Specification, e.g. "K" (keyboard), "S" (screen) or "P" (printer).

Channels 0 through 3 are initialized at power-on or execution of a NEW command to support the standard system devices and character I/O functions as shown in Figure 4.1-1. Channels 4-15 are considered "Closed". You can re-assign the standard I/O, e.g. OPEN # 2,"P" will direct all PRINT and LIST commands to the 2040 Printer instead of the screen. You can also assign Channels 4-15 and then direct I/O by including the Channel number (or a variable equated to the channel number) in the I/O statement, e.g. PRINT # n. Support for other than the standard system devices described above is not implemented in the original version of the TS 2068 and attempts to OPEN Channels or "Streams" using other than the standard device specifications ("K", "S" or "P") will result in an error message. One possibility for adding BASIC support for new devices is to intercept the I/O error on OPEN and other commands such as CAT and FORMAT via ON ERR and interpret the BASIC program line using your own machine code routines.

	<u>Channel/ Stream #</u>	<u>Device Specification</u>	<u>Command/Function</u>
RESERVED	-3	"K"	Keyboard/Lower Screen
	-2	"S"	Main Screen
	-1	"R"	RAM Write (not used)
	0	"K"	Output to Lower Screen
	1	"K"	INPUT command
	2	"S"	PRINT/LIST commands
	3	"P"	LPRINT/LLIST commands

FIGURE 4.1-1

The Channel architecture is implemented by a number of tables located in both ROM and RAM.

- A. STRMS STRMS is a 38 byte table (2 bytes for each of the 19 channels) located in the System Variables area beginning at 23568 (5C10H). It is initialized at power-on or NEW to the following values:

<u>LOCATION</u>	<u>VALUE</u>	
5C10	0100 (Channel -3)	(Copied from SMINIT in module EDIT of the Home ROM)
5C12	0600 (Channel -2)	
5C14	0B00 (Channel -1)	
5C16	0100 (Channel 0)	
5C18	0100 (Channel 1)	
5C1A	0600 (Channel 2)	
5C1C	1000 (Channel 3)	
5C1E	0000 (Channel 4)	
.	.	
.	.	
5C34	0000 (Channel 15)	

This table is accessed using  $((\text{Ch.}\# * 2) + 16\text{H})$  as an index added to 5C00H. The 2-byte value in the table is an index into the CHANS area of memory which contains the addresses of the I/O routines for the selected channel. If the 2-byte value is zero, the Channel is closed. The STRMS table is modified via the OPEN # and CLOSE # commands. When a Channel is OPENed, the device specification is used to obtain the 2-byte value to be inserted. This value is taken from the table STRMINIT in module EDIT of the Home ROM. When Channels 0 through 3 are CLOSEed, the values are restored to those used at power-on time. All others are cleared to zero.

- B. CHANS The CHANS System Variable at 23631 (5C4FH) contains the address of a 21-byte table initialized at power-on or execution of a NEW command to support "stream" I/O to the four standard system devices ("K", "S", "R" and "P"). Each table entry is 5 bytes long and is indexed by the value obtained from the STRMS table added to (CHANS)-1. Each entry has the following format:

Output Routine Address	2 Bytes
Input Routine Address	2 Bytes
Device Specification	1 Byte

This table is copied from CHINIT in module EDIT of the Home ROM. The last byte of the table contains an 80H which will immediately precede the first line of the BASIC Program (PROG).

Whenever an I/O operation is performed, the appropriate Channel is "selected", i.e. its number is used as an index into STRMS to obtain the offset into the CHANS table. This offset is added to

(CHANS)-1 and the resultant pointer is loaded into the System Variable CURCHL for use by the next character I/O operation (WRCH/RDCH). The device specification from CHANS is used to find and execute the initialization routine in SELTAB.

- C. SELTAB The Select Table is located in the EDIT module of the Home ROM and contains offsets to device dependent initialization routines for the standard devices "K", "S" and "P".
- D. SPEC\_T The Specification Table is located in the CHANS module of the Home ROM and contains offsets to device dependent OPEN routines for the standard devices "K", "S" and "P". It is accessed whenever an OPEN # is executed.
- E. CL\_TAB The Close Table is located in the CHANS module of the Home ROM and contains offsets to device dependent CLOSE routines for the standard system devices "K", "S" and "P". It is accessed whenever a CLOSE # is executed.

The following sections describe the standard system I/O devices supported via Channel I/O.

#### 4.1.1 Keyboard

The low-level routines supporting keyboard input are executed every 1/60 of a second out of the Interruption Handler (Location 56 (38H)). The controlling routine is labelled UPD\_K. This routine calls K\_SCAN to determine if any key(s) are currently being depressed, controls the debouncing and repeat algorithms, calls K\_BASE to determine the Base Code, calls CHCODE to translate the Base Code based on Mode (e.g. "K", "G" or "E" Mode), and finally, stores the resultant keystroke code in LAST\_K and sets the flag KEYHIT. Figure 4.1.1-1 illustrates the mode control variable and associated flags and Figure 4.1.1-2 contains flowcharts of the keyboard support routines.

The character input routine associated with Device Spec. "K" is labeled IN K. The entry address is obtained using the pointer in CURCHL when Channel 1 has been Selected and the Character I/O Input routines RDCH/INCH are executed. The IN K routine tests the KEYHIT flag to detect the presence of input from the keyboard. When the KEYHIT flag=1, the contents of LAST\_K are returned to the requestor.

FIGURE 4.1.1-1  
TS 2068 MODE CONTROLS

<u>System Variable</u>	<u>Location</u>	<u>Description</u>
MODE	23617(5C41H)	<div>Value</div> <div>0 = "K" or "L" Mode</div> <div>1 = "E" Mode</div> <div>2 = "G" Mode</div>
FLAGS	23611(5C3BH)	If MODE = 0 then:  Bit 3 = 0 for "K" Mode = 1 for "L" Mode
FLAGS2	23658(5C6AH)	If in "L" Mode then:  Bit 3 = 0 CAPS Lock Off = 1 CAPS Lock On

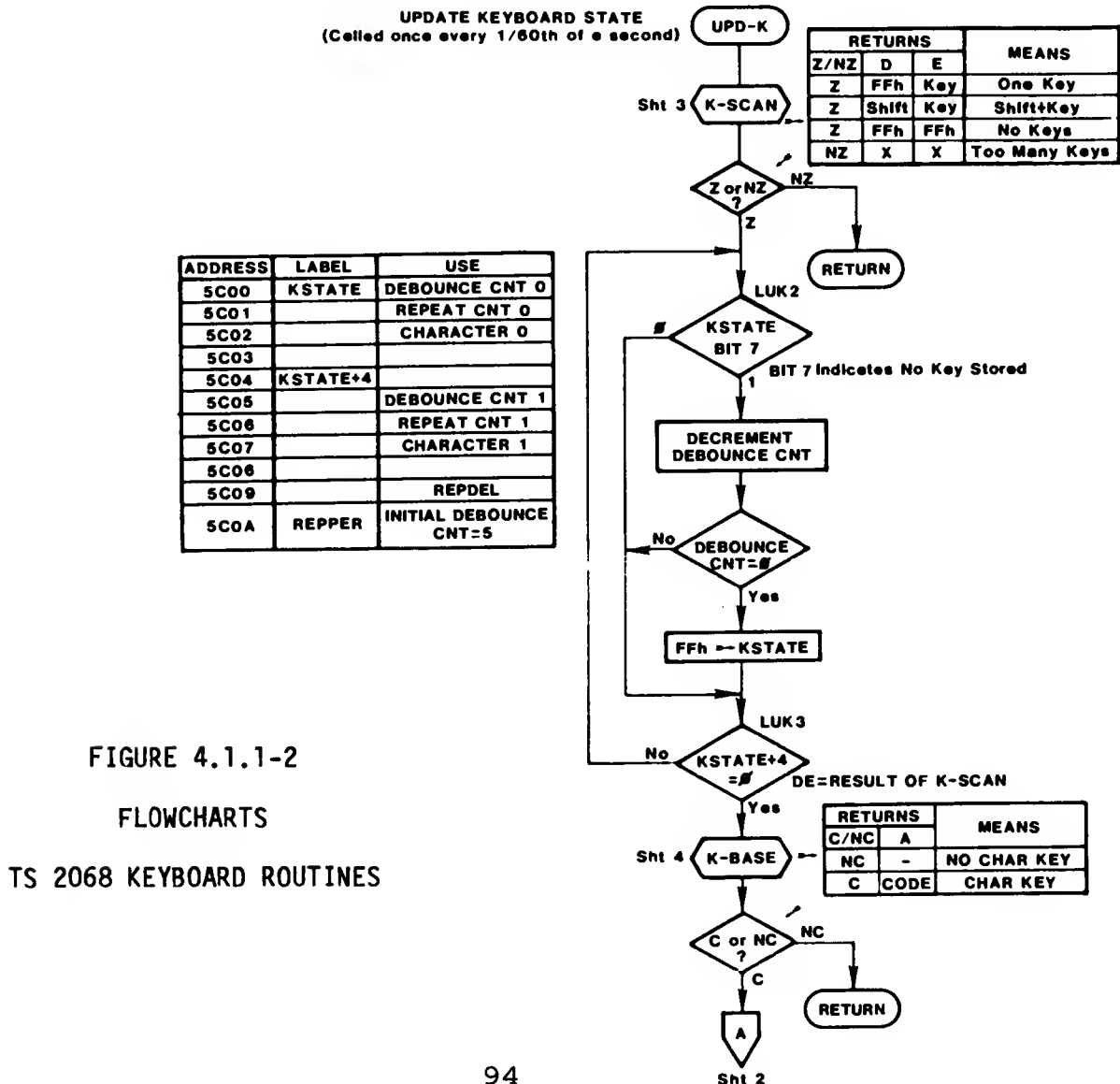
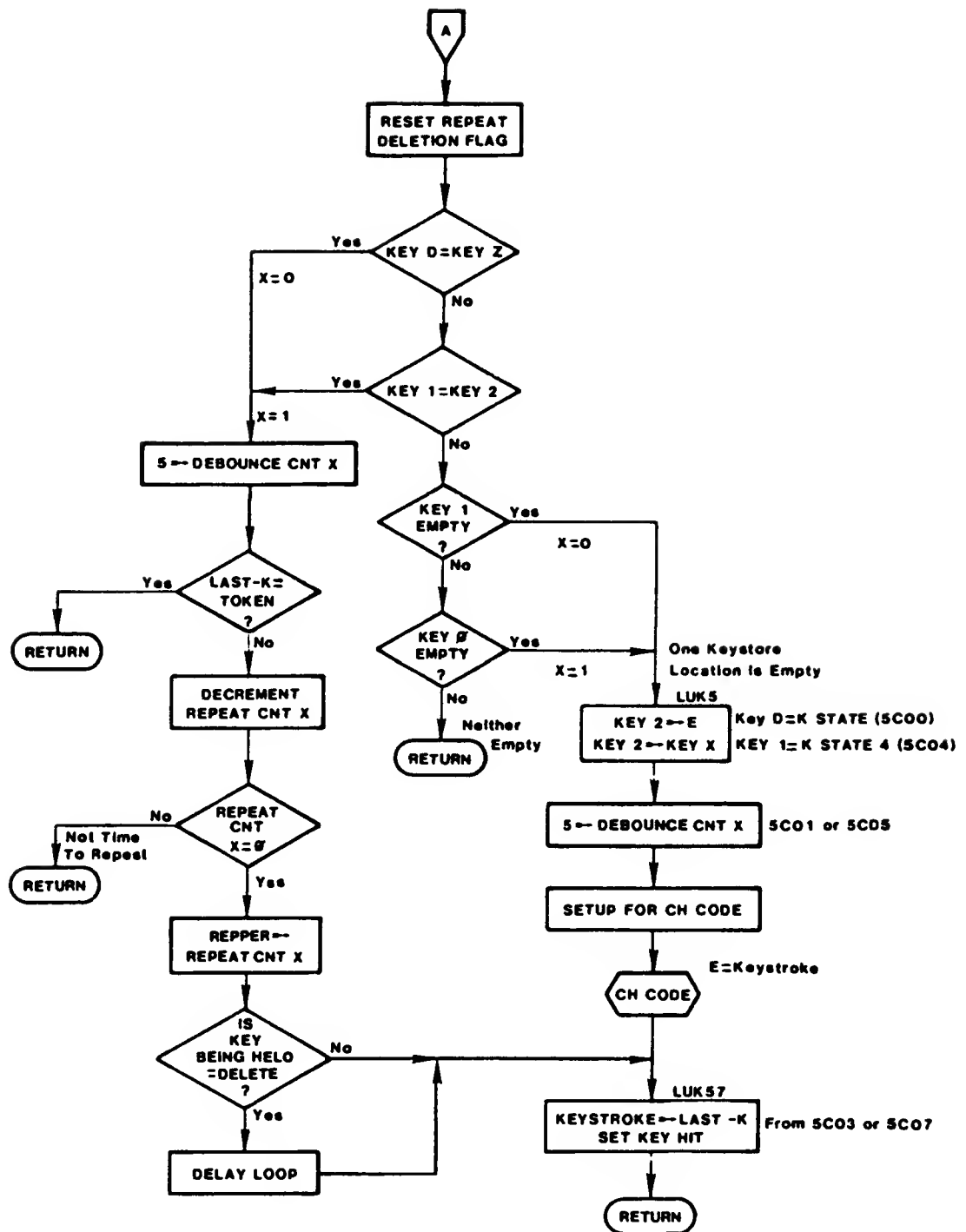


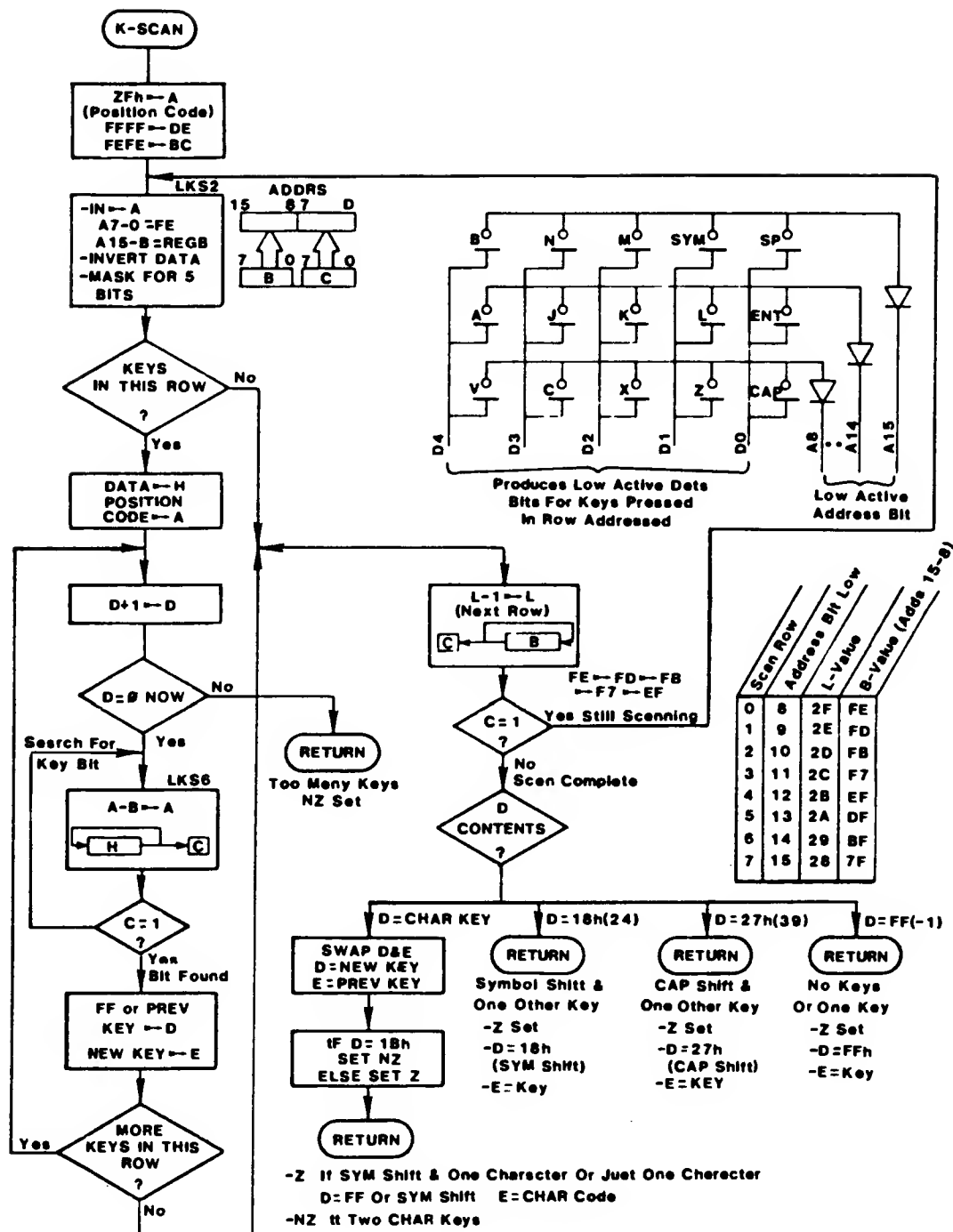
FIGURE 4.1.1-2

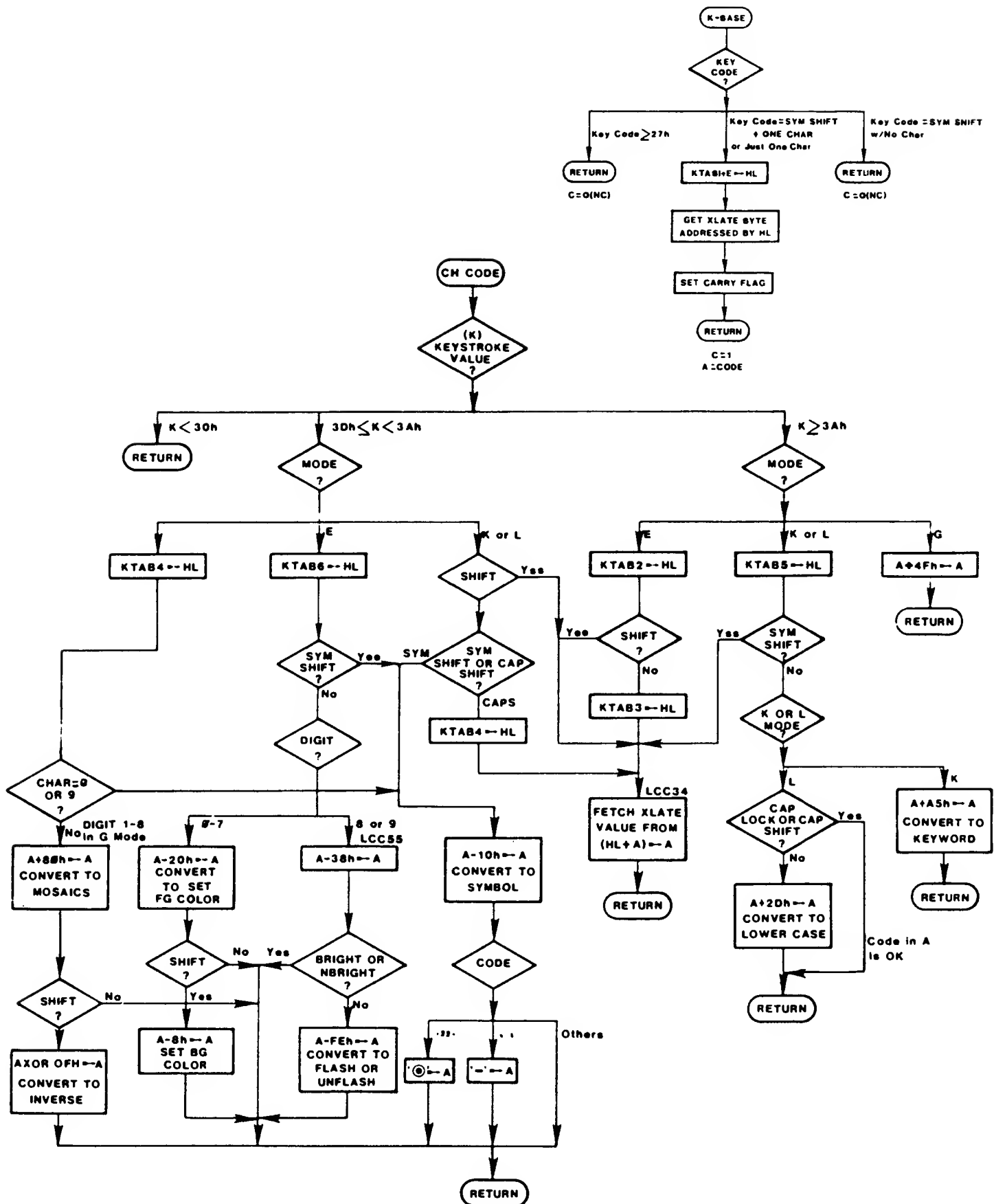
FLOWCHARTS

TS 2068 KEYBOARD ROUTINES









#### 4.1.2 Video Screen

The TS 2068 system software supports I/O in the primary display file only. See Section 2.1.10 for the display file organization. The screen, which is 32 columns X 24 lines, is partitioned into two parts, the main or upper screen (22 lines) and the lower screen (2 lines). The lower portion of the screen is used for output of system messages and to echo input from the keyboard of BASIC commands, BASIC program lines, or data. The lower screen expands as needed for multi-line input, scrolling the entire screen upwards. The variable DF SZ reflects the number of lines in the lower screen (default=2).

Character output to the screen is done using the Channel I/O described in Section 4.1 using device specification "K" for the lower screen and "S" for the upper screen. Each character is defined by an 8 X 8 group of pixels. The 8 bytes needed for each of the 133 characters supported by the TS 2068 are located as shown in Figure 4.1.2-1. Note that by constructing your own pixel data and placing (base address-100H) into CHARS, you can define your own character set.

Associated with each character position is an Attribute Byte controlling the background (PAPER) color, the foreground (INK) color, the intensity (BRIGHT), and whether the position is constant or alternates between true and inverse video (FLASH). Two other "attributes", OVER and INVERSE, are implemented by software at the time the character(s) are placed into the display file.

FIGURE 4.1.2-1

#### TS 2068 STANDARD CHARACTER TABLES

<u>Character Set</u>	<u>No.of Chars.</u>	<u>Char.Codes</u>	<u>Location</u>
Standard	96	32-127 (20-7FH)	Home ROM (3D00-3FFFH) (Address-100H in CHARS)
Std.Graphics	16	128-143 (80-8FH)	Dynamically Generated by Software
User Defined Graphics	21	144-164 (90-A4H)	Home RAM (Address in UDG)

The screen output routine, SENDTV, is in Module IO\_1 of the Home ROM. This routine is used for output to both the screen (upper and lower) and the dot matrix printer. The following sequence illustrates the major operations involved in executing a PRINT "A" statement:

1. Channel 2 is Selected (normal assignment assumed)
  - loads CURCHL with pointer into CHANS area for Channel 2 (first 2 bytes are address of Output Routine - SENDTV).
  - clears printer and lower screen flags
  - sets ATTR\_T to values based on ATTR\_P (current "permanent" attribute values are transferred to the system variable used by the screen output routine). If the PRINT statement contained temporary attribute controls, they would override the settings established via Select.
2. The character code for "A" (65/41H) is placed in Register A and a RESTART 16 (10H) is executed (WRCH). This jumps to SENDCH in module EDIT of the Home ROM which passes control to the SENDTV routine based on (CURCHL).
3. The registers are loaded from the System Variables with the current Row/Column position (S\_POSN) and Display File address (DF\_CC) for the main screen.
4. The character code is determined to be from the standard character set so the registers are loaded with the address from CHARS and the offset to the pixel pattern for "A" is calculated using the character code X 8 (shift left 3 places).
5. The first pixel row (8X1) from the character table is copied to the display file. The character table address is incremented by 1 and the display file address is incremented by 256 (100H). The next pixel row (8X1) is copied to the display file. This process is repeated until the 8 pixel rows have been copied. Masking of the data going into the display file is done based on the flags from P\_FLAG thus controlling the OVER and INVERSE attributes.
6. The attribute byte controlling the character position just written is updated based on the value in ATTR\_T and other flags.

7. The variables S POSN and DF CC are updated to reflect the next screen position and return is made from the WRCH operation.

In the above sequence, if the print position for the "A" had started a new line following the 22 lines of the main screen, the SCROLL? prompt would have been outputted to the lower screen and, assuming a positive response, the upper screen would be scrolled up 1 line, a blank line inserted at the bottom of the upper screen, and the "A" printed at the start of the new line.

Graphics I/O using pixel coordinates is supported in the primary display file by the PLOT, DRAW and CIRCLE commands. The Home ROM module GRAPHS contains the major routines which implement these commands. They are limited to the 22 lines of the upper screen (256 X 176 pixels).

Figure 4.1.2-2 shows the internal representation used to designate row (line) and column positions. See Section 2.1.10 for details on the organization of the Display Pixel and Attribute Files. See Section 5.2 for details on software support necessary for the advanced video modes.

FIGURE 4.1.2-2

DISPLAY FILE ROW/COLUMN NOTATION

BASIC Parameters		Internal Representation	
Line/Row	0	24 (18H)	UPPER SCREEN
	1	23 (17H)	
	.	.	
	.	.	
	21	3	
	-----	-----	
	22	2	LOWER SCREEN
	23	1	
Column	0	33 (21H)	
	1	32 (20H)	
	.	.	
	.	.	
	31	2	

#### 4.1.3 2040 Dot Matrix Printer

Character output to the 2040 Printer is handled by the same routine used for the screen, SENDTV. When the Printer Flag=1, set by initialization for device "P", the pixel data is written into the Print Buffer instead of into the Display File. There is no Attribute Byte. The "attributes" OVER and INVERSE which are software controlled can be active. Since the Print Buffer is always precleared to zeros, OVER has no effect. INVERSE works exactly as it does for the screen, i.e. INK pixels are zero and PAPER pixels are 1.

The Print Buffer is located at 23296 (5B00H) and is 256 (100H) bytes long, the data needed to print one line of 32 characters, each character comprised of 8 bytes (8 X 8 pixels/character). The buffer is cleared to zeros and the flag PRLEFT set to zero at power-on time (or execution of a NEW command). The PRLEFT flag is set to 1 whenever pixel data is written to the buffer. This flag is used when exit is made from a program to print any unprinted data prior to program termination. As the pixel data for a particular character is entered into the buffer, the buffer address is incremented by 32 (20H); the sequential data in the buffer therefore represents 8 complete scan lines of 32 characters. When the Print Buffer is full, or upon processing an End-of-Line (ODH), or at program termination, the contents of the buffer are written to the Printer, the buffer is cleared and the PRLEFT Flag is set to zero.

Printer I/O is done via Port OFBH, but the Printer responds to any I/O Read/Write with Address Bit 7=1 and Address Bit 2=0. Therefore, any Port providing this combination, e.g. Ports OFA through OF8 and Ports OF3 through OF0 as well as others, will interface to the Printer. See Section 2.1.13.3 for the bit definitions for Printer I/O. The pixel data is written to the device by the routine PRSCAN in module IO\_2 of the Home ROM which outputs 1 scan line (32 bytes), one bit at a time on each call to the routine.

There are two controlling routines for output to the printer. DUMPPR is called from SENDTV based on buffer full or End-of-Line control. This routine will call PRSCAN 8 times to output the 256 bytes of the Print Buffer (8 scan lines). The other routine is K DUMP which implements the COPY command. This routine calls PRSCAN 176 times to write the contents of the primary display file for the main screen to the printer (8 X 22). All of the low level print routines are in module IO\_2 of the Home ROM.

## 4.2 Cassette Tape

Tape I/O is done via Port OFEH. An I/O read of Port OFEH pulls in the cassette input on Bit 6. An I/O write of Port OFEH Bit 3 controls the tape output with Bit 3 = 1 generating a high output and Bit 3 = 0 generating a low output.

Data is written to the tape under software control creating the following frequencies and format:

- Sync Pattern of 4032 cycles at 806.5 Hz. (5 sec.)
- Header: 17 bytes of data identifying the following data block as either Program, Number Array, Character Array, or Binary Code and containing other control information.

The header is written as Data, i.e. the Most Significant Bit first in each byte, 1 cycle at 2040 Hz. for a Zero and 1 cycle at 1020 Hz. for a One. The first byte is zero identifying the header. The final byte is a Checksum calculated by XOR of all preceding data bytes.

- Software delay of approximately 835 milliseconds.
- Sync Pattern of 1612 cycles at 806.5 Hz. (2 secs.)
- Transition Pattern of 1 cycle at 2400 Hz.
- Data Block: Written as Data (see above) with first byte = -1 (FFH) and a final Checksum byte.

Figure 4.2-1 shows the header formats for the various types of data.

The routines used to actually write and read the tape (W\_TAPE and R\_TAPE) are in the TAPE Module of the Extension ROM (see map in Appendix A). They are accessible via the Extension ROM Interface Routine listed in Figure 3.2.2-2. The general flow required to write a header and data block is:

1. Call W\_TAPE with A=0. IX contains the address of the header and DE contains the length.
2. Delay loop approximately 1 second.
3. Call W\_TAPE with A=FFH. IX contains the address of the data block and DE contains the length.



The R\_TAPE routine performs either a LOAD (transfers data from tape to memory) or VERIFY (compare data from tape against data in memory) operation, based on the status at entry: Carry Set for Load and No Carry if Verify. As for the Write, A=Block Type (0 for Header and -1 (FFH) for Data Block). IX contains the memory address.

The tape routines return Carry=1 for successful completion and No Carry for error or Break Key detected. Both W\_TAPE and R\_TAPE exit via the routine W\_BORD which restores the Border color based on bits 3-5 of the system variable BORDCR. If the Break Key is detected during this exit routine, a RESTART 8 (ERROR) is executed.

NOTE: The write to Port OFEH in the exit routine restoring the Border Color has bit 3 = 0. This creates a final transition on the tape following a write operation. This transition is necessary in order to successfully read back the final data bit from some tape recording devices. If you are calling the W\_TAPE routine so as to bypass the normal exit path, you must perform this final write to Port OFEH with Bit 3 = 0 within a similar timeframe.

Addendum to R\_TAPE routine: Register DE must contain the length of the block to be read (DE=17 for the Header, and DE=HDLEN for Data). See Fig. 4.2-1 for a definition of HDLEN.

FIGURE 4.2-1

## TAPE HEADER FORMATS

	HDDTYPE(1)	HDNAME(10)	HDLEN (LSB/MSB)	HDADD (LSB/MSB)	HDVARS (LSB/MSB)
PROGRAM	0	Up to 10 ASCII Chars.	Length of Program + Variables (E LINE - PROG)	Starting Line No. or 8000H E.G.: 0500=Line 5 or 0080H if no Line No.	Length of Pro- gram = Offset to Variables) (VARS) - (PROG)
NO.ARRAY	1	"	Length Field from Data Structure	LSB=00 MSB=Array ID 7.....0 100 (ASCII - 60H)	N/A(=0)
CHAR.ARRAY	2	"	Length Field from Data Structure	LSB=00 MSB=Array ID 7.....0 110 (ASCII - 60H)	N/A(=0)
CODE (BINARY)	3	"	Length Specified in SAVE	Address Specified in SAVE	N/A (=0)

## 4.3 Joysticks

The two joysticks are controlled via Register 14 (I/O Port A) of the Programmable Sound Generator Chip (see Sections 2.1.6 and 2.1.7). Address and data are passed via Ports 0F5H and 0F6H respectively. The joysticks are read by first addressing Register 14 in the PSG by writing a 14 (0EH) to Port 0F5H. The data is then read by executing an IN from Port 0F6H, having the port address in Z80 Register C and the joystick (player) number in Register B (number = 1 or 2). Note that PSG Register 7, Bit 6 is assumed to be zero, enabling I/O Port A for input. If you ever use I/O Port A for output (R7,B6=1), you will want to clear Bit 6 prior to any input operation.

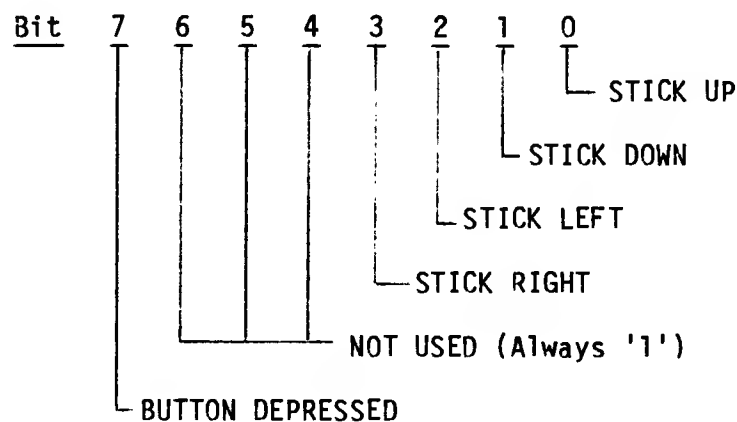
Sample routine:

GETJOY	LD	A,0EH	Load A = 14
	OUT	A,(OF5H)	Address the joystick port
	LD	B,playerno	
	LD	C,OF6H	Data Port address to C
	IN	A,(C)	Joystick data to A
	CPL		Complement to High Active
	AND	8FH	Get significant bits

The data read is LOW ACTIVE, i.e. all bits = 1 (byte=FFH) when the stick is at center and the button is not depressed. Figure 4.3-1 shows the interpretation of the data byte.

FIGURE 4.3-1

#### JOYSTICK DATA



#### 4.4 S/W Generated Sound (BEEP)

The BEEP command produces sound using the speaker by toggling Bit 4 of I/O Port OFEH to generate a signal of a calculated frequency and duration based on the command parameters. It uses the routine PARP which takes as input two parameters, one defining the period of the signal (HL) and the other defining the number of cycles to be generated (DE) and outputs DE+1 cycles of a tone having the period  $8N+235$  to  $8N+246$  T-States where  $(HL) = N$ . Both the BEEP and PARP routines are in the K\_SCAN module of the Home ROM. The PARP routine is also used to generate the keyboard "click" and the "raspberry" which can be varied by modifying the values in the system variables PIP (23609/5C39H) and RASP (23608 5C38H).

#### 4.5 Sound Chip (SOUND)

The SOUND command writes the first parameter (register number) to Port OF5H (address to Programmable Sound Generator) and the second parameter (load data) to Port OF6H (data to PSG). The program line is scanned for multiple parameter pairs and continues writing address/data pairs to the PSG until the end of the statement is reached. See Section 2.1.6 for details on the hardware of the PSG.

## 5.0 Advanced Concepts

### 5.1 Cartridge Software/Hardware

#### 5.1.1 LROS

An LROS is identified by the following overhead bytes:

<u>Location</u>	<u>Description</u>
0000	Not Used
0001	Cartridge Type 01=LROS
0002/0003	Starting Address (LSB/MSB)  Address to be jumped to after Operating System initialization is complete. Order of bytes is as for a JP instruction.
0004	Memory Chunk Specification. Bits 0-7 represent Chunks 0-7 respectively in the Dock Bank in <u>low active</u> format:  0 if in use 1 if not in use

NOTE: When writing to the Horizontal Select Register (Port F4H), the Chunk Specification is High Active

The Memory Chunk Specification is used to enable the specified chunks in the Dock Bank prior to jumping to the address specified in Location 2 and 3. Control is transferred from the Initialization code in the Extension ROM via the GOTO BANK routine in Home Bank RAM Chunk 3, therefore Bit 3 of the Memory Chunk Specification must be set to 1 in order for the transfer to be accomplished as designed (Chunk 3 also contains the Machine Stack).

CAUTION: If Chunk 3 is marked for use in the Dock Bank, then when the Memory Chunk Spec. is written to Port F4H by the Bank Enable code, execution will continue from that point in Chunk 3 in the Dock Bank with the Stack Pointer addressing ROM.

An LROS is Z80 machine code and is in complete control of the TS 2068 hardware after transfer to the starting address has been made. It can directly implement an application, or it

can support multiple applications by implementing a language other than BASIC. An AROS dependent on such an LROS would have to be part of the same cartridge since there is only one cartridge connector.

Interruption Mode 1 has been set by the TS 2068 and interruptions are enabled prior to passing control to the LROS starting address, therefore the LROS must contain appropriate code at location 56 (38H) to cover the case where the interruption occurs after Chunk 0 in the Dock Bank has been enabled, but before any action by the software cartridge to disable the interruption has been taken. Once control is transferred, the LROS may then disable the standard TS 2068 interruption by setting bit 6 of Port FFH, mask the interruption by executing a DI instruction, or set a different Interruption Mode. It may change the location of the Machine Stack. It may also change the memory selection by writing to Port OF4H with each bit set to 1 for the corresponding chunk to be enabled in the Dock Bank (high active format) or 0 to be enabled in the Home Bank. Thus, an LROS may contain code in Chunk 3, but it should be enabled after the OS RAM code has finished execution.

Now that your LROS is in the driver's seat, you are on your own! Some important points to remember when mapping your Dock Bank memory and doing bank switching are:

1. The Display RAM is in Home Bank Chunk 2 for the primary display file and Chunk 3 for the second display file. This memory is accessed independently by the video hardware. The software only needs to enable it when actually reading or writing it.
2. The Dock Bank and Extension ROM Bank are mutually exclusive since they share the Horizontal Select Register in Port F4H. You will need a routine in the Home Bank RAM to do any switching between the two. You must also be careful to have the appropriate Home Bank Chunks enabled which are referenced by the Extension ROM code, e.g. the System Variables in Chunk 2 or possibly the bank switching code in Chunk 3.
3. Some interesting switching routines can be constructed by having parallel code in shadowing chunks of memory to take advantage of the "instant" switch in execution from one bank to another when the memory selection is made. E.g., a routine in the Dock Bank ROM in Chunk 6 could push a Home Bank address on the stack, write to Port F4H enabling Chunk 6 and any other desired chunks in the Home Bank (by deselecting them in the Dock), and have code at the next sequential instruction address in Home Bank RAM Chunk 6 to continue the path. A Return

instruction, for example, would pass control to the address on the stack. Code to switch memory back to the Dock Bank could be mapped in a similar way.

4. If you plan to use any of the System software routines, unless you know otherwise it is probably necessary to maintain the contents of Home Bank Chunks 2 and 3 intact (and Chunk 7 if the OS RAM routines have been relocated). The system routines rely heavily on the System Variables and assume that any pointers in them are pointing to the Home Bank. See Section 3.3.4.1 for details on using the RAM Interruption Handler and Section 6.0 for known corrections when using System S/W.
5. If you design an LROS implementing a higher-level language and want to support an AROS application, you must design your own initialization code to detect the presence of such an AROS. The TS 2068 will not look for the presence of an AROS if an LROS is present, therefore there will be no entry for the AROS in the System Configuration Table. Note that since there is only one cartridge connector, such an AROS would also have to be integrated with the supporting LROS in a single cartridge or cartridge board.

### 5.1.2 AROS

An AROS is identified by the following overhead bytes:

<u>Location</u>	<u>Description</u>
32768 (8000H)	Language Type 1 = BASIC [and machine code] 2 = Machine code only (Any other value will result in Error S, Missing LROS)
32769 (8001H)	Cartridge Type 2 = AROS
32770/32771 (8002/8003H)	Starting Address(LSB/MSB) BASIC AROS = Addr. of First Program Line  Machine Code AROS = Addr. of First Z80 Instruction
32772 (8004H)	Memory Chunk Specification Bits 0-7 represent Chunks 0-7 respectively in the Dock Bank in <u>low active</u> format as follows:  0 if in use 1 if not in use  NOTE: Bits 0-3 must be set to 1 for proper execution.
32773 (8005H)	Autostart Specification 0 = No Autostart 1 = Autostart
32774/32775 (8006/8007H)	Number of bytes of RAM to be Reserved for Machine Code Variables (LSB/MSB - 0100H=1 byte Reserved; 0002H=512 bytes Reserved.

#### 5.1.2.1 BASIC AROS

A BASIC AROS is supported by special code in the System ROM (Section 3.2.1.2). The portion of the cartridge containing BASIC program lines is restricted to the upper half of the memory space beginning at location 32776 (8008H) in the Dock Bank. Support for User-Defined Functions, which requires searching for

the definition parameters within the program, is not implemented. Also, because the support code interfaces directly to the bank switching code in Home RAM Chunk 3 (does not allow for it to be relocated to Chunk 7), a BASIC AROS cannot utilize the advanced video modes and also execute BASIC program statements. If the cartridge contained machine code supporting advanced video modes, the TS 2068 would have to be returned to "Normal" video mode with the RAM mapped accordingly (see Figure 1.1-3) if control were to be returned to the BASIC Interpreter USR code.

Since execution of the cartridge BASIC program is done by copying program lines to a buffer in the Home Bank RAM (ARSBUF), the most efficient cartridge execution is obtained by making program lines as large as possible, i.e. making use of the multi-statement feature of the TS 2068. The reverse is true concerning execution of READ commands. An entire DATA statement is copied to the Home Bank RAM, but only the current item is accessed. It therefore will be more efficient to not make DATA statements excessively long. The BASIC program lines appear in the cartridge in exactly the same format used in the RAM, i.e. Line Number (2 bytes), Length (2 bytes), Command Token, etc. terminated by an Enter (ODH). Numerical constants appearing in a program line are followed by the CHR\$ (OEH) byte and 5-byte floating point format described in the User Manual (see Appendix C of the TS 2068 User Manual). The Variables area is built in the RAM (address in VARS) exactly as though the program were in the RAM. All variables, including arrays, are built at the time of program execution - there is no provision for copying or accessing pre-defined variables from the cartridge, however, see Section 5.3.2. The last program line must be followed by a terminator byte having the Most Significant Bit set (e.g. 80H), otherwise the Interpreter cannot detect the end of the program.

A BASIC AROS may contain machine code accessed via the USR function. If the machine code address is within the memory designated by the AROS Memory Select Specification as "in use", the Dock Bank will be enabled, otherwise the machine code address is assumed to be in the Home Bank. (See Section 6.0 for details on known problems in this area of the code.) Obviously, once control is transferred to the machine code in the AROS, the ball is now in your court. You could have additional machine code residing in the lower half of the Dock Bank memory space which you can now switch in. You only have to know what you're about. If and when you are ready to go back to



executing your BASIC program, you must enable Chunks 0-3 in the Home Bank and have the stack and other Home Bank RAM in the proper state for return to the USR function code in the BASIC Interpreter, i.e. what it was when the USR function passed control to you.

The Autostart feature begins execution out of the BASIC AROS immediately after system initialization. If the Autostart parameter is zero, control will go to the BASIC Interpreter as if there were no cartridge installed, although internal flags have been set noting that a BASIC AROS is present. The cartridge will be started when you execute a RUN or GOTO Line Number command.

The final parameter in the overhead bytes allows you to reserve RAM beginning in Chunk 3 at Location 26688 (6840H) for machine code and/or machine code variables. The designated number of bytes are reserved by the AROS support code prior to beginning program execution. The AROS buffer (ARSBUF) begins immediately following this reserved area (see Fig. 1.1-3). Note that this area is part of the RAM that gets relocated if the second display file is opened. Therefore access to your machine code and/or variables should be conditional on the video mode rather than direct if you are going to be using the advanced video modes. This reserved area begins at 31488 (7B00H) when the second display file is open. Remember -- use of the second display file and execution of BASIC program from the cartridge are mutually exclusive.

The standard technique of reserving space for machine code by modifying RAMTOP could also be used to place machine code/variables at the top of the Home Bank RAM. If you place code above (RAMTOP) which is to be accessed via the BASIC USR function, the affected memory chunk(s) cannot be marked as "in use" in the cartridge in the AROS Memory Selection Specification.

#### 5.1.2.2 Machine Code AROS

A machine code AROS is similar to an LROS with the exception that it is dependent on the System ROM for interruption handling if the interruption is enabled. This implies that Chunks 0-3 are enabled in the Home Bank.

The Autostart parameter should be set to 1 since if it is zero, control will be passed to the BASIC Interpreter as if the cartridge were not present. There is no BASIC command to directly start execution of a Machine Code AROS.

Because of a "bug" in the Initialization code handling a Machine Code AROS, the parameter specifying the number of bytes to be reserved for machine code variables must be adjusted by adding 21 (15H) to the actual number of bytes needed. This preserves the 21 byte CHANS area starting at 26688 (6840H). The reserved area then starts at 26709 (6855H) (or 31488 (7B15H) when the second display file is open). Access to the variables should be conditional based on the video mode rather than direct if you plan to use the advanced video modes. If you do not plan to utilize any of the system software, you can disregard the above and "do your own thing" with the RAM.

See Section 6.0 for known corrections when using System S/W.

### 5.1.3 EPROM Cartridge Board Application

Figure 5.1-1 provides the logic diagram for a pluggable EPROM cartridge board capable of configuring up to four 16K-byte (128K-bit) EPROM's of the 27128 type. The artwork for the PC board implementing that logic diagram is provided in Figures 5.1-2, 5.1-3 and 5.1-4 for the Component Side art, the Solder Side art, and the Solder Mask (one common mask for both sides), respectively.

See Section 2.4.2 for mechanical details of the connector portion of the PCB.

FIGURE 5.1-1  
PLUGGABLE EPROM CARTRIDGE BOARD  
LOGIC DIAGRAM

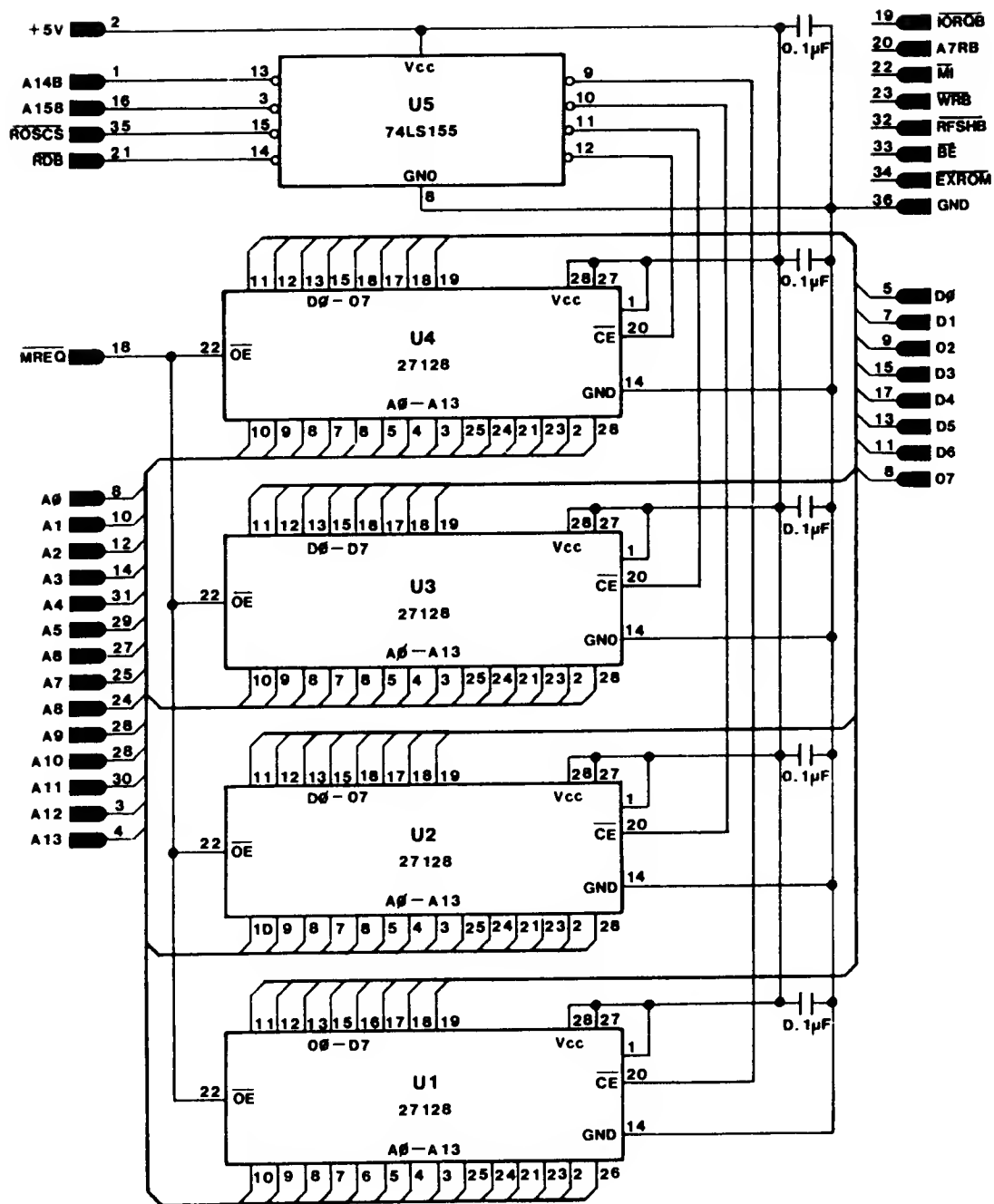


FIGURE 5.1-2  
EPROM CARTRIDGE BOARD  
COMPONENT SIDE ARTWORK

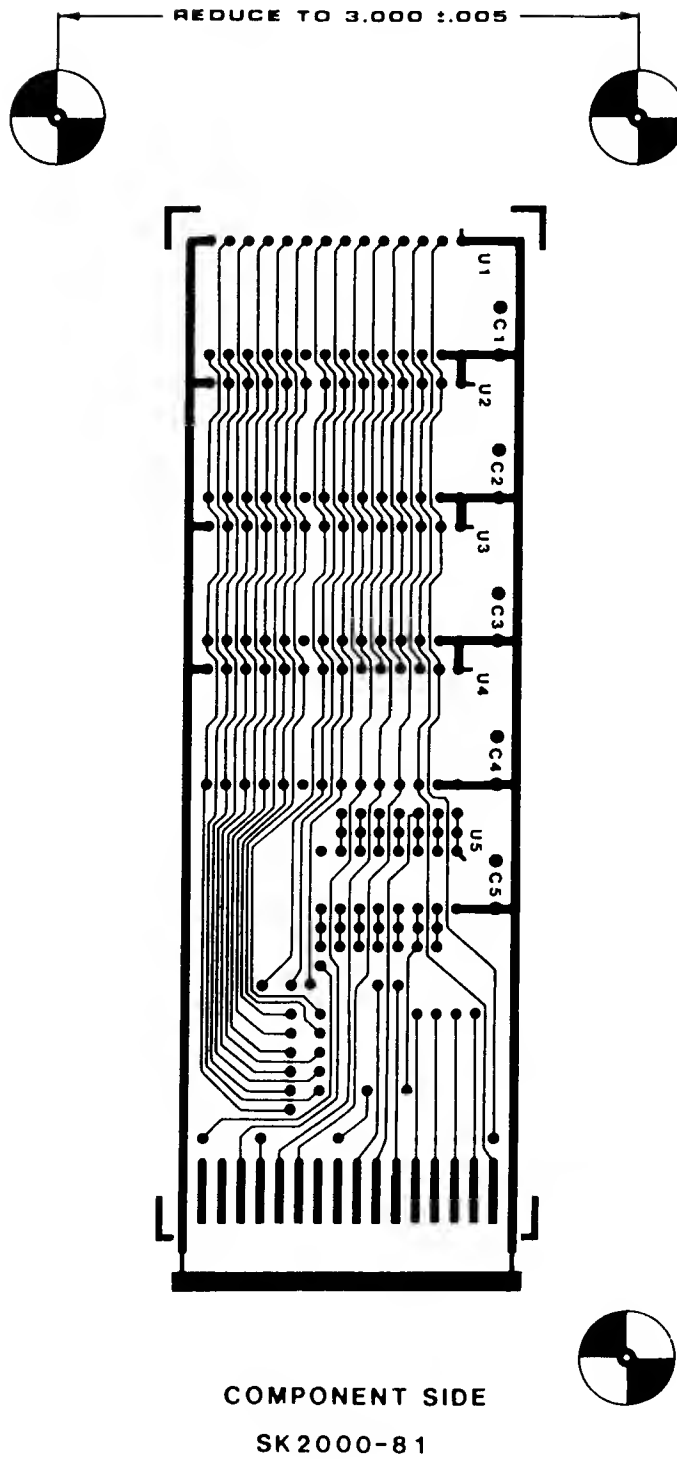
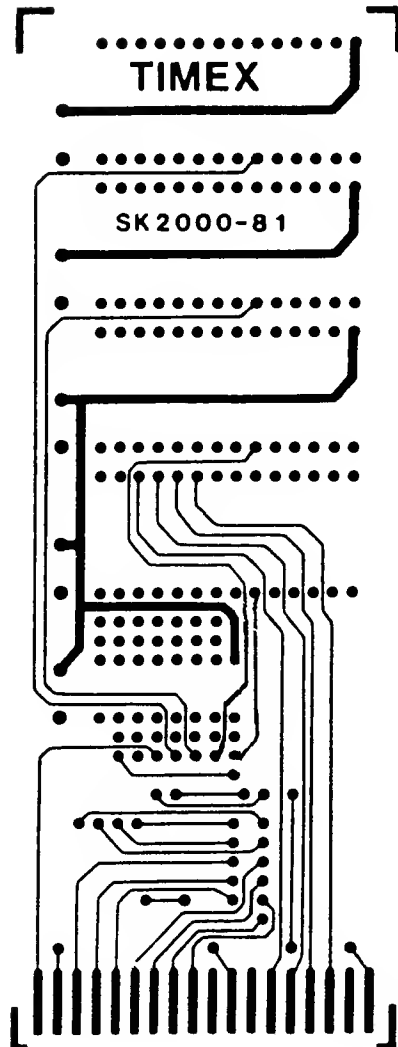
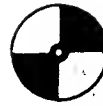
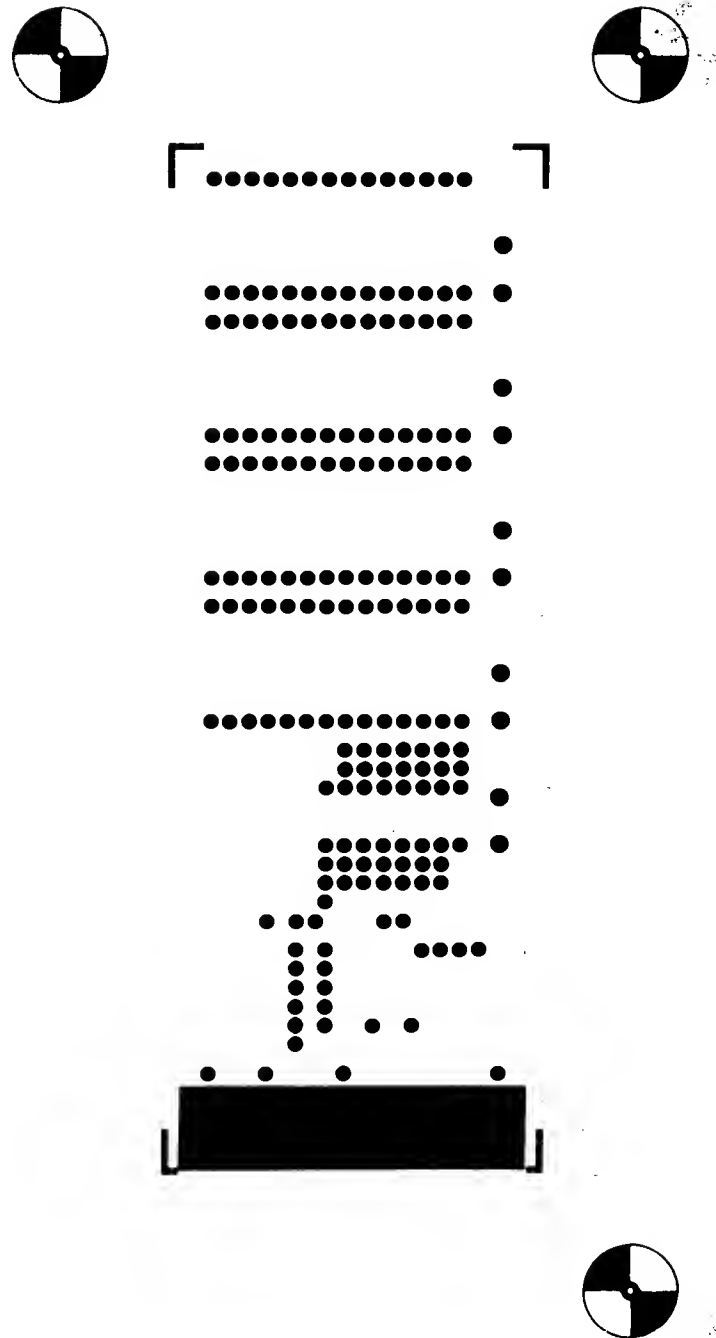


FIGURE 5.1-3  
EPROM CARTRIDGE BOARD  
SOLDER SIDE ARTWORK



SOLDER SIDE  
SK 2000-81

FIGURE 5.1-4  
 EPROM CARTRIDGE BOARD  
 SOLDER MASK



SOLDER MASK  
 SK2000-81

## 5.2 Advanced Video Modes

The following sections describe the various video modes available on the TS 2068 and the major software support functions necessary. See Sections 3.2.2.3 and 3.2.2.4 for details on using the Video Mode Change Service. Appendix C contains descriptions and code listings for a number of software packages developed by Timex that support various screen modes and applications. Reference to these packages should aid in gaining an understanding of the software techniques needed to support the video mode hardware.

The TS 2068 video mode hardware works out of two areas of RAM, the primary display file at 4000H and the second display file at 6000H. Each area consists of 6912 (1B00H) bytes used for pixel and/or attribute data based on the mode selected via bits 0-5 of Port FFH. The pixel data area divides into three blocks, each supporting 8 contiguous lines on the screen. See Section 2.1.10 for details on organization of the display RAM. Because the two display files occupy the same relative positions within their respective 8K Chunks, by setting/clearing Address Bit 13 a software routine can address the corresponding location in each file:

Address Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00	
	0	1	0	X	X	X	X	X	X	X	X	X	X	X	X	X	DF1
	4000 - 5AFFH (Bit 13 = 0)																

Address Bit	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00	
	0	1	1	X	X	X	X	X	X	X	X	X	X	X	X	X	DF2
	6000 - 7AFFH (Bit 13 = 1)																

In order to display a character on the screen, 8 bytes of pixel data must be entered into the display file, one for each scan row. For a particular character position, the scan rows are 100H bytes apart. E.g, the 8 bytes of pixel data for position Line 0/Column 0 are located at 4000H, 4100H, 4200H,.....,4700H. Since this is the first character position on the screen, its Attribute byte, in Normal Mode, is the first byte in the Attribute File which starts at 5800H. The 768 (300H) Attribute Bytes are in sequential order starting at position 0/0 through 0/31, 1/0 through 1/31, and so forth, ending with 23/0 through 23/31.

One method of determining the starting display file address for a particular line/column position is to build a table containing the starting address of each of the 24 lines (2 bytes per entry). Then construct an algorithm that takes the

line number and forms an index by multiplying it by 2 (shift left 1), add the index to the base address of the table, and read out the display file address. The column position is then simply an offset added to this address. By testing VIDMOD (23746 - 5CC2H) you can determine whether to set Bit 13 for the second display file, e.g. because you are in an odd column in 64-column mode, or simply because you are using the second display file in dual screen mode.

The following example illustrates this method. The table entries are in Hex:

TABLE				
LINE #	INDEX	LSB/MSB		
0	0	00 40	4000H =	Line 0 (Top of Screen)
1	2	20 40		Line 1
2	4	40 40		Line 2
.	.	(+20H)		
.	.	(+20H)		
7	14(0EH)	E0 40		Line 7 (End of Upper Block)
8	15(10H)	00 48	4800H =	Line 8 (Top of Middle Block)
9	18(12H)	20 48		Line 9
.	.	(+20H)		
.	.	(+20H)		
15	30(1EH)	E0 48		Line 15(End of Middle Block)
16	32(20H)	00 50	5000H =	Line 16(Top of Bottom Block)
17	34(22H)	20 50		Line 17
.	.	(+20H)		
.	.	(+20H)		
23	46(2EH).....	E0 50		Line 23(End of Bottom Block)

Line 17, Column 23 (11H/17H) would yield a display file address of 5020H + 17H = 5037H. If VIDMOD indicated the second display file was to be used, setting Bit 13 of the address would yield 7037H. If we were using 64-column mode, because the column is odd (Bit 0=1) we would set Bit 13 of the starting line address getting 7020H, then divide the column address by 2 (shift right 1) since there are only 32 columns in each display file. This would give us an offset of 11 (0BH) which added to the starting address results in a display file address of 702BH. Having the display file address, we now insert the 8 bytes of pixel data for the character desired, incrementing the display file address by 100H between each write (this is easily done by simply incrementing the upper register of the register pair containing the address). The following routine is a simplified version illustrating this process. It assumes that Reg. Pair DE contains the address of the desired character in the character table and that HL contains the address of the desired position in the display file.



```

      .
      .
      .
      LD      B,8          Set Scan Count
LOOP  LD      A,(DE)       Get pixel pattern
      LD      (HL),A       Write to Display File
      INC     DE           Next pixel pattern byte
      INC     H            Next DF Position (+100H)
      DJNZ    LOOP        Continue for 8 Scan Rows
      .
      .
      .

```

Finally, we must update the Attribute Byte controlling the updated character position. The following sample algorithm will formulate the Attribute File address given the address of any of the scan rows of the character position. We will assume we have saved off the starting display file address and now have it in Register Pair HL.

```

GETATT  LD      A,H        MSB of DF Address
        RRCA             Shift right circular
        RRCA             to get Bits 3&4 (Block #)
        RRCA             to positions 0&1
        AND      3        Clear other bits
        OR       58H       OR in Attr.File Base Adrs.
        LD      H,A       Update MSB

```

NOTE: The LSB is the same as  
for the pixel data.

Using our first example, with a Display File address of 5037H, the Attribute File address would be 5A37H. The second example was using 64-Column Mode which does not require attribute file update (attributes determined by video mode setting).

See Section 5.2.2 for a sample algorithm to formulate the display file address for X,Y pixel coordinates. The above routine for calculating Attribute File address would be substituted for the method used in the example if not working in High Resolution Graphics mode.

In addition to data insertion, two major screen support functions are scrolling and clearing the screen. Scrolling is done in the System ROM by copying the entire display file data and attribute controls up one line position (Line 1 to Line 0, Line 2 to Line 1, etc.) and inserting a blank line at the bottom. Numerous more elaborate scrolling techniques can be implemented using various directions (up, down, left,

right) and smaller areas or "windows" of the screen. Similarly, clearing the screen, which consists of writing zeros to the data file and updating the attribute bytes to a uniform value, can be implemented on smaller sections of the screen. The software packages in Appendix C contain examples of such implementations.

#### 5.2.1 Dual Screen Mode

In this mode the second display file is used to provide a second independent screen having the same data and attribute organization as the primary display file. By writing to Port FFH with Bits 0-5 = 1 (Bit 0 set), the second display file is activated at the video screen. Appendix C contains a software package supporting Dual Screen Mode. The software package uses the system variable VIDMOD to determine which display file is the target of the current operation. Special values for VIDMOD have been defined to permit building of one display file while the other is active at the screen so that a complete screen image is ready when the hardware mode is changed. Copy and Exchange routines have been provided to move data within and between the two display files. This enables the BASIC graphics commands like PLOT, CIRCLE and DRAW, which work only in the primary display file, to be used to create screens which are then moved into the second display file.

Because the System ROM works only in the primary display file, you can come up with some unusual situations when you have the second display file active at the screen and you are executing BASIC or using the System ROM routines. If an error occurs, for example, the error message will be placed into the primary display file and the ROM will be waiting for input from the keyboard to direct the next action, but all of this is invisible since you have the other display file active. The machine will appear to be "hung", but it is only doing its normal thing. Be prepared to enter a OUT 255,0 to an invisible command line in order to switch the display back to the standard file!!! Don't forget to also set VIDMOD (POKE 23746,128) to keep things consistent inside the dual screen support code.

#### 5.2.2 High Resolution Graphics Mode

This mode is set by writing to Port OFFH with Bits 0-5=2 (Bit 1 set). In this mode, also called Extended Color Mode, the second display file is used to expand the number of Attribute bytes from one for each 8 X 8 pixel group to one for each 8 X 1 pixel group thus giving 32 X 192 positions within each of which two colors plus Bright and Flash can be defined. Each byte of pixel data entered into the primary display file has

its own Attribute byte in the corresponding location in the second display file, e.g. the byte written to Location 4000H has its Attribute byte at Location 6000H, the byte at 47FFH (last byte of last scan row in Line 7) has its Attribute byte at Location 67FFH, the byte at 57FFH (last byte of last scan row in Line 23) has its Attribute byte at Location 77FFH. The routine writing data to the screen would therefore enter the pixel data to the desired location and then set Address Bit 13 of the Primary Display File address and write the desired attribute control byte to the resultant location. If normal characters are being written to the screen in this mode, eight Attribute bytes must also be written, one for each of the bytes defining the character. The same technique would be used for writing to both display files, i.e. for each of the seven bytes entered after the first, the display file address would be incremented by 256 (100H).

The System ROM graphics commands (PLOT, DRAW and CIRCLE) place data into the Primary Display File and update the Attribute File associated with the standard video mode (5800H-5AFFH). In High Resolution Graphics Mode, the hardware does not access this area for attribute control, therefore its contents have no visible effect. If before or immediately following execution of the BASIC graphics operation, you update the attribute control information in the second display file, you could possibly take advantage of the System ROM graphics capability. Admittedly, this is not a simple operation in the case of circles or drawing diagonal lines and it will be more efficient to develop code specifically to support this video mode.

The following sample routine takes as input two single byte binary digits representing the X and Y coordinates of a pixel position on the screen. It formulates the display file address of the byte containing the pixel, creates a pattern or mask byte for the specified bit position, sets the bit in the display file, and updates the attribute byte (High Resolution Graphics Mode assumed). This represents a simplified version of the approach used in the System ROM graphics support routines PLOTBC and SCRMBL.

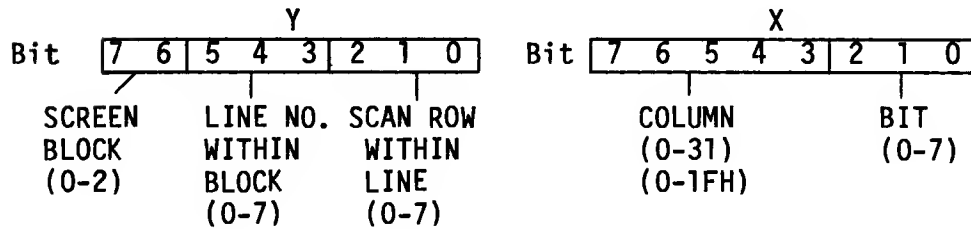
The two inputs are assumed to be as follows:

Reg. C = X Coordinate 0-255 (0-FFH) going left to right across the screen.

Reg. B = Y Coordinate 0-191 (0-BFH) going from bottom to top of the screen.

NOTE: This covers the full vertical range of 192 positions.

The Y Coordinate is checked for valid range and reversed directionally so that 0 represents the top of the screen and 191 represents the bottom. After this reversal, the two coordinates represent the following values:



We first formulate the MSB of the display file address using the Block and Scan Line information in the Y Coordinate:

PLOTXY	PUSH (SAVECO),BC	Save coordinates
	LD A,191	Test Y within range
	SUB B	
	JP C,ERROR	Y coordinate beyond range
	LD B,A	Y Coordinate now 0=Top
	AND OCOH	Get Block No. (0-2)
	RRA	Shift Bits to Pos. 3&4
	RRA	
	RRA	
	LD H,A	Save Block Bits
	LD A,B	Y Coordinate
	AND 07	Get Scan Row Bits
	OR H	Combine Block and Scan Row
	OR 40H	Base Address of DF (4000H)
	LD H,A	H = MSB of DF Address

Next we formulate the LSB of the display file address using the Line information from the Y Coordinate and the Column information from the X Coordinate:

	LD A,C	Get X Coordinate
	RLCA	Align to Pick Up Line
	RLCA	Bits from Y
	RLCA	A=2 LS Bits Column/XXX/3 MS
;		Bits Column
	AND 0C7H	Clear Bits 3-5
	LD L,A	Save A in L
	LD A,B	Get Y Coordinate
	AND 38H	Get Line Bits
	OR L	Combine with Col.Bits
	RLCA	Shift to Final Position
	RLCA	A=Line #/Column
	LD L,A	L = LSB Display File Addr.

Next we get the pixel position within the byte by taking the last 3 bits of the X Coordinate and create a mask byte having all bits zero except the addressed pixel. This mask is then used to set the bit in the Display File. The address is set to Display File 2 to update the Attribute File (High Res. Graphics Mode is assumed to be active), and the routine is finished. The memory locations defined as ATTR and SAVECO are for illustration purposes only:

	LD	A,C	Get Pixel Position
	AND	7	0=Leftmost (MSB);7=
			Rightmost (LSB)
;	LD	B,A	Use as Control Count
	INC	B	B=1-8
	LD	A,00000001B	Bit Mask
LOOP	RRCA		Rotate Mask Bit
	DJNZ	LOOP	to Proper Position
	OR	(HL)	OR Bit into DF
	LD	A,20H	
	OR	H	Set Bit 13 for DF2
	LD	H,A	HL = Attribute File
	LD	A,(ATTR)	Get Attribute Byte
	LD	(HL),A	Update Attribute File
	POP	BC	Original X/Y to BC Regs.
	RET		

Repetitive calls to this routine with the appropriate X/Y Coordinate values will "draw" on the screen. The System ROM routines for drawing lines and circles calculate the successive X/Y Coordinate values and use common low-level routines similar to the above to place each pixel in the display file.

### 5.2.3 64-Column Mode

In this mode, set by writing to Port OFFH with Bits 0-2=6 (Bits 1 and 2 set) and Bits 3-5 selecting ink color (0-7), the pixel data portions of the two display files are merged by the hardware on an alternating column basis to produce 64-columns across the screen. All even columns (0,2,4....62) are derived from the primary display file and all odd columns (1,3,5.....63) are derived from the second display file. There are still 24 lines vertically from top to bottom. The attributes are controlled by bits 3-5 written to Port FFH selecting one of eight ink/paper combinations. The Bright and Flash attributes are fixed at 0 and the Border is fixed to match the paper color. The Attribute Files in RAM at 5800H-5AFFH (primary display file) and 7800H-7AFFH (second display file) are not utilized in this mode.

Software supporting this mode must set up the display file address for character insertion based on the column position (even=DF1; odd=DF2). When scrolling the screen (or a portion of it), any line of text on the screen requires the same operation to be done at the corresponding locations in each display file. This is also true to clear the screen (or a portion of it). To save a Screen on tape you must save two Code files, one for each display file. The SAVE filename SCREEN\$ will work for the Primary Display File only. You will have to specifically SAVE the second display file via a SAVE filename CODE 24576,6144. Note also that because the Border color is fixed by the video mode, you will not see the usual "stripes" during a tape operation.

Code to support an 80-column mode screen was developed utilizing the 64-column hardware mode and redefining the character size to a 6 X 8 pixel group (there is really room for 84 characters if the full 256 pixel width is used). Since individual characters now can span the two display files (e.g. 2 pixels in DF1 and 4 in DF2) insertion of data into the display files involves masking the 6-bit character (or portion thereof) with the 8 bits of data read/written from/to the display file.

Appendix C contains descriptions and code listings of software packages supporting 64 and 80-Column modes.

#### 5.2.4 Other

Appendix C also contains software packages supporting the following video screen features:

- A. 40-Column Mode - utilizes the 6 X 8 character set defined for 80-Column Mode in "normal" mode. May be combined with the Dual Screen package.
- B. Sprites - supports movement of software-defined objects and multi-directional screen scrolling services in the Primary Display File. You must create the actual bit map defining the shape of your sprite(s), but this package does the rest.

### 5.3 Other Advanced Concepts

#### 5.3.1 Interruption Fielding

For a machine code program executing in the Home RAM, you can intercept the 17 ms. interruption for your own purposes by permanently enabling Chunk 0 in the Extension ROM Bank (write a 1 to Port OF4H and always have Bit 7 of Port OFFH = 1) and inserting at Location 25262 (62AE Hex) a branch to your own interruption handler. (Or if VIDMOD is not zero, insert your branch instruction at Location 64110 (FA6EH).) By doing this you are forcing the interruption to branch to the RAM and then bypassing the OS RAM Interruption Handler - see Sections 3.2.2.1 and 3.3.3.1. Because the Video Mode Change Service automatically updates internal branch addresses in the OS RAM code when it is relocated between Chunk 3 and Chunk 7, you probably do not want to directly overlay the OS RAM Interruption Handler with your own code if you will be using the Video Mode service. Your branch instruction at 62AEH, however, will be copied unmodified to location FA6EH in Chunk 7 and vice versa.

Note that this technique cannot be used if you are using BASIC since then you must have Chunk 0 enabled in the Home Bank. It also cannot be used from a cartridge because the memory selection hardware (Port OF4H) is common to the Dock and Extension ROM Banks and can only enable one of them at a given time as selected by Bit 7 of Port OFFH.

#### 5.3.2 BASIC AROS Variables

In order to use pre-defined arrays and/or other BASIC variables, store them in the cartridge (possibly in the lower half of the addressable space which is not usable for BASIC program) and branch to a machine code routine via the USR function at the beginning of your BASIC AROS program. Use this routine to do the necessary memory selection and copy your data from the cartridge to the RAM (address in VARS). Adjust the System Variables E LINE, WORKSP, STKBOT and STKEND to all point to the first free memory following your BASIC variables. Of course, all BASIC variables must conform to the format expected by the BASIC Interpreter. In addition to BASIC structures, you can also store screen images and machine code/variables in the cartridge for transfer to the RAM under your control. Consider using the XFER\_BYTES service in the OS RAM.

## 6.0 Known "BUGS" and Corrections

This section describes the known problems in the TS 2068 System Software and gives corrections or work-arounds where these have been defined.

### 6.1 LROS and Autostart Machine Code AROS

6.1.1 If you will be using the System ROM Keyboard routines and accessing the input character code from system variable LAST\_K (5C08H), you must initialize the TS 2068 to "L" mode by setting the system variable MODE at 23617(5C41H) to zero and setting Bit 3 of FLAGS (23611-5C3BH) to 1. (The TS 2068 is in "K" mode when control is passed from System Initialization to the Cartridge; Keyword Token codes will be placed in LAST\_K instead of character codes.)

6.1.2 If you will be using the System ROM Calculator routines (RESTART 40 (28H) ) or any ROM routines that invoke them, you must initialize the System Variable MEM by doing the following:

```
LD    HL,5C92H          Set HL=MEMBOT
LD    (5C68H),HL        Initialize MEM
```

6.1.3 Chunk 3 must not be designated as "in use" by the Cartridge Memory Selection Specification byte. This will cause deselection of the bank switching code prior to completion of the transfer of control to the cartridge starting address. Once control has been transferred, the cartridge code may then enable Chunk 3 in the Dock Bank if desired. (See Section 5.1.)

6.1.4 No entry is made in the System Configuration Table for an AROS if an LROS is present. This means that an LROS designed to support either RAM based or cartridge based applications must include code for detection of an AROS.

### 6.2 Machine Code AROS

When setting the AROS Overhead parameter requesting RAM space for machine code variables,  $21 + n$  bytes ( $15H + n$ ) must be requested where  $n$  is the number of bytes needed. The machine language variables area then starts at 6855H immediately following the 21-byte CHANS area. (See Section 5.1.2.3.)

NOTE: This does not apply to an AROS that contains both BASIC and machine code.



## 6.3 BASIC AROS

- 6.3.1 USR Function - When testing the USR address against the Cartridge Memory Selection byte to determine if the address is in the Home Bank or the Dock Bank, the wrong nibble is tested in the register thus a valid cartridge address could be erroneously processed as a Home Bank address. Since the ROM code cannot be corrected, the machine code in the cartridge would have to be moved to an address that does not cause a problem.
- 6.3.2 FOR/NEXT - If the limit of the FOR statement has already been passed on its initial execution, (e.g. FOR A=1 TO 10 and A has been set to 12), control is passed to the statement following the corresponding NEXT. In the AROS support code, the address of this statement is lost giving unpredictable results. Since the ROM code cannot be corrected, care must be taken not to use this technique in an AROS Cartridge. Normal usage of FOR/NEXT loops is not affected.
- 6.3.3 Advanced Video Modes - Because the BASIC AROS support code interfaces directly to the Bank Switching code in Chunk 3 (does not access based on its relocatability), the second display file cannot be open when executing BASIC program from an AROS.

## 6.4 Video Mode Change Service

- 6.4.1 Available Memory Test - When the size of memory needed is calculated by adding the size of the second display file (6912 bytes or 1B00H) to the memory now in use (address in System Variable STKEND), the code fails to check for overflow. Thus if the address in STKEND is greater than 58623 (E4FFH), the fact that there is not enough free memory to open the second display file will not be detected and the system will "crash". If your BASIC program and/or variables area are large, you may want to make this test yourself prior to invoking the Video Mode Change Service in order to avoid this problem. The size of memory needed is subsequently tested against the contents of RAMTOP and if there is not sufficient space (value in RAMTOP is less than size needed), you will get Error 4, Out of Memory.

6.4.2 RAMTOP - When the machine stack and OS RAM code is moved to Chunk 7, the User Defined Graphics area is moved down in RAM by 2112 bytes (840H) to make room for the stack and OS RAM routines at the top of memory. The pointer in UDG is updated, however, the value in RAMTOP is not modified to insure that the relocated UDG area as well as the OS code and stack are protected from expansion of the BASIC program. You can avoid problems by setting RAMTOP via a CLEAR command specifying an address no greater than 63255 (F717H) prior to invoking the Video Mode Change Service. This reserves space between RAMTOP and the end of memory of 2280 bytes (8E8H) utilized as:

168 bytes (A8H)	User Defined Graphics (21 X 8)
2112 bytes (840H)	Machine Stack and OS Routines
<u>2280</u>	<u>(8E8H)</u>

Example:	RAMTOP = 63255	(F717H)
	+ Reserved Area	2280 (08E8H)
		<u>65535 (FFFFH)</u>

The software packages in Appendix C are written assuming that RAMTOP is set to 57343 (DFFFH) or lower to protect the machine code which is loaded beginning at 57344 (E000H).

6.4.3 NEW Command - If you have used the Video Mode Change Service to open the second display file and now wish to execute the NEW command, you should first return the computer to "normal" mode by calling the video mode service with A=zero. This returns the User Defined Graphics and other RAM structures to their normal locations. If you don't do this, the UDG area will remain in the alternate location and, if you have not corrected RAMTOP as explained above, part or all of your UDG area could be cleared to zeros by the NEW command.

6.4.4 VIDMOD - When Mode 128 (80H) is designated for activating the Primary Display File in Dual Screen Mode the System Variable VIDMOD at 23746 (5CC2H) is set to zero instead of to 128. This creates a potential problem if the 17 ms. interruption occurs before VIDMOD can be corrected since the interruption fielder will branch to Chunk 3 instead of to Chunk 7 and Chunk 3 is now in use for the second display file. This problem is corrected by disabling the interruption prior to calling the Video Mode Change Service and setting VIDMOD to the correct value prior to re-enabling it. These corrections are included in the Extension ROM Interface Routine in Figure 3.2.2-2.

NOTE: On an initial access changing video mode from normal to Mode 128, the interruption is re-enabled within the Video Mode Change Service itself after copying the stack and other Chunk 3 data to Chunk 7. This cannot be corrected, but has not proven to present a problem in actual use. At the point where the interruption is first enabled, the Chunk 3 code is still intact allowing for correct processing of one interruption, and the path length from there to the point of correcting VIDMOD is apparently less than 17 ms. The interruption is also re-enabled within the Video Mode Change Service if you have applied the patches for the BANK\_ENABLE and RESTORE\_STATUS routines (Section 6.5.4) which are executed in connection with inserting space into the RAM to open the second display file. Again, this has not proven to be a problem in actual use.

6.4.4 Interruption Inhibit - By setting Bit 6 of Port OFFH to a 1, the normal 17 ms. interruption generated from the SCLD to the Z80A CPU will be inhibited. When Port OFFH is written to by the Video Mode Change Service, Bit 6 is forced to zero. If you wish to inhibit the normal interruption via this mechanism, and also plan to use the Video Mode Change Service, it is recommended that you first invoke the service to remap the RAM and open the second display file, then set Bit 6 of Port OFFH to inhibit the normal interruption and write your own routine(s) for subsequent changing of the video mode setting that do not involve remapping the RAM. In this way you can maintain the value in Bit 6.

## 6.5 OS RAM Routines

In patching the OS RAM routines, care must be taken not to relocate CALL and JP instructions since this affects the modification of the code when it is moved between Chunks 3 and 7. All of the code containing actual addresses must be modified to reflect the relocation and this is done using a table in the Extension ROM. Since the table cannot be changed, none of these instructions can be moved. Also, any CALL or JP instructions added must be modified by you when the code is relocated.

6.5.1 Function Dispatcher -For a variety of reasons such as conflict with use of the IX Register, incorrect entries in the ROM Function Dispatcher Jump Table, etc. some Service Codes have been deleted from the Function Dispatcher table (Table 3.3.4-2). In addition, the following correction to the GET\_STATUS routine is required in order to successfully utilize the Function Dispatcher from a cartridge.

6.5.2 GET\_STATUS- Returns invalid memory selection status for the Home Bank, ROM Extension and Dock. This results in switching out of either the Home Bank or the Dock when status is "restored". This affects use of the Function Dispatcher and GET\_WORD routines, and any other code using GET\_STATUS. Figure 6.5-1 shows the patches and additions necessary to correct this routine.

6.5.3 PUT\_WORD- Write data passed in Reg. Pair DE is overwritten prior to use. Figure 6.5-2 shows corrections.

6.5.4 BANK\_ENABLE and RESTORE\_STATUS-

If the 17 ms. interruption occurs during update of the memory selection hardware, it can cause the system to hang and RAM to be overwritten. This occurs when the interruption happens in an interval when Port FF Bit 7 is zero (thus selecting the Dock Bank) and Port F4 Bit 0 is one (thus enabling Chunk 0 in the Dock Bank) and there is no memory in Chunk 0 of the Dock Bank. This can be true when there is no cartridge installed, or if the cartridge installed is an AROS. This problem is corrected by disabling or masking the interruption while updating the memory selection hardware. Figure 6.5-3 shows one implementation of this correction.

6.5.5 SAVE\_STATUS and RESTORE\_STATUS - The value of Port FFH which includes video mode and interruption inhibit as well as Ext. ROM/Dock Select is saved and restored as a full 8-bits. Therefore any modification of this port by code accessed between execution of SAVE\_STATUS and subsequent execution of RESTORE\_STATUS (e.g. via CALL\_BANK or use of the Function Dispatcher) is "undone". This is one reason the Video Mode Change Service and some of the bank switching routines such as BANK\_ENABLE cannot be meaningfully accessed via the Function Dispatcher.

6.5.6 CALL\_BANK- Does not correctly retrieve the stack entry designating the count of parameters being passed. Memory is overwritten in the case where this count is not zero. This is corrected by setting Location 6610H = 9 (POKE 26128,9). You only need to apply the correction once; it will be duplicated in Chunk 7 if the code is relocated.

FIGURE 6.5-1  
GET\_STATUS CORRECTIONS

LOCATION (HEX)	OBJ.CODE (HEX)	SOURCE STATEMENT	COMMENTS
Input: Bank # in B			
Output: Bank # in B (Bank Status if Exp.Bank) Memory Selection in C (Low Active Format)			
6405	F5	GET_STATUS PUSH AF	Save Regs.
6406	D5	PUSH DE	
6407	78	LD A,B	Get Bank #
6408	FEFE	CP OFEH	Test if Ext.(254)
* 640A	2824	JR Z,GS_EXT	
640C	FEFF	CP OFFH	Test if Home(255)
* 640E	2837	JR Z,GS_HOME	
6410	A7	AND A	Test if Dock (0)
* 6411	2827	JR Z,GS_DOCK	
6413	.	.	
.	.	.	(Code for Expansion Banks not applicable)
.	.	.	
.	.	.	
* 6430	0EFF	GS_EXT LD C,OFFH	Assume none
* 6432	DBFF	IN A,(OFFH)	Test if selected
* 6434	E680	AND 80H	
* 6436	2812	JR Z,GS_XT1	Not active
* 6438	1808	JR GETHS	Get Hor.Select
* 643A	0EFF	GS_DOCK LD C,OFFH	Assume none
* 643C	DBFF	IN A,(OFFH)	Test if selected
* 643E	E680	AND 80H	
* 6440	2008	JR NZ,GS_XT1	Not active
* 6442	DBF4	GETHS IN A,(OFFH)	Get Hor.Select Reg.
* 6444	2F	CPL	Invert to Low Active
* 6445	1802	JR GS_XT0	Exit
* 6447	DBF4	GS_HOME IN A,OFFH	All bits set are not active in Home Bank
* 6449	4F	GS_XT0 LD C,A	Memory Select to C
644A	D1	GS_XT1 POP DE	Restore Regs.
644B	F1	POP AF	
644C	C9	RET	Return

The asterisks mark the locations modified. See next page for list of corresponding POKE's for BASIC.

FIGURE 6.5-1  
GET\_STATUS CORRECTIONS  
(continued)

From BASIC:

POKE 25610,40	(Location 640AH)
POKE 25611,36	
POKE 25614,40	(Location 640EH)
POKE 25615,55	
POKE 25617,40	(Location 6411H)
POKE 25618,39	
POKE 25648,14	(Location 6430H)
POKE 25649,255	
POKE 25650,219	
POKE 25651,255	
POKE 25652,230	
POKE 25653,128	
POKE 25654,40	
POKE 25655,18	
POKE 25656,24	
POKE 25657,8	
POKE 25658,14	
POKE 25659,255	
POKE 25660,219	
POKE 25661,255	
POKE 25662,230	
POKE 25663,128	
POKE 25664,32	
POKE 25665,8	
POKE 25666,219	
POKE 25667,244	
POKE 25668,47	
POKE 25669,24	
POKE 25670,2	
POKE 25671,219	
POKE 25672,244	
POKE 25673,79	

FIGURE 6.5-2  
PUT\_WORD CORRECTIONS

LOCATION (HEX)	OBJ.CODE (HEX)	SOURCE STATEMENT	COMMENTS
Input: Data in DE, Address in HL, Bank # in B			
633B	F5	PUT_WORD    PUSH    AF	Save Regs.
633C	C5	PUSH    BC	
633D	CD5E64	CALL    GET_NUMBER	Bank # of Owner
* 6340	D5	PUSH    DE	Save Data
6341	50	LD      D,B	Save Target Bank #
6342	47	LD      B,A	Bank # of Owner
6343	CD0564	CALL    GET_STATUS	Get Bank Status
6346	C5	PUSH    BC	Save It
6347	CD4D64	CALL    GET_CHUNK	Get Bit Map
634A	2F	CPL	Set High Active
634B	42	LD      B,D	Target Bank # to B
634C	4F	LD      C,A	Memory Select Byte
634D	CD9964	CALL    BANK_ENABLE	Enbl.Target Mem.
* 6350	C1	POP     BC	Saved Bank Status
* 6351	D1	POP     DE	Saved Data
* 6352	73	LD      (HL),E	Write LSB
* 6353	23	INC     HL	Increment Adrs.
* 6354	72	LD      (HL),D	Write MSB
* 6355	2B	DEC     HL	Restore HL
6356	CD9964	CALL    BANK_ENABLE	Restore Bank St.
6359	C1	POP     BC	Restore Regs.
635A	F1	POP     AF	
635B	C9	RET	Return

The asterisks mark the locations modified.

From BASIC:

```
POKE 25408,213
POKE 25424,193
POKE 25425,209
POKE 25426,115
POKE 25427,35
POKE 25428,114
POKE 25429,43
```

NOTE: The corrections to GET\_STATUS and BANK\_ENABLE are also required.

FIGURE 6.5-3

## BANK\_ENABLE AND RESTORE\_STATUS CORRECTIONS

BANK_ENABLE:	Location	Object Code	From BASIC	
			POKE Address	Value
	6499H	00 NOP	25753	0
	649DH	F3 DI	25757	243
	651CH	FB EI	25884	251

## RESTORE\_STATUS:

654AH	F3	DI	25930	243
6570H	FB	EI	25968	251

In both cases, the Disable Interrupt and Enable Interrupt are being done by deleting the preservation of the AF Registers (PUSH AF/POP AF). If your code requires AF to be saved, you must do it prior to calling either of these routines or any other system routines that use them. Note also that if you already have the interruption masked when these routines are entered, it will be enabled when they are exited. If this proves to be a problem, replace the Enable Interruption (EI) instruction with a NOP and do the enable at a more appropriate place in your own code.

6.5.6 GET\_NUMBER- Always returns the Dock Bank # for any memory enabled in the ROM Extension. Unlikely to be a problem because of limited use of the ROM Extension.

6.5.7 XFR\_BYTES- Improperly passes memory select byte for the case where source and destination are in the same bank. This is corrected by setting Location 676AH = 5FH (POKE 26474,95).

## 6.6 GENERAL

6.6.1 Pressing ENTER multiple times with an invalid tape command on the edit line (syntax error) causes the system to reset. This is due to overflowing the Bank Status Stack in RAM Chunk 3/7 due to the multiple calls to and from the Extension ROM via the Call Bank code without normal termination (the error causes a RESTART 8 to be executed out of Home ROM code called from the ROM Extension). It shouldn't take anybody that many tries to get a tape command right, so this is not a real problem, but you may want to keep it in mind. For any call made through the OS RAM services, you should have a corresponding return to keep the structures clean.



6.6.2 ON ERR GOTO - If a non-existent line number is specified, followed by an error, the system will hang. The ROM code is in an endless loop trying to report the absence of a valid error handler to the non-existent error handler!!! On some errors, you will get an unexpected 0 OK termination showing the line number of your Error Handler. This is because some ROM routines temporarily clear the INTPT Flag (Bit 7 of FLAGS). This flag is set to 0 when checking syntax and set to 1 when executing; if an error is detected while the Flag=0, the error handler code is branched to but is not executed.

6.6.3 Parameters to the SOUND command are not fully validated, therefore you can specify a number beyond the valid range for a given operation and not get an error, for example, you can write a value greater than 63 to the Enable Register (Reg.7), possibly changing the I/O Port used for reading the joysticks from input to output. If you specify a number larger than 255 (FFH), only the least significant byte will be actually written to the Programmable Sound Generator. Access to PSG Reg. 14 (IO-A) used for the Joysticks is also not precluded via the SOUND command.

If you experience difficulty in reading the joystick(s), do a write to PSG Reg. 7 clearing Bit 6 to 0 to guarantee that the joystick path is enabled for input (see Section 4.3). This write can be done by executing a SOUND 7,63 (or any value less than 63).

The INTEGER function for (-65536) gives an incorrect result of -1, and for other cases where the result should be -65536, it gives -1E-38. Since the ROM code cannot be changed, there is no correction.

6.6.4 If you respond to the SCROLL? message using multiple keys such as Cap Shift/2 or Cap Shift/Symbol Shift, you will get strange results like dumping of the Edit Line with the "C" or "E" cursor, display of ROM data, or multiple scrolls. Stick to single key responses and you won't have any problems!

6.6.5 When DELETE (Cap Shift/0) is held down to do deletion of characters in the Edit Line, sometimes it outputs the DELETE Keyword instead (it should not do this in auto-repeat mode). This is especially noticeable when the input line is long. Since the ROM code cannot be corrected, you must try releasing and pressing the DELETE key at differing frequencies and you will be able to get past this "Bug".

# APPENDIX A

## HOME ROM MAP

LINK 1.7

LOAD MAP  
MODULE

ORIGIN LENGTH

BLOCK	0000	0000
BASIC	0000	0227
KSCAN	0227	02D9
IO_1	0500	0502
IO_2	0A02	031B
EDIT	0D1D	0682
CHANS	139F	0142
LIST	14E1	02D4
AROS	17B5	0190
SYNTAX	1945	080A
SYNTWO	214F	04B4
GRAPHS	2603	0251
EXPRN	2854	041C
IDENT	2C70	03E9
INOUT	3059	0301
SUMS	335A	032A
CALC	3684	0437
FUNCTS	3ABB	01CE
TAPEMSG	3C89	0053
CHLSET	3D00	0300

GLOBAL ADDRESS MODULE

ACS	3C5E	FUNCTS
ADD	33D3	SUMS
ALNUM?	3046	IDENT
ALPHA?	304B	IDENT
ANGLE	3B9E	FUNCTS
AROS	18C6	AROS
ARRAY	37C5	CALC
AR_LN	17EA	AROS
AR_NXT	17FF	AROS
ASN	3C4E	FUNCTS
ATN	3BFD	FUNCTS
ATTBYT	0710	IO_1
BEEP	0436	KSCAN
BORDER	243E	SYNTWO
BREAK?	2009	SYNTAX
CAT	25C8	SYNTWO
CHCODE	0371	KSCAN
CHINIT	11AA	EDIT
CHK_SZ	1FBB	SYNTAX
CIRCLE	2679	GRAPHS
CLCHAN	13BE	CHANS
CLEAR	1F36	SYNTAX
CLEL	133F	EDIT
CLLHS	08A9	IO_1
CLOSE	139F	CHANS
CLPR	0A35	IO_2
CLR_BC	1F39	SYNTAX
CLS	08EA	IO_1
CLS_B	097F	IO_1
COLITM	23A6	SYNTWO
COLOUR	23DE	SYNTWO
CONT	1EE4	SYNTAX
COS	3BC5	FUNCTS
CP_BC	16E8	LIST
CTRO	371A	CALC

DATA	1E22	SYNTAX
DEF	201D	SYNTAX
DELREC	1750	LIST
DELSYM	0B7E	IO_2
DEL_DE	174D	LIST
DEL_K	0BFD	IO_2
DESLUG	0D0D	IO_2
DE_HL	1668	LIST
DIGIT?	30D9	INOUT
DIM	2FC0	IDENT
DIVIDE	356E	SUMS
DRAW	26DB	GRAPHS
DRAWLN	2813	GRAPHS
DRAW_L	2810	GRAPHS
DUMPPR	0A23	IO_2
DYADIC	1BDC	SYNTAX
ECHO	0C83	IO_2
EDIT_K	0A82	IO_2
END?	1B44	SYNTAX
ENDSTT	1AB9	SYNTAX
ENDTEM	1B4A	SYNTAX
ERASE	25D4	SYNTWO
ERR2	1B91	SYNTAX
ERR4	1FCF	SYNTAX
ERR5	07C1	IO_1
ERR6	356C	SUMS
ERRB	1F29	SYNTAX
ERRH	237E	SYNTWO
ERRO	123D	EDIT
EXCUTE	1AD8	SYNTAX
EXP	3ADF	FUNCTS
EXPRN	2854	EXPRN
FIND_L	16D6	LIST
FIND_N	2C70	IDENT
FIX_U	1F23	SYNTAX
FIX_U1	1F1E	SYNTAX
FLASHA	160D	LIST
FLOAT	3656	SUMS
FOR	1C78	SYNTAX
FORMAT	25CC	SYNTWO
FP2A	3193	INOUT
FP2BC	3160	INOUT
F_ATTR	28D7	EXPRN
F_INKY	29F2	EXPRN
F_PI	29E5	EXPRN
F_PNT	2624	GRAPHS
F_SCRN	283E	EXPRN
GETAL	17CF	AROS
GET_LEL	2D54	IDENT
GET_LN	1324	EDIT
GET_XY	2660	GRAPHS
GO_SUB,	1F99	SYNTAX
GR_COL	239C	SYNTWO
HIFLSH	241D	SYNTWO
INCH	11E1	EDIT
ININT	30F9	INOUT
INIT	0D31	EDIT
INPUT	222B	SYNTWO
INSI	12B8	EDIT
INSA	0AE7	IO_2
INSERT	12B8	EDIT
INT	3ACA	FUNCTS
INTDIV	3ABB	FUNCTS

INTPT?	2889	EXPRN	REMGSZ	12CA	EDIT
IN_K	0C0E	IO_2	RESET	1354	EDIT
I_SEQ	226B	SYNTWO	RESTBC	1ECA	SYNTAX
JUMP	1EF1	SYNTAX	RETURN	1FD4	SYNTAX
K_BASE	035C	KSCAN	RND	29B6	EXPRN
K_CLS	08A6	IO_1	ROOM?	3768	CALC
K_DUMP	0A02	IO_2	ROOT	3C65	FUNCTS
K_LIST	1545	LIST	RSET	2454	SYNTWO
K_LLST	1541	LIST	RSTSTR	13A8	CHANS
K_LPR	2155	SYNTWO	R_ATT5	0888	IO_1
K_NEW	0D1D	EDIT	SCRL	0939	IO_1
K_PRIN	2159	SYNTWO	SCRMEL	2603	GRAPHS
K_SCAN	02B0	KSCAN	SEARCH	136B	EDIT
LCU2	132D	EDIT	SELECT	1230	EDIT
LDDE	313D	INOUT	SEL_HL	1248	EDIT
LDMES	3CA8	TAPEMSG	SENDCH	11ED	EDIT
LDTVCU	061A	IO_1	SENDTV	0500	IO_1
LE3	0055	BASIC	SEPRMT	3C89	TAPEMSG
LED18	0E2F	EDIT	SETCUR	0914	IO_1
LED4	0E8D	EDIT	SETTVC	0914	IO_1
LET	2EBD	IDENT	SET_AT	05B2	IO_1
LINENO	1768	LIST	SHIFT	339C	SUMS
LIST	14E1	LIST	SIN	3BD0	FUNCTS
LN	3B2E	FUNCTS	SKIP	1D28	SYNTAX
LPO	15AC	LIST	SKIPIT	2569	SYNTWO
LS4	1A44	SYNTAX	SLICER	2E10	IDENT
LT22	1BBC	SYNTAX	SINIT	11C1	EDIT
MOVE	25D0	SYNTWO	SOUND	2128	SYNTAX
MULT	3468	SUMS	SRCHSC	1374	EDIT
NC_HL	0077	BASIC	STBOOL	3926	CALC
NEGATE	382D	CALC	STDE_S	314C	INOUT
NEW	0D82	EDIT	STDE_U	314A	INOUT
NEWDEV	24D2	SYNTWO	STKUSN	3059	INOUT
NEXT	1D55	SYNTAX	STK_O	1C51	SYNTAX
NEXTCH	0074	BASIC	STK_A	30E6	INOUT
NEXT_L	165B	LIST	STK_BC	30E9	INOUT
NOTKB?	2380	SYNTWO	STK_M	3773	CALC
NXT_HL	2C69	EXPRN	STOP	1C59	SYNTAX
OPCHAN	1465	CHANS	STRITO	220F	SYNTWO
OPEN	142A	CHANS	STTVCU	05F3	IO_1
OPTNO	1C49	SYNTAX	SUB	33CE	SUMS
OUTPUT	31A1	INOUT	SUBLIN	16F0	LIST
PAEDCB	2E74	IDENT	SUBLN1	16F3	LIST
PARP	03F3	KSCAN	SUMSLD	3379	SUMS
PASSEM	25B9	SYNTWO	SYNERR	1BED	SYNTAX
PAUSE	1FEB	SYNTAX	SYNTAX	1A27	SYNTAX
PHLAF	004F	BASIC	TAN	3BF5	FUNCTS
PLOT	2635	GRAPHS	TC_HL	0078	BASIC
PLOTBC	263E	GRAPHS	TEM1	1B82	SYNTAX
PLUGIN	0000	BASIC	TEM10	1BEF	SYNTAX
POPSTR	2FAF	IDENT	TEM6	1BE5	SYNTAX
PRSCAN	0A4A	IO_2	TEMP38	19E0	SYNTAX
PR_CUR	162D	LIST	TEMP39	19E1	SYNTAX
PR_TV2	0776	IO_1	TERM?	21E7	SYNTWO
PSHSTR	2E70	IDENT	TESTO	3904	CALC
PUT	15C9	LIST	TIMES	3489	SUMS
PUTDIG	11EA	EDIT	TOKENS	0098	BASIC
PUTMES	073F	IO_1	TO_THE	3C6C	FUNCTS
PUT_BC	1788	LIST	TRUNC	35D3	SUMS
PUT_LN	1795	LIST	TVFUL?	0790	IO_1
PUT_SR	15A1	LIST	TV_COL	23BB	SYNTWO
P_LFT	053A	IO_1	UPD_K	02E1	KSCAN
P_NL	0566	IO_1	USRRET	3882	CALC
P_RT	0554	IO_1	WRCH	0010	BASIC
P_SEQ	217E	SYNTWO	XEY	310D	INOUT
RAMNO	377F	CALC	X_CALC	134E	EDIT
RAND	1ED4	SYNTAX	X_T_HL	1363	EDIT
RDCH	11CF	EDIT			
READ	1D97	SYNTAX			
RECLN	1720	LIST			

PROGRAM BLOCK -- 4000 BYTES  
ENTRY: 0000

# EXTENSION ROM MAP

## LINK 1.7

### LOAD MAP MODULE

ORIGIN LENGTH

XBASIC	0000	0068
TAPE	0068	087F
INIT	08E7	04C9
CHNG_VID	0DB0	0193
PASSING	0F43	0047
BS	0F8A	001E

### GLOBAL

ADDRESS

MODULE

AKEY	08AA	TAPE
BLDSCT	09F4	INIT
CALL_B	0F99	BS
CHNG_V	0E8E	CHNG_VID
CLDFIL	0E27	CHNG_VID
EXINIT	08E7	INIT
GOTO_B	0F8A	BS
LOAD	05CC	TAPE
MERGE	06E5	TAPE
OPDFIL	0DB0	CHNG_VID
PASSIN	0F43	PASSING
RD_BIT	0189	TAPE
RESSCT	0C4C	INIT
R_EDGE	018D	TAPE
R_TAPE	00FC	TAPE
SAVE	0851	TAPE
SLVM	01AB	TAPE
W_BORD	00E5	TAPE
W_TAPE	0068	TAPE

PROGRAM XBASIC -- 1000 BYTES

ENTRY: 0000

DISPATCH 1000 0624 THIS MODULE IS COPIED TO RAM 6200 (space reserved 6200-683FH).

GLOBAL ADDRESS MODULE Relocated to RAM F9C0-FFFFH when second Display File is used.

BANK_E	6499	DISPATCH
BS_MAX	6315	DISPATCH
BS_SP	65CE	DISPATCH
CALL_B	65D0	DISPATCH
CREATE	66E8	DISPATCH
DISPAT	6200	DISPATCH
GET_CH	644D	DISPATCH
GET_NU	645E	DISPATCH
GET_ST	6405	DISPATCH
GET_WO	6316	DISPATCH
GOTO_B	6572	DISPATCH
GOTO_E	6815	DISPATCH
INT	62AE	DISPATCH

ORIGIN LENGTH

TABLES:	FIXTBL	1000	007C
	JMPTBL	1E0C	0124
UNUSED:		1624	06DC
		107C	0160

```

420 *LIST ON
421 *LIST ON
422 *INCLUDE NEWL.SYSVAR.D.S
423 ! HERE ARE SOME NEW SYSTEM VARIABLE DEFINITIONS
424 !
425 !
426 STKSZ EQU 200H
427 SADDPT EQU 0F5H !SOUND CHIP ADDR PORT
428 SDATPT EQU 0F6H !SOUND CHIP DATA PORT
429 HS EQU 40H
430 HS_LSN EQU HS
431 BNA EQU 20H
432 HS_MSN EQU BNA
433 ABN EQU 0A0H
434 HSP EQU ABN !HS REG ADDR
435 STALL EQU ABN
436 CMD EQU 0C0H
437 STALO EQU CMD
438 LOWNYB EQU 0C00H !RESET NYBBLE STEERING LOGIC CMD
439 FREE_BYTES EQU 32
440 PRM_OUT EQU 8
441 HOR_SEL EQU 10
442 BANK EQU 11
443 UPD_K EQU 02E1H
444 !
445 !
446 !
447 GLOBAL DISPATCH, INT, NMI, PUT_WORD, GET_WORD
448 GLOBAL WRITE_BS_REG, READ_BS_REG, GET_STATUS, GET_NUMBER
449 GLOBAL GET_CHUNK, BANK_ENABLE, GOTO_BANK, CALL_BANK
450 GLOBAL XFER_BYTES, BS_MAX_BANK, BS_SP
451 GLOBAL CREATE_BITMAP, MOVE_BYTES
452 !
453 ! DISPATCH (SVC_CODE: PASSED ON STACK)
454 !
455 ! SVC_CODE IS A 16 BIT QUANTITY. BIT 15 IS USED AS A JUMP FLAG: IF
456 ! SET, THE DISPATCHER WILL DO A GOTO_BANK TO THE SPECIFIED ROUTINE,
457 ! OTHERWISE IT WILL DO A CALL_BANK.
458 !
459 !
460 !
461 JMP_TBL EQU 1FFFH
462 LAST_EXT_SVC EQU 13
463 LAST_RAM_SVC EQU 24
464 !
465 !
466 !
467 ORG 6200H
468 !
6200 !
6200 0B210000 469 DISPATCH LD IX, 0
6204 DD39 470 ADD IX, SP !IX = SP
6206 C5 471 PUSH W !RESERVE A WORD ON THE STACK
6207 F5 472 PUSH AF !SAVE REGS
6208 C5 473 PUSH W
6209 D5 474 PUSH DE
620A E5 475 PUSH HL
620B DD5E02 476 LD E, (IX+2)
620E DD5603 477 LD D, (IX+3) !DE = SVC_CODE
6211 AF 478 XOR A
6212 CB23 479 SLA E
6214 CB12 480 RL D !DE = 2*DE
6216 17 481 RLA !A = JUMP FLAG
6217 210D00 482 LD HL, LAST_EXT_SVC
621A CB25 483 SLA L
621C CB14 484 RL H !HL = 2*HL
621E A7 485 AND A
621F ED52 486 SBC HL, DE !COMPARE HL AND DE
6221 3015 487 JR NC, D_EXT !IF DE <= HL
6223 211900 488 LD HL, LAST_RAM_SVC
6226 CB25 489 SLA L
6228 CB14 490 RL H
622A A7 491 AND A
622B ED52 492 SBC HL, DE
622D 380F 493 JR C, D_HOME
622F 06FF 494 LD B, 255 !HERE FOR RAM-BASED SERVICES
6231 CD0564 495 CALL GET_STATUS !GET STATUS OF HOME BANK
6234 06FF 496 LD B, 255 !BC = HOME BANK / HORIZ-SELECT
6236 180A 497 JR D_SAVE
6238 06FE 498 LD B, 254 !HERE FOR EXT. ROM BASED SERVICES
623A 0EFE 499 LD C, 0FEH
623C 1804 500 JR D_SAVE
623E 06FF 501 LD B, 255 !SET BANK_ENABLE PARAMS FOR HOME
6240 0E00 502 LD C, 0
6242 F5 503 D_SAVE PUSH AF
6243 C5 504 PUSH BC !SAVE JUMP FLAG AND BANK_ENABLE PARAMS
6244 21FF1F 505 LD HL, JMP_TBL !CALC. ADDR OF TABLE ENTRY
6247 37 506 SCF
6248 ED52 507 SBC HL, DE
624A 06FE 508 LD B, 254
624C CD1663 509 CALL GET_WORD !READ TABLE ENTRY
624F EB 510 EX DE, HL
6250 C1 511 POP BC
6251 F1 512 POP AF !RESTORE JUMP FLAG, ETC.
6252 A7 513 AND A
6253 281F 514 JR Z, D_CALL
6255 DD71FE 515 LD (IX-2), C !PUT BANK# AND HOR-SEL ON STACK
6258 DD70FF 516 LD (IX-1), B
625B DD6E00 517 LD L, (IX) !SAVE RET ADDR
625E DD6601 518 LD H, (IX+1)
6261 DD7403 519 LD (IX+3), H !PUT RET ADDR BACK ON STACK

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6264	DD7502	520	LD	(IX+2), L	
6267	DD7201	521	LD	(IX+1), D	!SET UP STACK FOR GOTO_BANK
626A	DD7300	522	LD	(IX), E	!PUT ADDR ON STACK
626D	E1	523	POP	HL	!RESTORE REGS
626F	D1	524	POP	DE	
626F	C1	525	POP	BC	
6270	F1	526	POP	AF	
6271	CD7265	527	CALL	GOTO_BANK	!HERE IF JUMP FLAG NOT SET
6274	DD6D00	528	LD	L, (IX)	!SET UP STACK FOR CALL_BANK
6277	DD6601	529	LD	H, (IX+1)	!PUT RET_ADDR IN PROPER LOC
627A	E5	530	PUSH	HL	
627B	DD6E04	531	LD	L, (IX+4)	
627E	DD6A05	532	LD	H, (IX+5)	
6281	DD75FE	533	LD	(IX-2), L	
6284	DD74FF	534	LD	(IX-1), H	
6287	DD6E06	535	LD	L, (IX+6)	!PUT PRM_OUT IN PROPER LOC
628A	DD6607	536	LD	H, (IX+7)	
628D	DD7500	537	LD	(IX), L	
6290	DD7401	538	LD	(IX+1), H	
6293	DD7102	539	LD	(IX+2), C	!PUT BANK #, HS ON STACK
6296	DD7003	540	LD	(IX+3), B	
6299	DD7304	541	LD	(IX+4), E	!PUT ADDR ON STACK
629C	DD7205	542	LD	(IX+5), D	
629F	E1	543	POP	HL	
62A0	DD7506	544	LD	(IX+6), L	
62A3	DD7407	545	LD	(IX+7), H	
62A6	E1	546	POP	HL	!RESTORE REGS
62A7	D1	547	POP	DE	
62A8	C1	548	POP	BC	
62A9	F1	549	POP	AF	
62AA	CD0065	550	CALL	CALL_BANK	!HERE IF JUMP FLAG NOT SET
62AD	C9	551	RET		
		552			
		553			
		554			!FIRST 56: HERE TO SERVICE INTERRUPT BY READING KEYBOARD
		555			
62AE	F5	556	INT	PUSH AF	
62AF	E5	557		PUSH HL	
62B0	DDE5	558		PUSH IX	
62B2	210000	559		LD HL, 0	
62B5	39	560		ADD HL, SP	
62B6	D5	561		PUSH DE	
62E7	3A1563	562		LD A, (BS_MAX_BANK)	
62BA	5F	563		LD E, A	
62BB	1600	564		LD D, 0	
62BD	13	565		INC DE	
62BE	13	566		INC DE	
62BF	A7	567		AND A	
62C0	ED52	568		SBC HL, DE	
62C2	EB	569		EX DE, HL	
62C3	DD210000	570		LD IX, 0	
62C7	DD19	571		ADD IX, DE	
62C9	D1	572		POP DE	
62CA	DDF0	573		LD SP, IX	
62CC	CD1E65	574		CALL SAVE_STATUS	
62CF	C5	575		PUSH BC	
62D0	06FF	576		LD B, 0FFH	
62D2	CD0564	577		CALL GET_STATUS	
62D5	06FF	578		LD B, 0FFH	
62D7	79	579		LD A, C	
62D8	E6F8	580		AND 0FFH	
62DA	4F	581		LD C, A	
62DB	CD9964	582		CALL BANK_ENABLE	
62DE	C1	583		POP BC	
62DF	2A785C	584		LD HL, (FRAMES) !NOW INCREMENT FRAME COUNTER	
62E2	23	585		INC HL	
62E3	22785C	586		LD (FRAMES), HL	
62E6	7C	587		LD A, H	
62E7	B5	588		OR L	
62E8	2003	589		JR NZ, LIT3	
62EA	FD3440	590		INC (IX-Y+FRAME2)	
62ED	C5	591	LIT3:	PUSH BC	
62EE	D5	592		PUSH DE	
62EF	CDE102	593		CALL UPD_LK	
62F2	D1	594		POP DE	
62F3	C1	595		POP BC	
		596	PHLAF:	!JUMP HERE TO POP HL, POP AF, ENABLE INTERRUPTS & RETURN	
62F4	DD210000	597		LD IX, 0	
62F8	DD39	598		ADD IX, SP	
62FA	CD4A65	599		CALL RESTORE_STATUS	
62FD	DD23	600		INC IX	
62FF	DDF9	601		LD SP, IX	
6301	DDE1	602		POP IX	
6303	E1	603		POP HL	
6304	F1	604		POP AF	
6305	FB	605		E1	
6306	C9	606		RET	
		607			
		608			!HERE TO SERVICE NON-MASKABLE INTERRUPT
		609			!IF (NMIADD) = 0 THEN RETURNS STRAIGHT AWAY;
		610			!ELSE, JUMPS TO (NMIADD) WITH HL (ON TOP), AF & RETN ADDR ON
		611			! THE STACK.
6307	F5	612	NMI	PUSH AF	
6309	E5	613		PUSH HL	
6309	2AB05C	614		LD HL, (NMIADD)	
630C	7C	615		LD A, H	
630D	B5	616		OR L	
630E	2001	617		JR NZ, LNI3	!IF NO USER-SUPPLIED SERVICE ROUTINE
6310	E9	618		JP (HL)	

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6311 E1 619
6312 F1 620 LN13: POP HL
6313 ED45 621 POP AF
622 RETN
623
624
625 PS_MAX_BANK DEFS 1 ;THIS IS A COPY OF MAX_BANK
626
627 GET_WORD (ADDR: HL, BANK: B, WORD: HL)
628
629
6314 F5 630 GET_WORD PUSH AF ;SAVE REGS
6317 C5 631 PUSH BC
6318 D5 632 PUSH DE
6319 CD5E64 633 CALL GET_NUMBER ;GET BANK # OF OWNER OF ADDR
631C FF 634 PUSH AF
631D 50 635 LD D, H
631E 47 636 LD B, A
631F CD0564 637 CALL GET_STATUS ;GET STATUS OF OWNER
6322 C5 638 PUSH BC
6323 CD4D64 639 CALL GET_CHUNK ;SET HS FOR GETTING AT ADDR
6326 2F 640 CPL ;PUT IN ACTIVE LOW FORMAT
6327 42 641 LD B, D
6328 4F 642 LD C, A
6329 CD9964 643 CALL BANK_ENABLE ;ENABLE ADDR
632C 7E 644 LD E, (HL) ;READ THE WORD
632D 23 645 INC HL
632E 56 646 LD D, (HL)
632F 2B 647 DEC HL
6330 EB 648 EX DE, HL
6331 C1 649 POP BC
6332 F1 650 POP AF
6333 47 651 LD B, A
6334 CD9964 652 CALL BANK_ENABLE ;REENABLE OWNER OF ADDR
6337 D1 653 POP DE ;RESTORE REGS
6338 C1 654 POP BC
6339 F1 655 POP AF
633A C9 656 RET
657
658
659 PUT_WORD (WORD: DE, ADDR: HL, BANK: B)
660
661
633B F5 662 PUT_WORD PUSH AF ;SAVE REGS
633C C5 663 PUSH BC
633D CD5E64 664 CALL GET_NUMBER ;GET BANK # OF OWNER OF ADDR
6340 F5 665 PUSH AF
6341 50 666 LD D, B
6342 47 667 LD B, A
6343 CD0564 668 CALL GET_STATUS ;GET STATUS OF OWNER
6346 C5 669 PUSH BC
6347 CD4D64 670 CALL GET_CHUNK ;SET HS FOR GETTING AT ADDR
634A 2F 671 CPL ;PUT IN ACTIVE LOW FORMAT
634B 42 672 LD B, D
634C 4F 673 LD C, A
634D CD9964 674 CALL BANK_ENABLE ;ENABLE ADDR
6350 73 675 LD E, (HL) ;WRITE THE WORD
6351 23 676 INC HL
6352 72 677 LD HL, D
6353 2B 678 DEC HL
6354 C1 679 POP BC
6355 F1 680 POP AF
6356 CD9964 681 CALL BANK_ENABLE ;REENABLE OWNER OF ADDR
6359 C1 682 POP BC
635A F1 683 POP AF
635B C9 684 RET
685
686
687 WRITE_BS_REG (REG_ADDR: D, REG_DATA: E)
688
689
635C F5 690 WRITE_BS_REG PUSH AF ;SAVE REGISTERS
635D C5 691 PUSH BC
635E E5 692 PUSH HL
635F 62 693 LD H, D
6360 2E00 694 LD L, 0 ;HL = MEMORY MAPPED ADDR
6362 3A00C0 695 LD A, (LOWNYB) ;SAVE (0E000H)
6365 F5 696 PUSH AF
6366 7E 697 LD A, (HL) ;SAVE (HL)
6367 F5 698 PUSH AF
6368 3E07 699 LD A, 7
636A D3F5 700 OUT (SADDPT), A ;SAVE VALUES OF SOUND REGS 7 AND E
636C DBF6 701 IN A, (SDATPT)
636E 47 702 LD B, A
636F 3E0E 703 LD A, 0EH
6371 D3F5 704 OUT (SADDPT), A
6373 DBF6 705 IN A, (SDATPT)
6375 4F 706 LD C, A
6376 3E07 707 LD A, 7 ;SET IOA CHANNEL TO OUTPUT
6378 D3F5 708 OUT (SADDPT), A
637A 3E40 709 LD A, 40H
637C D3F6 710 OUT (SDATPT), A
637E 3E0E 711 LD A, 0EH
6380 D3F5 712 OUT (SADDPT), A
6382 AF 713 XOR A
6383 D3F6 714 OUT (SDATPT), A
6385 3E02 715 LD A, 2
6387 3200C0 716 LD (LOWNYB), A ;RESET NYBBLE STEERING LOGIC
638A 7B 717 LD A, E
638B 77 718 LD (HL), A ;WRITE LSN OF DATA
638C CB2F 719 SRA A

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638E	CB2F	720	SRA	A		
6390	CB2F	721	SRA	A		
6392	CB2F	722	SRA	A		
6394	77	723	LD	(HL), A	WRITE MSN OF DATA	
6395	3E07	724	LD	A, 7	RESTORE SOUND REGS	
6397	D3F5	725	OUT	(SADDPT), A		
6399	78	726	LD	A, B		
639A	D3F6	727	OUT	(SDATPT), A		
639C	3E0E	728	LD	A, 0EH		
639E	D3F5	729	OUT	(SADDPT), A		
63A0	79	730	LD	A, C		
63A1	D3F6	731	OUT	(SDATPT), A		
63A3	F1	732	POP	AF		
63A4	77	733	LD	(HL), A	RESTORE (HL)	
63A5	F1	734	POP	AF		
63A6	3200C0	735	LD	(LOWNYB), A	RESTORE (0E000H)	
63A9	E1	736	POP	HL	RESTORE REGISTERS	
63AA	C1	737	POP	BC		
63AB	F1	738	POP	AF		
63AC	C9	739	RET			
		740				
		741				
		742				
		743				
		744				
63AD	F5	745	READLSN_REG	PUSH	AF	SAVE REGISTER
63AE	C5	746		PUSH	BC	
63AF	E5	747		PUSH	HL	
63B0	62	748		LD	H, D	
63B1	2E00	749		LD	L, 0	HL = MEMORY MAPPED ADDR
63B3	3A00C0	750		LD	A, (LOWNYB)	SAVE (0E000H)
63B4	F5	751		PUSH	AF	
63B7	7E	752		LD	A, (HL)	SAVE (HL)
63B9	F5	753		PUSH	AF	
63BA	3E07	754		LD	A, 7	
63BB	D3F5	755		OUT	(SADDPT), A	SAVE VALUES OF SOUND REGS 7 AND E
63BD	D3F6	756		N	A, (SDATPT)	
63BF	47	757		LD	B, A	
63C0	3E0E	758		LD	A, 0EH	
63C2	D3F5	759		OUT	(SADDPT), A	
63C4	D3F6	760		IN	A, (SDATPT)	
63C6	4F	761		LD	C, A	
63C7	C5	762		PUSH	BC	
63C8	3E07	763		LD	A, 7	SET IOA CHANNEL TO OUTPUT
63CA	D3F5	764		OUT	(SADDPT), A	
63CC	3E40	765		LD	A, 40H	
63CE	D3F6	766		OUT	(SDATPT), A	
63D0	3E0E	767		LD	A, 0EH	
63D2	D3F5	768		OUT	(SADDPT), A	
63D4	AF	769		XOR	A	
63D5	D3F6	770		OUT	(SDATPT), A	
63D7	3E02	771		LD	A, 2	
63D9	3200C0	772		LD	(LOWNYB), A	RESET NYBBLE STEERING LOGIC
63DC	7E	773		LD	A, (HL)	READ LSN OF DATA
63DD	E60F	774		AND	0FH	
63DF	4F	775		LD	C, A	
63E0	63	776		LD	H, E	
63E1	7E	777		LD	A, (HL)	READ MSN OF DATA
63E2	CB27	778		SLA	A	
63E4	CB27	779		SLA	A	
63E6	CB27	780		SLA	A	
63E8	CB27	781		SLA	A	
63EA	B1	782		OR	C	
63EB	5F	783		LD	E, A	RETURN BYTE DATA IN E
63EC	C1	784		POP	BC	
63ED	3E07	785		LD	A, 7	RESTORE SOUND REGS
63EF	D3F5	786		OUT	(SADDPT), A	
63F1	78	787		LD	A, B	
63F2	D3F6	788		OUT	(SDATPT), A	
63F4	3E0E	789		LD	A, 0EH	
63F6	D3F5	790		OUT	(SADDPT), A	
63F8	79	791		LD	A, C	
63F9	D3F6	792		OUT	(SDATPT), A	
63FB	F1	793		POP	AF	
63FD	77	794		LD	(HL), A	RESTORE (HL)
63FD	F1	795		POP	AF	
63FE	3200C0	796		LD	(LOWNYD), A	RESTORE (0E000H)
6401	E1	797		POP	HL	RESTORE REGISTERS
6402	C1	798		POP	BC	
6403	F1	799		POP	AF	
6404	C9	800		RET		
		801				
		802				
		803				
		804				
		805				
6405	F5	806	GET_STATUS	PUSH	AF	SAVE SAVE REGS
6406	D5	807		PUSH	DE	
6407	78	808		LD	A, B	
6409	FEFE	809		CP	0FEH	
640A	2E2E	810		JR	Z, GS_EXT	IF BANK = 254
640C	FEFF	811		CP	0FFH	
640E	2E1D	812		JR	Z, GS_HOME	IF BANK = 255
6410	A7	813		AND	A	
6411	2E1F	814		JR	Z, GS_DOC	IF BANK = 0
6413	1680	815		LD	D, BNA	HERE IF EXP. BANK
6415	58	816		LD	E, B	
6416	CD5C63	817		CALL	WRITE_BS_REG	
6419	1640	818		LD	D, HS_LSN	
641B	1E80	819		LD	E, HS_MSN	



6410	CDAD63	820	CALL	READ_BS_REG	I READ HS
6420	7B	821	LD	A, E	
6421	2F	822	CPL		
6422	4F	823	LD	C, A	
6423	16A0	824	LD	D, STA_L	
6425	1EC0	825	LD	E, STA_0	
6427	CDAD63	826	CALL	READ_BS_REG	I READ STATUS NYBBLES
642A	43	827	LD	B, E	
642B	1B1D	828	JR	OS_EXIT	
642D	010000	829	LD	BC, 0	I RETURN 0 FOR HOME BANK STATUS
6430	1B1B	830	JR	OS_EXIT	
6432	DBF4	831	IN	A, (DKHSPT)	I RETURN DOCK BANK STATUS
6434	2F	832	CPL		
6435	47	833	LD	B, A	
6436	0E00	834	LD	C, 0	
6438	1B10	835	JR	OS_EXIT	
643A	DBFF	836	IN	A, (HREXPT)	
643C	E680	837	AND	80H	I CLEAR ALL BITS EXCEPT BIT 7
643E	2F	838	CPL		
643F	07	839	RLCA		I PUT ACTIVE LOW BIT IN BIT ZERO
6440	47	840	LD	B, A	
6441	DBF4	841	IN	A, (DKHSPT)	
6443	2F	842	CPL		
6444	E601	843	AND	1	
6446	BC	844	OR	B	
6447	47	845	LD	B, A	
6448	0E00	846	LD	C, 0	
644A	D1	847	MOV	DE	I RESTORE D, C
644B	F1	848	MOV	AF	
644C	C7	849	RET		
		850			
		851			
		852			
		853			
		854			
644D	05	855	GET_CHUNK	PUSH	BC
644E	7C	856		LD	A, H
644F	0605	857		LD	R, 5
6451	0B3F	858	GC_SHIFT	SRL	A
6453	10FC	859		DWZ	GC_SHIFT
6455	3C	860		INC	A
6456	47	861		LD	B, A
6457	AF	862		XOR	A
6458	37	863		SCF	
6459	17	864	GC_ROLL	RLA	
645A	14FD	865		DWZ	GC_ROLL
645C	C1	866		POP	BC
645D	C9	867		RET	
		868			
		869			
		870			
		871			
		872			
645E	05	873	GET_NUMBER	PUSH	BC
645F	D5	874		PUSH	DE
6460	CDAD64	875		CALL	GET_CHUNK
6463	4F	876		LD	C, A
6464	3A1563	877		LD	A, (BS_MAX_BANK)
6467	A7	878		AND	A
6468	2B0A	879		JR	Z, ON_RD_DOCK
646A	47	880		LD	B, A
646B	5B	881	ON_CHECK	LD	E, B
646C	CD0564	882		CALL	GET_STATUS
646F	A1	883		AND	C
6470	2923	884		JR	Z, ON_EXP
6472	10F7	885		DWZ	ON_CHECK
6474	DBF4	886	ON_RD_DOCK	IN	A, (DKHSPT)
6476	2F	887		CPL	
6477	A1	888		AND	C
6478	2B1B	889		JR	Z, ON_DOCK
647A	0D	890		DEC	C
647B	2011	891		JR	NZ, ON_HOME
647D	DBFF	892		IN	A, (HREXPT)
647F	E680	893		AND	80H
6481	57	894		LD	D, A
6482	DBF4	895		IN	A, (DKHSPT)
6484	E601	896		AND	1
6486	0F	897		RRCA	
6487	A2	898		AND	D
6488	2B04	899		JR	Z, ON_HOME
648A	3EFE	900		LD	A, OFEH
648C	1B08	901		JR	ON_EXIT
648E	3EFF	902	ON_HOME LD	A, OFFH	I IN HOME BANK, SO RETURN 255
6490	1B04	903		JR	ON_EXIT
6492	AF	904	ON_DOCK	XOR	A
6493	1B01	905		JR	ON_EXIT
6495	7B	906	ON_EXP	LD	A, B
6496	D1	907	ON_EXIT	POP	DE
6497	C1	908		POP	BC
6498	C9	909		RET	
		910			
		911			
		912			
		913			
		914			
6499	F5	915	BANK_ENABLE	PUSH	AF
649A	C5	916		PUSH	BC
649B	D5	917		PUSH	DE
649C	E5	918		PUSH	HL
649D	60	919		LD	H, B

649E	3A1563	920		LD	A, (BS_MAX_BANK):10ET LARGEST BANK NUMBER
64A1	A7	921		AND	A
64A2	2911	922		JR	Z, BE_SKIP 11F NO EXP. BANKS
64A4	1680	923		LD	D, BNA
64A6	1E00	924		LD	E, 0
64A8	CD5C63	925		CALL	WRITE_BS_REG
64AB	16A0	926		LD	D, HSP
64AD	F5	927		PUSH	AF
64AE	79	928		LD	A, C
64AF	2F	929		CPL	
64B0	5F	930		LD	E, A
64B1	F1	931		POP	AF
64B2	CD5C63	932		CALL	WRITE_BS_REG 1TURN OFF APPROPRIATE BITS OF
		933			1 ALL EXP. BANKS
64B5	79	934	BE_SKIP	LD	A, B
64B6	A7	935		AND	A
64B7	2011	936		JR	NZ, BE_NTDCK
64B9	79	937		LD	A, C
64BA	FEFF	938		CP	OFFH
64BC	2806	939		JR	Z, BE_EXT_OK
64BE	DBFF	940		IN	A, (HREXT) (HERE FOR DOCK
64C0	CBFF	941		RES	7, A
64C2	D3FF	942		OUT	(HREXT), A
64C4	79	943	BE_EXT_OK	LD	A, C
64C5	2F	944		CPL	
64C6	D3F4	945		OUT	(DNHSPT), A 1ENABLE DOCK
64C8	184F	946		JR	BE_EXIT
64CA	79	947	BE_NTDCK	LD	A, B 1CHECK IF EXT.
64CB	FEFE	948		CP	OFFH
64CD	201D	949		JR	NZ, BE_NTEXT
64CF	DBFF	950		IN	A, (HREXT) 1HERE FOR EXT.
64D1	17	951		RLA	
64D2	CB19	952		RR	C
64D4	2F	953		CCF	
64D5	1F	954		PRA	
64D6	D3FF	955		OUT	(HREXT), A
64D8	CB7F	956		BIT	7, A
64DA	2003	957		JR	NZ, BE_SET
64DC	DBF4	958		IN	A, (DNHSPT)
64DE	CEB7	959		RES	0, A
64E0	DBF4	960		OUT	(DNHSPT), A
64E2	1835	961		JR	BE_EXIT
64E4	DBF4	962	BE_SET	IN	A, (DNHSPT)
64E6	CBF7	963		SET	0, A
64E8	D3F4	964		OUT	(DNHSPT), A
64EA	182D	965		JR	BE_EXIT
64EC	DBF4	966	BE_NTEXT	IN	A, (DNHSPT) 1DISABLE DOCK
64EE	2F	967		CPL	
64EF	5F	968		LD	E, A
64F0	79	969		LD	A, C
64F1	2F	970		CPL	
64F2	B3	971		OR	E
64F3	2F	972		CPL	
64F4	D3F4	973		OUT	(DNHSPT), A
64F6	CB41	974		BIT	0, C
64F8	200C	975		JR	NZ, BE_CHL_HOME
64FA	DBFF	976		IN	A, (HREXT) 1DISABLE EXT.
64FC	CBFF	977		RES	7, A
64FE	D3FF	978		OUT	(HREXT), A
6500	DBF4	979		IN	A, (DNHSPT)
6502	CB37	980		RES	0, A
6504	18F4	981		OUT	(DNHSPT), A
6506	78	982	BE_CHL_HOME	LD	A, B 1CHECK IF HOME
6507	FEFF	983		CP	OFFH
6509	280E	984		JR	Z, BE_EXIT 1IS HOME, SO DONE
650B	1680	985		LD	D, BNA 1WRITE NEW EXP. BANK STATUS
650D	58	986		LD	E, B
650E	CD5C63	987		CALL	WRITE_BS_REG
6511	16A0	988		LD	D, HS
6513	79	989		LD	A, C
6514	2F	990		CPL	
6515	5F	991		LD	E, A
6516	CD5C63	992		CALL	WRITE_BS_REG
6519	E1	993	BE_EXIT	POP	HL 1RESTORE REGISTERS
651A	D1	994		POP	DE
651B	C1	995		POP	BC
651C	F1	996		POP	AF
651D	C9	997		RET	
		998			
		999			
		1000			1 SAVE_BANK_STATUSES (STATUS_ADDR: 1X)
		1001			
		1002			1 PUSHES THE STATUS OF ALL BANKS ON THE STACK
		1003			
		1004			
651E	F5	1005	SAVE_STATUS	PUSH	AF 1SAVE REGS
651F	C5	1006		PUSH	BC
6520	D5	1007		PUSH	DE
6521	DBFF	1008		IN	A, (HREXT)
6523	00	1009		NOP	1SAVE EXT. BANK STATUS
6524	00	1010		NOP	1LEAVE BITS 0-6 ALONE! NOFS PUT IN
6525	DD7700	1011		LD	(1X), A 1 TO KEEP ADDRS THE SAME
6526	DD23	1012		INC	1X
652A	DBF4	1013		IN	A, (DNHSPT) 1GET DOCK BANK STATUS
652C	DD7700	1014		LD	(1X), A
652F	DD23	1015		INC	1X
6531	3A1563	1016		LD	A, (BS_MAX_BANK):10ET NUMBER OF BANKS
6534	A7	1017		AND	A
6535	280D	1018		JR	Z, SS_EXIT
6537	47	1019		LD	B, A 1SET UP COUNTER
6538	58	1020	SS_LOOP	LD	E, B 1BANK NUMBER INTO E
6539	CD0564	1021		CALL	DET_STATUS 1GET BANK STATUS OF BANK: 0B

653C	D07190	1022	LD	(IX), C	
653F	D029	1023	INC	IX	
6541	43	1024	LD	B, E	
6542	10F4	1025	DJNZ	SS_LOOP	1DO FOR ALL
6544	D028	1026	SS_EXIT	DEC	IX
6546	D1	1027	POP	DE	1RESTORE REGS
6547	C1	1028	POP	BC	
6548	F1	1029	POP	AF	
6549	C9	1030	RET		
		1031			
		1032			
		1033		RESTORE_BANK_STATUSES (STATUS_ADDR: IX)	
		1034			
		1035		RESTORES BANK STATUS TO ALL BANKS	
		1036			
		1037			
654A	F5	1038	RESTORE_STATUS	PUSH	AF 1SAVE REGS
654B	C5	1039		PUSH	BC
654C	15	1040		PUSH	DE
654D	D07E00	1041		LD	A, (IX) 1GET EXT. ROM STATUS
6550	D3FF	1042		OUT	(HREXT), A
6552	D023	1043		INC	IX
6554	D07E00	1044		LD	A, (IX) 1GET DOCK BANK STATUS
6557	D3F4	1045		OUT	(DOHSPT1), A
6559	D023	1046		INC	IX
655B	3A1563	1047		LD	A, (BS_MAX_BANK) 1GET NUMBER OF BANKS
655E	A7	1048		AND	A
655F	2608	1049		JR	Z, RS_EXIT
6561	47	1050		LD	B, A 1SET UP COUNTER
6562	D04E00	1051	RS_LOOP	LD	C, (IX)
6565	CD9964	1052		CALL	BANK_ENABLE 1WRITE BANK STATUS OF BANK #B
6566	D023	1053		INC	IX
656A	10F6	1054		DJNZ	RS_LOOP 1DO FOR ALL
656C	D028	1055	RS_EXIT	DEC	IX
656E	D1	1056		POP	DE 1RESTORE REGS
656F	C1	1057		POP	BC
6570	F1	1058		POP	AF
6571	C9	1059		RET	
		1060			
		1061			
		1062		GOTO_BANK (BANK), HORIZONTAL_SELECT, ADDR: PASSED ON STACK	
		1063			
		1064			
		1065		SETS IN: THE DESTINATION BANK AND JUMPS WITHOUT RETURN TO ADDRESS	
		1066		IN BANK.	
		1067			
		1068			
6572	D0210000	1069	GOTO_BANK	LD	IX, 0 1SET IX TO 0
6576	D029	1070		ADD	IX, SP
6579	D07190	1071		LD	(IX), C 1SAVE BC AND TRASH RET ADDR
657B	D07001	1072		LD	(IX+1), B
657E	D04E02	1073		LD	C, (IX+2) 1GET FARMS FOR BANK_ENABLE
6581	D04603	1074		LD	B, (IX+3)
6584	CD9964	1075		CALL	BANK_ENABLE
6587	C1	1076		POP	BC 1RESTORE BC
6589	DDE1	1077		POP	IX 1TRASH FARMS TO GOTO_BANK
658A	DDE1	1078		POP	IX 1GET ADDR
658C	DDE9	1079	JMP IX	JP	(IX)
		1080			
		1081			
		1082		CALL_BANK (ADDR, BANK, HORIZONTAL_SELECT, PRM_OUT, PRM_IN)	
		1083		ALL INPUT PARAMETERS ARE PUSHED ON THE STACK	
		1084			
		1085		CLOBBERS IX	
		1086			
		1087			
		1088		SETS UP THE BANK AND MAKES A JUMP WITH RETURN ADDRESS TO ADDRESS	
		1089		IN BANK.	
		1090			
		1091			
658E		1092	BS_STACK	DEFS	64
659E		1093	BS_SP	DEFS	2
		1094			
		1095			
65D0	E3	1096	CALL_BANK	EX	(SP), HL 1GET RET ADDR
65D1	D02ACE65	1097		LD	IX, (BS_SP1
65D5	D028	1098		DEC	IX
65D7	D07400	1099		LD	(IX), H
65DA	D028	1100		DEC	IX
65DC	D07500	1101		LD	(IX), L 1PUSH HL ON BS_STACK
65DF	E1	1102		POP	HL
65E0	E3	1103		EX	(SP), HL 1GET PRM_IN
65E1	D028	1104		DEC	IX
65E3	D07400	1105		LD	(IX), H
65E6	D028	1106		DEC	IX
65E9	D07500	1107		LD	(IX), L 1PUSH PRM_IN ON BS_STACK
65EB	D022CE65	1108		LD	(BS_SP), IX 1UPDATE BS_SP
65EF	D5	1109		PUSH	DE 1SAVE REGS
65F0	C5	1110		PUSH	BC
65F1	F5	1111		PUSH	AF
65F2	210000	1112		LD	HL, 0
65F5	39	1113		ADD	HL, SP 1HL = SP
65F6	54	1114		LD	D, H
65F7	50	1115		LD	E, L
65F8	3A1563	1116		LD	A, (BS_MAX_BANK)
65FB	4F	1117		LD	C, A
65FC	0600	1118		LD	B, 0
65FE	03	1119		INC	BC
65FF	03	1120		INC	BC 1BC = MAX_BANK + 2
6600	A7	1121		AND	A

6601	ED42	1122	SBC	HL, BC	
6603	F9	1123	LD	SP, HL	
6604	DD210000	1124	LD	IX, 0	
6608	DD19	1125	ADD	IX, DE	
660A	EB	1126	EX	DE, HL	IDE, HL NOW CONTAIN DEST. SRC
		1127			POINTERS FOR A BLOCK MOVE
660B	DD4E08	1128	LD	C, (IX+PRM_OUT1)	
660E	DD4608	1129	LD	B, (IX+PRM_OUT1)	
6611	3E0E	1130	LD	A, 14	
6613	81	1131	ADD	A, C	
6614	4F	1132	LD	C, A	
6615	3001	1133	JR	NC, CB_NC1	IBC = PRM_OUT + 14
6617	04	1134	INC	B	
6618	EDB0	1135	LDIR		MAKE ROOM FOR BANK STATUS
661A	D5	1136	PUSH	DE	
661B	DDE1	1137	POP	IX	IX = DE
661D	CD1E65	1138	CALL	SAVE_STATUS	
6620	DD210000	1139	LD	IX, 0	
6624	DD39	1140	ADD	IX, SP	
6626	DD4E0A	1141	LD	C, (IX+HOR_SEL1)	GET PARAMS FOR BANK_ENABLE
6629	DD4608	1142	LD	B, (IX+BANK1)	
662C	CD9964	1143	CALL	BANK_ENABLE	ENABLE DESTINATION BANK
662F	F1	1144	POP	AF	RESTORE REGS
6630	C1	1145	POP	BC	
6631	D1	1146	POP	DE	
6632	E1	1147	POP	HL	
6633	DDE1	1148	POP	IX	TRASH PARAMS TO CALL_BANK AND GET ADDR
6635	DDE1	1149	POP	IX	
6637	DDE1	1150	POP	IX	
6639	CD8C65	1151	CALL	JMP1X	CALL ADDRESS IN IX
663C	F5	1152	PUSH	AF	SAVE REGS
663D	C5	1153	PUSH	BC	
663E	D5	1154	PUSH	DE	
663F	E5	1155	PUSH	HL	
6640	DD2ACE65	1156	LD	IX, (BS_SP)	
6644	DD4E00	1157	LD	C, (IX)	
6647	DD23	1158	INC	IX	
6649	DD4600	1159	LD	B, (IX)	
664C	DD23	1160	INC	IX	POP PRM_IN OFF BS_STACK
664E	DD22CE65	1161	LD	(BS_SP), IX	UPDATE BS_SP
6652	DD210000	1162	LD	IX, 0	
6656	DD39	1163	ADD	IX, SP	
6658	3E08	1164	LD	A, 8	
665A	81	1165	ADD	A, C	
665B	4F	1166	LD	C, A	
665C	3001	1167	JR	NC, CB_NC2	IBC = PRM_IN + 8
665E	04	1168	INC	B	
665F	DD09	1169	ADD	IX, BC	IX = SP + PRM_IN + 8
6661	DD05	1170	PUSH	IX	
6663	E1	1171	POP	HL	HL = IX
6664	D8	1172	DEC	HL	HL = SRC POINTER FOR BLOCK MOVE
6665	DD4A65	1173	CALL	RESTORE_STATUS	RESTORE STATUS OF ALL BANKS
6668	DD05	1174	PUSH	IX	
666A	D1	1175	POP	HL	HL = DEST POINTER FOR BLOCK MOVE
666B	EDB6	1176	LDIR		DEALLOCATE SPACE FOR BANK STATUS
666D	EB	1177	EX	DE, HL	
666E	D3	1178	INC	HL	
666F	F9	1179	LD	SP, HL	RESTORE SP
6670	DD2ACE65	1180	LD	IX, (BS_SP)	
6674	DD4E00	1181	LD	C, (IX)	
6677	DD23	1182	INC	IX	
6679	DD4600	1183	LD	B, (IX)	
667C	DD23	1184	INC	IX	POP RET ADDR OFF BS_STACK
667E	DD22CE65	1185	LD	(BS_SP), IX	UPDATE BS_SP
6682	C5	1186	PUSH	BC	
6683	DDE1	1187	POP	IX	
6685	E1	1188	POP	HL	RESTORE REGS
6686	D1	1189	POP	DE	
6687	C1	1190	POP	BC	
6688	F1	1191	POP	AF	
6689	DDE5	1192	PUSH	IX	PUT RET ADDR ON STACK
668B	C9	1193	RET		
		1194			
		1195			
		1196			
		1197			HERE ARE SOME EQUATES WHICH ARE USED BY XFER_BYTES AND THE ROUTINES IT
		1198			CALLS.
		1199			
		1200	DIRECTION	EQU	0
		1201	BUF_PTR	EQU	0
		1202	LENGTH	EQU	2
		1203	DEST_ADDR	EQU	4
		1204	SRC_ADDR	EQU	6
		1205	DEST_BANK	EQU	8
		1206	SRC_BANK	EQU	9
		1207			
		1208			MOVE_BYTES (BYTES_TO_MOVE, DE, DIRECTION, A)
		1209			
		1210			
668C	E5	1211	MOVE_BYTES:	PUSH	HL
668D	D5	1212		PUSH	DE
668E	C5	1213		PUSH	BC
668F	48	1214		LD	C, B
6690	DD4609	1215		LD	B, (IX+SRC_BANK)
6693	CD9964	1216		CALL	BANK_ENABLE
6696	42	1217		LD	B, D
6697	4B	1218		LD	C, E
6698	DD5E00	1219		LD	E, (IX+BUF_PTR)
669B	DD5601	1220		LD	D, (IX+BUF_PTR+1)
669E	DD6E06	1221		LD	L, (IX+SRC_ADDR)
66A1	DD6607	1222		LD	H, (IX+SRC_ADDR+1)

66A4	07	1223	RLCA		
66A5	0F	1224	RRCA		
66A6	3805	1225	JR	C, MB_RV1	IF A < 0
66A8	EDB0	1226	LDIR		
66AA	09	1227	ADD	HL, DC	INCREMENT POINTER
66AB	1805	1228	JR	MB_UP1	
66AD	EDB8	1229	LDIR		
66AF	A7	1230	AND	A	
66B0	ED42	1231	SBC	HL, BC	DECREMENT POINTER
66B2	DD7506	1232	LD	(IX+SRC_ADDR), L	STORE NEW POINTER VALUE
66B5	DD7407	1233	LD	(IX+SRC_ADDR+1), H	
66B8	C1	1234	POP	BC	
66B9	E1	1235	POP	HL	
66BA	E5	1236	PUSH	HL	
66BB	C5	1237	PUSH	BC	
66BC	DD4608	1238	LD	B, (IX+DEST_BANK)	
66BF	CD9964	1239	CALL	BANK_ENABLE	SELECT DEST BANK
66C2	44	1240	LD	B, H	MOVE FROM STACK TO DEST
66C3	4D	1241	LD	C, L	
66C4	DD5604	1242	LD	E, (IX+DEST_ADDR)	
66C7	DD5405	1243	LD	D, (IX+DEST_ADDR+1)	
66CA	DD6E00	1244	LD	L, (IX+BUF_PTR)	
66CD	DD6601	1245	LD	H, (IX+BUF_PTR+1)	
66D0	07	1246	RLCA		
66D1	0F	1247	RRCA		
66D2	3805	1248	JR	C, MB_RV2	IF A < 0
66D4	EDB0	1249	LDIR		
66D6	09	1250	ADD	HL, BC	INCREMENT POINTER
66D7	1805	1251	JR	MB_UP2	
66D9	EDB8	1252	LDIR		
66DB	A7	1253	AND	A	
66DC	ED42	1254	SBC	HL, BC	DECREMENT POINTER
66DE	DD7504	1255	LD	(IX+DEST_ADDR), L	STORE NEW POINTER VALUE
66E1	DD7405	1256	LD	(IX+DEST_ADDR+1), H	
66E4	C1	1257	POP	BC	RESTORE REGISTERS
66E5	D1	1258	POP	DE	
66E6	E1	1259	POP	HL	
66E7	C9	1260	RET		
		1261			
		1262			
		1263		CREATE_BITMAP (ADDR: HL; BITMAP: A)	
		1264			
		1265			
66E8	54	1266	LD	D, H	SAVE START ADDR
66E9	5D	1267	LD	E, L	
66EA	DD4E02	1268	LD	D, (IX+LENGTH)	
66ED	DD4603	1269	LD	B, (IX+LENGTH+1)	
66F0	DD7E00	1270	LD	A, (IX+DIRECTION)	
66F3	07	1271	RLCA		CALCULATE END ADDR
66F4	0F	1272	RRCA		
66F5	3803	1273	JR	C, CB_SUB	IF A < 0
66F7	09	1274	ADD	HL, BC	
66F8	1802	1275	JR	CB_CONT	
66FA	ED42	1276	SBC	HL, DC	
66FC	CD4D04	1277	CALL	GET_CHUNK	GET END CHUNK BIT
66FF	2F	1278	CPL		
6700	47	1279	LD	B, A	
6701	EB	1280	EX	DE, HL	
6702	CD4D04	1281	CALL	GET_CHUNK	GET START CHUNK BIT
6705	2F	1282	CPL		
6706	4F	1283	LD	L, A	
6707	A8	1284	XOR	B	
6708	2816	1285	JR	Z, CB_EXIT	
670A	79	1286	LD	A, C	IF START AND END CHUNKS
670B	A0	1287	AND	B	ARE NOT THE SAME
670C	47	1288	LD	H, A	PUT START AND END BITS TOGETHER AND
670D	0E00	1289	LD	C, 0	FILL IN BETWEEN THEM WITH ZEROS
670F	37	1290	SCF		
6710	78	1291	LD	A, B	TEST NEXT BIT
6711	CB11	1292	RL	C	
6713	A1	1293	AND	C	
6714	20FA	1294	JR	NZ, CB_NB1	OTHERWISE, FOUND FIRST ZERO
6716	78	1295	LD	A, B	TEST NEXT BIT
6717	CB11	1296	RL	C	
6719	A1	1297	AND	C	
671A	2804	1298	JR	Z, CB_EXIT	FOUND LAST ZERO
671C	A8	1299	XOR	B	OTHERWISE, UPDATE BITMAP
671D	47	1300	LD	B, A	
671E	18F6	1301	JR	CB_NB2	
6720	78	1302	LD	A, B	RETURN BITMAP
6721	C9	1303	RET		
		1304			
		1305			
		1306		XFER_BYTES (DIRECTION, LENGTH, DEST_ADDR, SRC_ADDR, DEST_BANK,	
		1307		SRC_BANK: PASSED ON STACK IN ORDER SHOWN; STATUS_CODE: A)	
		1308			
		1309		ALL PARAMETERS ON STACK HAVE OFFSETS DEFINED ABOVE.	
		1310			
		1311			
6722	F5	1312	PUSH	AF	SAVE REGS
6723	C5	1313	PUSH	BC	
6724	D5	1314	PUSH	DE	
6725	E5	1315	PUSH	HL	
6726	210000	1316	LD	HL, 0	
6729	39	1317	ADD	HL, SP	
672A	110A00	1318	LD	DE, 10	
672D	19	1319	ADD	HL, DE	
672E	EB	1320	EX	DE, HL	DE POINTS TO START OF PARAMS
672F	3A1563	1321	LD	A, (BS_MAX_BANK)	

6732	4F	1322	LD	C, A
6733	0600	1323	LD	B, 0
6735	210000	1324	LD	HL, 0
6738	39	1325	ADD	HL, SP
6739	A7	1326	AND	A
673A	ED42	1327	SBC	HL, BC
673C	2B	1328	DEC	HL
673D	2B	1329	DEC	HL
673E	E5	1330	PUSH	HL
673F	DDE1	1331	POP	IX
6741	D0F9	1332	LD	SP, IX
6743	DD1E65	1333	CALL	SAVE_STATUS
6746	D5	1334	PUSH	DE
6747	DDE1	1335	POP	IX
6749	DD6A06	1336	LD	L, (IX+SRC_ADDR)
674C	DD6A07	1337	LD	H, (IX+SRC_ADDR+1)
674F	CDE866	1338	CALL	CREATE_BITMAP
6752	F5	1339	PUSH	AF
6753	DD6E04	1340	LD	L, (IX+DEST_ADDR)
6756	DD6605	1341	LD	H, (IX+DEST_ADDR+1)
6759	CDE866	1342	CALL	CREATE_BITMAP
675C	4F	1343	LD	C, A
675D	F1	1344	POP	AF
675E	47	1345	LD	B, A
675F	DD7E09	1346	LD	A, (IX+SRC_BANK)
6762	DD5608	1347	LD	D, (IX+DEST_BANK)
6765	84	1348	CP	D
6766	2005	1349	JR	NZ, XB_DIFF_BANKS
6768	78	1350	LD	A, B
6769	A1	1351	AND	C
676A	47	1352	LD	B, A
676B	180B	1353	JR	XB_DO_MOVE
676D	78	1354	LD	A, B
676E	81	1355	OR	C
676F	FEFF	1356	CP	OFFH
6771	202D	1357	JR	NZ, XB_OVERLAP
6773	58	1358	LD	E, B
6774	42	1359	LD	B, D
6775	CD9964	1360	CALL	BANK_ENABLE
6778	DD4609	1361	LD	B, (IX+SRC_BANK)
677B	4B	1362	LD	C, E
677C	CD9964	1363	CALL	BANK_ENABLE
677F	DD6E06	1364	LD	L, (IX+SRC_ADDR)
6782	DD4607	1365	LD	H, (IX+SRC_ADDR+1)
6785	DD5E04	1366	LD	E, (IX+DEST_ADDR)
6788	DD5605	1367	LD	D, (IX+DEST_ADDR+1)
678B	DD4E02	1368	LD	C, (IX+LENGTH)
678E	DD4603	1369	LD	B, (IX+LENGTH+1)
6791	DD7E00	1370	LD	A, (IX+DIRECTION)
6794	07	1371	RLCA	
6795	0F	1372	RRCA	
6796	3804	1373	JR	C, XB_REVERSE
6798	EDB0	1374	LDIR	
679A	1852	1375	JR	XB_EXIT
679C	EDB8	1376	LDUR	XB_EXIT
679E	184E	1377	JR	HL, HSTBOT
67A0	21C05C	1378	LD	BC
67A3	C5	1379	PUSH	BC
67A4	06FF	1380	LD	B, 255
67A6	CD1663	1381	CALL	GET_WORD
67A9	C1	1382	POP	BC
67AA	110002	1383	LD	DE, STKSZ
67AD	A7	1384	AND	A
67AE	ED52	1385	SBC	HL, DE
67B0	112000	1386	LD	DE, FREE_BYTES
67B3	19	1387	ADD	HL, DE
67B4	EB	1388	EX	DE, HL
67B5	210000	1389	LD	HL, 0
67B8	39	1390	ADD	HL, SP
67B9	13	1391	INC	DE
67BA	A7	1392	AND	A
67BB	ED52	1393	SBC	HL, DE
67BD	3004	1394	JR	NC, XB_SPACE
67BF	7E01	1395	LD	A, 1
67C1	182B	1396	JR	XB_EXIT
67C3	1B	1397	DEC	DE
67C4	EB	1398	EX	DE, HL
67C5	F0	1399	LD	SP, HL
67C6	12	1400	INC	DE
67C7	DD7E00	1401	LD	A, (IX+DIRECTION)
67CA	DD7500	1402	LD	(IX+BUF_PTR), L
67CD	DD7401	1403	LD	(IX+BUF_PTR+1), H
67D0	DD6E02	1404	LD	L, (IX+LENGTH)
67D3	DD6603	1405	LD	H, (IX+LENGTH+1)
67D6	A7	1406	AND	A
67D7	ED52	1407	SBC	HL, DE
67D9	3905	1408	JR	C, XB_LAST_MOVE
67DB	CD9C66	1409	CALL	MOVE_BYTES
67DE	18F6	1410	JR	XB_MOVE_LOOP
67E0	19	1411	ADD	HL, DE
67E1	EB	1412	EX	DE, HL
67E2	CD9C66	1413	CALL	MOVE_BYTES
67E5	EB	1414	EX	DE, HL
67E6	DD6E00	1415	LD	L, (IX+BUF_PTR)
67E9	DD6601	1416	LD	H, (IX+BUF_PTR+1)
67EC	19	1417	ADD	HL, DE
67ED	F9	1418	LD	SP, HL
67EE	AF	1419	XOR	A
67EF	DD210000	1420	LD	IX, 0
67F3	DD39	1421	ADD	IX, SP
67F5	CD4A65	1422	CALL	RESTORE_STATUS

67F8	DD23	1423	INC	IX	
67FA	DDF9	1424	LD	SP, IX	
67FC	E1	1425	POP	HL	RESTORE REGS
67FE	D1	1426	POP	DE	
67FF	C1	1427	POP	BC	
6800	F1	1428	POP	AF	
6800	DDE1	1429	POP	IX	CLEAN UP PARAMS
6802	DDE3	1430	EX	(SP), IX	
6804	DDE1	1431	POP	IX	
6806	DDE3	1432	EX	(SP), IX	
6808	DDE1	1433	POP	IX	
680A	DDE3	1434	EX	(SP), IX	
680C	DDE1	1435	POP	IX	
680E	DDE3	1436	EX	(SP), IX	
6810	DDE1	1437	POP	IX	
6812	DDE3	1438	EX	(SP), IX	
6814	C9	1439	RET		
		1440	!		
		1441	!		
		1442	!	GOTO_EXT_INIT (ADDR: HL)	
		1443	!		
		1444	!		
6815	DDE1	1445	GOTO_EXT	FUF	IX
6817	F5	1446	PUSH	AF	TRASH HL ADDR
6818	DEFF	1447	IN	A, (HREXPT)	
681A	CBFF	1448	SET	7, A	
681C	D3FF	1449	OUT	(HREXPT), A	
681E	3E01	1450	LD	A, I	
6820	D3F4	1451	OUT	(DIKHSPT), A	
6822	F1	1452	POP	AF	
6823	E9	1453	JP	(HL)	
		1454	END		

LOC	OBJ CODE	M	STMT	SOURCE STATEMENT	ASM 5.9
			1	DISPATCH	EQU 6200H
			2	INT	EQU 62A0H
			3	GET_WORD	EQU 6316H
			4	PUT_WORD	EQU 63BBH
			5	GET_STATUS	EQU 6405H
			6	GET_NUMBER	EQU 645EH
			7	BANK_ENABLE	EQU 6499H
			8	SAVE_STATUS	EQU 651EH
			9	RESTORE_STATUS	EQU 654AH
			10	BS_STACK	EQU 658EH
			11	BS_SP	EQU 65CEH
			12	GOTO_BANK	EQU 6572H
			13	CALL_BANK	EQU 65D0H
			14	MOVE_BYTES	EQU 668CH
			15	CREATE_BITMAP	EQU 66ERH
			16	XFER_BYTES	EQU 6722H
			17		
			18	! HERE IS THE FIXUP TABLE FOR THE VIDEO MODE CHANGER. IT DEFINES THE	
			19	! LOCATIONS IN RAM WHICH MUST BE UPDATED WHEN MOVED FROM CHUN: 3 TO CHUN: 7	
			20	! OR VICE-VERSA. THE ADDRESSES IN THE TABLE ARE DEFINED AS CHUN: 3 ADDRESSES	
			21		
			22		
1D00			23	ORG	1D00H
			24		
			25		
1D00	3262		26	FIXTBL	DEFW DISPATCH+32H
1D02	4062		27		DEFW DISPATCH+40H
1D04	7262		28		DEFW DISPATCH+72H
1D06	AB62		29		DEFW DISPATCH+0ABH
			30		
1D08	8862		31		DEFW INT+0AH
1D0A	CD62		32		DEFW INT+1FH
1D0C	D362		33		DEFW INT+25H
1D0E	DC62		34		DEFW INT+2EH
1D10	FB62		35		DEFW INT+4DH
			36		
1D12	1A63		37		DEFW GET_WORD+4H
1D14	2063		38		DEFW GET_WORD+0AH
1D16	2463		39		DEFW GET_WORD+0EH
1D18	2A63		40		DEFW GET_WORD+14H
1D1A	3563		41		DEFW GET_WORD+1FH
			42		
1D1C	3E63		43		DEFW PUT_WORD+3H
1D1E	4463		44		DEFW PUT_WORD+9H
1D20	4863		45		DEFW PUT_WORD+0DH
1D22	4E63		46		DEFW PUT_WORD+13H
1D24	5763		47		DEFW PUT_WORD+1CH
			48		
1D26	1764		49		DEFW GET_STATUS+12H
1D28	1E64		50		DEFW GET_STATUS+19H
1D2A	2864		51		DEFW GET_STATUS+23H
			52		
1D2C	6164		53		DEFW GET_NUMBER+3H
1D2E	6564		54		DEFW GET_NUMBER+7H
1D30	6D64		55		DEFW GET_NUMBER+0FH

1D36	B364	59	DEFW	BANK_ENABLE+1AH
1D38	0E65	60	DEFW	BANK_ENABLE+75H
1D3A	1665	61	DEFW	BANK_ENABLE+7DH
		62		
1D3C	3265	63	DEFW	SAVE_STATUS+14H
1D3E	3A65	64	DEFW	SAVE_STATUS+1CH
		65		
1D40	5C65	66	DEFW	RESTORE_STATUS+12H
1D42	6665	67	DEFW	RESTORE_STATUS+1CH
		68		
1D44	CE65	69	DEFW	BS_SP
		70		
1D46	8565	71	DEFW	GOTO_BANK+13H
		72		
1D48	D365	73	DEFW	CALL_BANK+3H
1D4A	ED65	74	DEFW	CALL_BANK+1DH
1D4C	F965	75	DEFW	CALL_BANK+29H
1D4E	1E66	76	DEFW	CALL_BANK+4EH
1D50	2D66	77	DEFW	CALL_BANK+5DH
1D52	3A66	78	DEFW	CALL_BANK+6AH
1D54	4266	79	DEFW	CALL_BANK+72H
1D56	5066	80	DEFW	CALL_BANK+80H
1D58	6666	81	DEFW	CALL_BANK+96H
1D5A	7266	82	DEFW	CALL_BANK+0A2H
1D5C	8066	83	DEFW	CALL_BANK+0B0H
		84		
1D5E	9466	85	DEFW	MOVE_BYTES+8H
1D60	C066	86	DEFW	MOVE_BYTES+34H
		87		
1D62	FD66	88	DEFW	CREATE_BITMAP+13H
1D64	0367	89	DEFW	CREATE_BITMAP+1BH
		90		
1D66	3067	91	DEFW	XFER_BYTES+0EH
1D68	4F67	92	DEFW	XFER_BYTES+2DH
1D6A	5067	93	DEFW	XFER_BYTES+2EH
1D6C	5A67	94	DEFW	XFER_BYTES+38H
1D6E	7667	95	DEFW	XFER_BYTES+54H
1D70	7D67	96	DEFW	XFER_BYTES+58H
1D72	A767	97	DEFW	XFER_BYTES+65H
1D74	DC67	98	DEFW	XFER_BYTES+0BAH
1D76	E367	99	DEFW	XFER_BYTES+0C1H
1D78	F667	100	DEFW	XFER_BYTES+0D4H
		101		
1D7A	0000	102	DEFW	0 ; THIS IS THE TABLE TERMINATOR

## APPENDIX B

### System Variables Definition File

#### 2068 HOME ROM

TS2000 HOME ROM	BASIC	
LOC OBJ CODE M STMT SOURCE STATEMENT		ASH 5.9
13	*EJECT	
14	*INCL SYSVAR	
15	*PAGESIZE 54	
16		
17	RST: MACRO #ROUT	
18	RST #ROUT	
19	ENDM	
20		
21	ASSERT: MACRO #COND	
22	COND .NOT.(#COND)	
23	ERROR IN ASSERTION #COND	
24	ENDC	
25	ENDM	
26		
27	; SYSTEM VARIABLES	
28		
29	L_LEN EQU 32 ; # CHARS PER LINE ON THE DISPLAY	
30	TV_LNS: EQU 24 ; NO. OF LINES ON TV SCREEN	
31	D_FILE: EQU 4000H ; ADDRESS OF DISPLAY FILE	
32	ATTRS: EQU D_FILE+L_LEN*TV_LNS*8 ; SCREEN ATTRIBUTES	
33	PRBUFF: EQU ATTRS+TV_LNS*L_LEN ; PRINTER BUFFER	
34	ASSERT PRBUFF.AND.OFFH=0	
	COND .NOT.(PRBUFF.AND.OFFH=0)	
	ERROR IN ASSERTION PRBUFF.AND.OFFH=0	
	ENDC	



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35 KSTATE: EQU PRBUFF+L_LEN*8      ! SEE KB DOCUMENTATION
36 KS_A: EQU 0                     ! 1ST BYTE IS A CHAR KEY PRESSED
37 KS_C: EQU 1                     ! 2ND IS TIME TILL COUNTS AS RELEASED
38 KS_B: EQU 2                     ! 3RD IS TIME (IN FRAMES) TILL REPEAT
39 KS_D: EQU 3                     ! 4TH IS CODE WHEN REPEATS
40                                ! 5TH - 8TH ARE A SECOND SET OF 1ST FOUR
41 LAST_K: EQU KSTATE+8            ! NEWLY PRESSED KEY
42 REPDEL: EQU LAST_K+1            ! DELAY BEFORE 1ST REPEAT (INITIALIZED TO 35)
43 REPPER: EQU REPDEL+1            ! DELAY BEFORE SUBSEQUENT REPEATS (INITIALIZED TO 5)
44 DEFADD: EQU REPPER+1            ! -> CHAR AFTER '(' IN FORMAL PARAMETER LIST; MUST BE
45                                ! 0 WHEN NO USER-DEFINED FN BEING EVALUATED
46 K_DATA: EQU DEFADD+2            ! DATA BYTE IN COMPOSITE CHAR FROM KEYBOARD
47 TVDATA: EQU K_DATA+1            ! USED FOR STORING BYTES IN COMPOSITE CHARACTERS:
48                                ! (TVDATA) = KEY BYTE,
49                                ! (TVDATA+1) = 1ST DATA BYTE FOR AT OR TAB.
50 STRMS: EQU TVDATA+2            ! STREAM DATA: POINTERS (OFFSETS FROM (CHANS)-1) TO
51                                ! CHANNELS. 0 = STREAM NOT OPEN.
52 HIDSTR: EQU 3                   ! NO. STREAMS HIDDEN FROM USER. THESE ARE TIED
53                                ! UNALTERABLY TO SPECIFIC CHANNELS.
54 HID_K: EQU -3                   ! KEYBOARD
55 HID_S: EQU -2                   ! TV, UPPER HALF OF SCREEN
56 HID_R: EQU -1                   ! INSERTION IN RAM
57 COM_ST: EQU 0                   ! STREAM FOR COMMANDS
58 INP_ST: EQU 1                   ! STREAM FOR INPUT DATA
59 PR_ST: EQU 2                    ! STREAM FOR PRINT
60 LPR_ST: EQU 3                   ! STREAM FOR LPRINT
61 CHARS: EQU STRMS+(HIDSTR+16)*2 ! -> 8*20H BYTES BEFORE CHARACTER SET
62 FART: EQU CHARS+2               ! NO. CYCLES OF ERROR NOISE (2 8VES BELOW MIDDLE C)
63 PIP: EQU FART+1                ! NO. CYCLES OF KEYBOARD NOISE (3 8VES ABOVE MIDDLE C)
64 Y: EQU PIP+1                   ! VALUE ALWAYS HELD IN IY
65 ERR_NR: EQU Y                   ! [RUN TIME ERROR #] - 1
66 FLAGS: EQU ERR_NR+1            ! VARIOUS FLAGS
67 SPC: EQU 0                     ! SUPPRESS SPACE BEFORE TOKENS
68 PR: EQU 1                      ! PRINTING TO PRINTER, NOT TV
69 LMODE: EQU 2                   ! L MODE, NOT K, AT CURRENT CHARACTER
70 LMODE: EQU 3                   ! L MODE, NOT K, AT CURSOR
71 KEYHIT: EQU 5                  ! KEYHIT FOUND
72 NO: EQU 6                      ! EXPRESSION IS NUMERICAL, NOT STRING
73 INTPT: EQU 7                   ! REQ INTERPRET RATHER THAN CHECK SYNTAX
74 TVFLAG: EQU FLAGS+1            ! FLAGS ASSOCIATED WITH THE TV
75 LHS: EQU 0                     ! PRINTING TO LOWER HALF OF SCREEN
76 EDIT: EQU 1                   ! OUTPUTTING LINE FOR EDIT OR NO. FOR STRING
77 ECHREQ: EQU 3                  ! ECHO REQUESTED IF INPUTTING FROM KEYBOARD
78 LIST: EQU 4                   ! OUTPUTTING AN AUTOMATIC LISTING
79 CLHS: EQU 5                    ! CLEAR LOWER HALF WHEN KEY PRESSED
80 ERR_SP: EQU TVFLAG+1           ! -> BOTTOM ITEM ON MACHINE STACK.
81 LISTSP: EQU ERR_SP+2           ! -> RETURN ADDRESS FROM AUTOMATIC LISTING
82 MODE: EQU LISTSP+2             ! 0 = K OR L, 1 = F, 2 = G.
83 NEWPPC: EQU MODE+1             ! LINE TO BE JUMPED TO
84 NSPPC: EQU NEWPPC+2            ! SUBLINE TO BE JUMPED TO (BIT 7 OFF FORCES JUMP)
85 PPC: EQU NSPPC+1               ! LINE # OF INSTR BEING INTERPRETED
86 SUBPPC: EQU PPC+2              ! NO. WITHIN LINE OF INSTR BEING INTERPRETED
87 BORDCR: EQU SUBPPC+1           ! BORDER COLOUR (SHIFTED LEFT BACKG BITS WITH 0S IN
88                                ! BITS 0-2 & 6-7)
89 E_PPC EQU BORDCR+1             ! LINE # OF "CURRENT" LINE IN LISTING
90                                ! THE VARIABLES FROM (VARS) UP TO & INCLUDING (STKEND)
91                                ! ARE 'MOVABLE' IN THE SENSE THAT THEY ARE ADJUSTED
92                                ! (BY REMGSZ IN MODULE EDIT) WHENEVER STUFF IS
93                                ! INSERTED IN OR DELETED FROM RAM.
94 VARS EQU E_PPC+2               ! -> 1ST RECORD FOR A VARIABLE (LAST IS 1 BYTE 80H)
95 DEST EQU VARS+2                ! -> VAR MATCHED BY TEMPL CODE 1 OR 4 (TEXT OR RECORD)
96 CHANS_: EQU DEST+2             ! -> CHANNEL DATA (INCLUDING FLOPPY BUFFERS).
97                                ! EACH ITEM COMPRISES:
98                                ! THE ADDRESS OF AN OUTPUT ROUTINE FOR WRCH.
99                                ! THE ADDRESS OF AN INPUT ROUTINE FOR INCH.
100                                ! A 1-BYTE CODE FOR THE DEVICE TYPE.
101                                ! &, WHERE APPROPRIATE, A FILE NAME, ADDITIONAL
102                                ! DATA & A BUFFER.
103 CURCHL: EQU CHANS_+2           ! -> DATA FOR CURRENT CHANNEL
104 PROG: EQU CURCHL+2            ! -> BASIC PROGRAM
105 NXTLIN: EQU PROG+2            ! -> NEXT LINE OF SOURCE CODE

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106 DATADD: EQU NXTLIN+2      ; -> TERMINATOR OF LAST DATA ITEM
107 E_LINE EQU DATADD+2      ; -> LINE BEING EDITED
108 K_CUR:  EQU E_LINE+2      ; -> CURRENT CHAR IN INPUT BUFFER
109 CH_ADD EQU K_CUR+2        ; -> CURRENT CHAR WHEN SYNTAX CHECKING ETC
110 X_PTR   EQU CH_ADD+2      ; -> 1ST CHAR NOT SYNTACTICALLY OK (0 IF ALL OK)
111                                     ; ALSO STORES (CH_ADD) DURING READ & INPUT
112 WORKSP: EQU X_PTR+2        ; -> TEMPORARY WORK SPACE
113 STKBOT: EQU WORKSP+2      ; -> BOTTOM OF CALCULATOR STACK
114 STKNXT: EQU STKBOT+2      ; -> NEXT FREE PLACE ON CALCULATOR STACK
115 STKEND: EQU STKNXT        ; ALTERNATIVE NAME
116
117 BREG:    EQU STKEND+2      ; KEEPS VALUE OF CALCULATOR B REGISTER
118 MEM:     EQU BREG+1        ; -> AREA USED BY CALCTR INSTRS MEMORY & COPY
119 FLAGS2: EQU MEM+2         ; MORE FLAGS
120 ALOS:    EQU 0             ; AUTOMATIC LISTING ON SCREEN
121 PRLEFT: EQU 1             ; PRINTER BUFFER NOT EMPTY
122 L_STR:   EQU 2             ; INSIDE STRING WHEN DOING KB MODE IN LISTCH
123 CAPS_L: EQU 3             ; CAPITALS SHIFT LOCK ON
124 RETPOS: EQU 4             ; RETYPE POSSIBLE AFTER SYNTAX ERROR
125 DELREP: EQU 5             ; DELETE KEY REPEAT (KEY HELD DOWN)
126 DF_SZ:   EQU FLAGS2+1     ; # LINES IN 2ND HALF OF SCREEN INC SEP'G BLANK LINE
127 S_TOP:   EQU DF_SZ+1      ; LINE # (IN PROGRAM) OF TOP LINE ON SCREEN
128 OLDPPC: EQU S_TOP+2       ; LINE # OF E.G. INTERRUPTED STMT
129 OSPPC:   EQU OLDPPC+2     ; (OLD SUB PPC) STATEMENT NO. WITHIN LINE FOR OLDPPC
130 FLAGX:   EQU OSPPC+1      ; FLAGS ASSOCIATED WITH ASSIGNMENT
131 FLEX:    EQU 0             ; FLEXIBLE LENGTH ASSIGNMENT REQUIRED
132 UNFND:   EQU 1             ; DESTINATION OF ASSIGNMENT NOT FOUND
133 INPLN:   EQU 5             ; REQ INPUT VALUE RATHER THAN LINE OF PROGRAM
134 NO:      EQU 6             ; REQD TYPE IS NUMERIC
135 LINPLN:  EQU 7             ; LINPUT (INPUT LINE) RATHER THAN STRAIGHT INPUT
136 STRLEN:  EQU FLAGX+1      ; LENGTH OF DESTINATION WHEN STRING TYPE
137 T_ADDR:  EQU STRLEN+2     ; -> NEXT BYTE IN TEMPLATE
138 SEED:    EQU T_ADDR+2     ; LAST RANDOM # BEFORE SCALING
139 FRAMES:  EQU SEED+2       ; LS 2 BYTES OF 3-BYTE FRAME COUNTER
140 FRAME2:  EQU FRAMES+2     ; MS BYTE OF 3-BYTE FRAME COUNTER
141 UDG:     EQU FRAME2+1     ; -> 1ST USER DEFINED GRAPHIC
142 COORDS:  EQU UDG+2        ; COORDINATES OF LAST PLOT ETC.: (COORDS) = X-COORD.,
143                                     ; (COORDS+1) = Y-COORD.
144 P_POSN:  EQU COORDS+2     ; COLUMN NO. OF PRINTER POSN
145 PR_CC:   EQU P_POSN+1     ; LS BYTE OF ADDRESS OF NEXT CHAR FOR PRINTER
146 ECHO_E:  EQU PR_CC+2      ; COORDS IN LOWER HALF OF END OF KEYBOARD INPUT BUFFER
147 DF_CC:   EQU ECHO_E+2     ; -> SCREEN CHAR UNDER PRINT CURSOR
148 DFCCL:   EQU DF_CC+2      ; LIKE DF_CC FOR LOWER HALF
149 S_POSN:  EQU DFCCL+2      ; SCREEN POSN (COL & LINE) OF NEXT CHAR TO BE OUTPUT
150 S_POSNL: EQU S_POSN+2     ; LIKE S_POSN FOR LOWER HALF
151 SCR_CT:  EQU S_POSNL+2    ; (SCROLL COUNT) DECREMENTED FOR EACH SCROLL
152 ATTR_P:  EQU SCR_CT+1     ; CURRENT PERMANENT PRINTING ATTRIBUTES
153 FOREG:   EQU 0            ; LS BIT OF FOREGROUND COLOUR
154 BLUE:    EQU 0            ;
155 RED:     EQU 1            ; (INK)
156 GREEN:   EQU 2            ;
157 BACKG:   EQU 3            ; LS BIT OF BACKGROUND COLOUR
158 BLUEB:   EQU 3            ; (PAPER)
159 REDB:    EQU 4            ;
160 GREENB:  EQU 5            ;
161 HILITE:  EQU 6            ; BRIGHT
162 FLASH:   EQU 7            ; FLASH
163 MASK_P:  EQU ATTR_P+1     ; CURRENT PERMANENT PRINTING ATTRIBUTES MASK:
164                                     ; 0 FOR NEW, 1 FOR OLD
165 ATTR_T:  EQU MASK_P+1     ; CURRENT TEMP. PRINTING ATTRIBUTES (BITS AS ATTR_P)
166 MASK_T:  EQU ATTR_T+1     ; CURRENT TEMPORARY PRINTING ATTRIBUTES MASK
167 P_FLAG:  EQU MASK_T+1     ; ADDITIONAL FLAGS FOR PRINTING: TEMPORARY FLAGS IN
168                                     ; EVEN BITS, PERMANENT FLAGS IN ODD BITS
169 XOR_CH:  EQU 0            ; NEW CHARS XOR'D INTO OLD RATHER THAN BEING LOADED
170 INV_CH:  EQU 2            ; NEW CHARS INVERTED
171 F_CB:    EQU 4            ; FOREGROUND := COMPLEMENT OF BACKGROUND
172 B_CB:    EQU 6            ; BACKGROUND := COMPLEMENT OF FOREGROUND
173 MEMBOT:  EQU P_FLAG+1     ; BOTTOM OF CALCULATOR MEMORY (6 NUMBERS)
174 NMIADD:  EQU MEMBOT+30    ; -> USER'S NMI SERVICE ROUTINE
175 RAMTOP:  EQU NMIADD+2     ; LAST ADDRESS OF BASIC SYSTEM AREA
176 P_RAMT:  EQU RAMTOP+2     ; -> LAST BYTE OF PHYSICAL RAM

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177
178          !**** ADDITIONAL
179 ERR_LN: EQU P_RANT+2      !POINTER TO ON ERROR LINE NUMBER FOR A GO-TO.
180 ERR_C: EQU ERR_LN+2      !STORE LINE NUMBER IN WHICH ERROR OCCURRED.
181 ERR_S: EQU ERR_C+2      !STORES STATEMENT NUMBER IN WHICH ERROR OCCURRED
182 ERR_T: EQU ERR_S+1      !STORE FOR 'ERROR TYPE' AFTER A 'ON ERR'
183 SYSCON: EQU ERR_T+1      !SYSTEM CONFIGURATION TABLE.
184 MAX_BANK: EQU SYSCON+2    !LARGEST BANK NUMBER ASSIGNED
185 CURCBN: EQU MAX_BANK+1    !BANK NUMBER OF THE CURRENT CHANNEL
186 MSTBOT: EQU CURCBN+1     !ADDRESS OF LOCATION ABOVE MACHINE STACK
187 VIDMOD: EQU MSTBOT+2
188 !
189 !
190 ARSBUF: EQU VIDMOD+2      !POINTER TO AROS BUFFER.
191 ARSFLG: EQU ARSBUF+2      !AROS FLAG - BIT 7 SET INDICATES AROS PRESENT.
192 !
193 !BIT 4 SET INDICATES NXTLIN POINTING TO AROS.
194 !BIT 3 SET INDICATES DATADD POINTING TO AROS.
195 !THESE BITS BECOME IMPORTANT FOR THE INSERT ROUTINE
196 !((POINTERS POINTING TO AROS SHOULD NOT BE UPDATED
197 !FOR AN INSERTION INTO RAM).
198 ADATLN: EQU ARSFLG+1      !POINTER TO THE START OF THE CURRENT DATA LINE
199 ! (AROS ONLY)
200 DTLNLN: EQU ADATLN+2      !LENGTH OF THE CURRENT DATA LINE (AROS ONLY).
201 STRNMH: EQU DTLNLN+2      !CURRENT STREAM NUMBER, USED FOR BUS EXPANSION
202 !
203 !UNIT DEVICES.
204 MSTACK: EQU 6200H         !LOCATION ABOVE MACHINE STACK
205 DRIVES: EQU 6840H         !START OF 'DRIVES' AREA
206 BANK_ENABLE EQU 6499H
207 CALL_BANK EQU 65D0H
208 MOVE_SZ EQU DRIVES-6000H
209 DEST7 EQU OFFFH-MOVE_SZ+1
210 FIX EQU DEST7-6000H
211 CALL_VBANK EQU CALL_BANK+FIX
212 GOTO_BANK EQU 6572H      !ADDRESS OF "GO TO BANK" BANK SWITCHING
213 ! AWARD.
214 XFER_BYTES EQU 6722H      !INDIRECT DATA TRANSFER BETWEEN BANKS.
215 GOTO_EXT EQU 6815H      !FOR INITIALIZATION CODE IN HOME BANK
216 ! EXTENTION.
217 SLVM EQU 01ABH          !ADDRESS OF TAPE ROUTINES FOR SAVE, LOAD
218 ! VERIFY AND MERGE COMMANDS.
219 BLDSCCT EQU 09F4H        !ADDRESS OF INITIALIZATION ROUTINE TO
220 ! BUILD THE SYSTEM CONFIGURATION TABLE.
221 RESSCT EQU 0C4CH         !ADDRESS OF RESET ROUTINE TO ADD DEVICES.
222 PASSING EQU 0F09H        !ADDRESS OF ROUTINE TO PUSH PARAMETERS TO
223 ! THE BEU ROUTINES ONTO THE MACHINE STACK.
224 !****
225
226 ; OTHER EQUATES
227
228 ; RESTARTS
229
230 ERROR: EQU 8
231 WRCH: EQU 16
232 IGN_SP: EQU 24
233 NXT_IS: EQU 32
234 CALCTR: EQU 40
235 COPYUP: EQU 48
236
237 NOSIZE EQU 5              ! # OF BYTES IN A FLOATING POINT NUMBER
238 DIGIT EQU '0'            ! DIGIT+N IS CODE FOR DIGIT N
239 LETTER EQU 0             ! LETTER+'ALPHA' IS CODE FOR LETTER ALPHA
240 DEBDEL: EQU 5            ! NO. CONSECUTIVE TIMES KB SWITCH FOUND OPEN BEFORE
241 ! KEY RECKONED RELEASED.
242
243 ; CONTROL CHARACTERS (APPEARING ON STREAM)
244
245 COM_CC: EQU 6             ! PRINT COMMA
246 EDT_CC: EQU 7            ! EDIT
247 BS_CC: EQU 8             ! BACKSPACE (CURSOR LEFT)
248 CRT_CC: EQU 9            ! CURSOR RIGHT
249 CD_CC: EQU 0AH           ! CURSOR DOWN
250 CU_CC: EQU 0BH           ! CURSOR UP

```

```

250 RUB_CC: EQU 0CH      ; RUBOUT
251 CR_CC: EQU 0DH      ; CARRIAGE RETURN (NEWLINE)
252 NL: EQU CR_CC
253 SLUG: EQU 0EH      ; PRECEDES 5 BYTES OF SLUG
254 FORECC: EQU 10H     ; FOREGROUND
255                     ; THE CONTROL CHARS FOR FORE, BACK, FLASH, BRIGHT,
256                     ; INVERT & OVER ARE CONSECUTIVE IN THAT ORDER.
257 AT_CC: EQU 16H      ; PRINT AT
258 TAB_CC: EQU 17H     ; PRINT TAB
259
260                     ; CONTROL CHARACTERS (RECEIVED FROM KEYBOARD)
261
262 STY_KC: EQU 0        ; STEADY
263 FSH_KC: EQU 1        ; FLASH
264 LOL_KC: EQU 2        ; LOWLIGHT
265 HIL_KC: EQU 3        ; HIGHLIGHT
266 NLV_KC: EQU 4        ; NORMAL VIDEO
267 INV_KC: EQU 5        ; INVERSE VIDEO
268 CSL_KC: EQU 6        ; CAPS SHIFT LOCK TOGGLE
269 TM_KC: EQU 0EH      ; TOKEN MODE
270 GRM_KC: EQU 0FH      ; GRAPHICS MODE
271 FG_KC: EQU 10H      ; FOREGROUND BLACK
272 BG_KC: EQU 18H      ; BACKGROUND BLACK
273
274 SPACE: EQU ' '
275 QUOTE EQU '"'        ; STRING QUOTE
276 DOLLAR EQU '$'       ; DOLLAR SIGN
277 COLON: EQU ':'
278 COMMA EQU ','
279 KET EQU ')'
226
227                     ; RESTARTS
228
229 ERROR: EQU 8
230 WRCH: EQU 16
231 IGN_SP: EQU 24
232 NXT_IS: EQU 32
233 CALCTR: EQU 40
234 COPYUP: EQU 48
235
236 NOSIZE EQU 5          ; # OF BYTES IN A FLOATING POINT NUMBER
237 DIGIT EQU '0'         ; DIGIT+N IS CODE FOR DIGIT N
238 LETTER EQU 0          ; LETTER+'ALPHA' IS CODE FOR LETTER ALPHA
239 DEBDEL: EQU 5         ; NO. CONSECUTIVE TIMES KB SWITCH FOUND OPEN BEFORE
240                     ; KEY RECKONED RELEASED.
241
242                     ; CONTROL CHARACTERS (APPEARING ON STREAM)
243
244 COM_CC: EQU 6          ; PRINT COMMA
245 EDT_CC: EQU 7          ; EDIT
246 BS_CC: EQU 8           ; BACKSPACE (CURSOR LEFT)
247 CRT_CC: EQU 9          ; CURSOR RIGHT
248 CD_CC: EQU 0AH        ; CURSOR DOWN
249 CU_CC: EQU 0BH        ; CURSOR UP
250 RUB_CC: EQU 0CH      ; RUBOUT
251 CR_CC: EQU 0DH      ; CARRIAGE RETURN (NEWLINE)
252 NL: EQU CR_CC
253 SLUG: EQU 0EH      ; PRECEDES 5 BYTES OF SLUG
254 FORECC: EQU 10H     ; FOREGROUND
255                     ; THE CONTROL CHARS FOR FORE, BACK, FLASH, BRIGHT,
256                     ; INVERT & OVER ARE CONSECUTIVE IN THAT ORDER.
257 AT_CC: EQU 16H      ; PRINT AT
258 TAB_CC: EQU 17H     ; PRINT TAB
259
260                     ; CONTROL CHARACTERS (RECEIVED FROM KEYBOARD)
261
262 STY_KC: EQU 0        ; STEADY
263 FSH_KC: EQU 1        ; FLASH
264 LOL_KC: EQU 2        ; LOWLIGHT
265 HIL_KC: EQU 3        ; HIGHLIGHT
266 NLV_KC: EQU 4        ; NORMAL VIDEO
267 INV_KC: EQU 5        ; INVERSE VIDEO

```

268	CSL_KC:	EQU 6	:	CAPS SHIFT LOCK TOGGLE
269	TM_KC:	EQU 0EH	:	TOKEN MODE
270	ORM_KC:	EQU 0FH	:	GRAPHICS MODE
271	FO_KC:	EQU 10H	:	FOREGROUND BLACK
272	BO_KC:	EQU 18H	:	BACKGROUND BLACK
273				
274	SPACE:	EQU ' '		
275	QUOTE	EQU '"'	:	STRING QUOTE
276	DOLLAR	EQU '\$'	:	DOLLAR SIGN
277	COLON:	EQU ':'		
278	COMMA	EQU ','		
279	KET	EQU '>'		
280	BRA	EQU '<'		
281	GT:	EQU '>'		
282	MINUS	EQU '-'		
283	EQUAL	EQU '='		
284	PLUS:	EQU '+'		
285	STROKE:	EQU '/'		
286	POWER:	EQU '^'		
287	POINT:	EQU '.'		
288	SHARP:	EQU 5FH	:	PRESTEL CODE FOR '#'
289	STD_GR:	EQU 80H	:	1ST STANDARD GRAPHIC
290	UD_OR:	EQU 90H	:	1ST USER-DEFINED GRAPHIC
291				
292			:	TOKENS
293				
294	TOK0:	EQU 0A5H	:	1ST TOKEN
295	RNDTOK:	EQU 0A5H	:	'RND'
296	INKEY:	EQU 0A6H	:	'INKEY\$'
297	PI:	EQU 0A7H	:	'PI'
298	FN_TK:	EQU 0A8H	:	'FN'
299	PNT_TK:	EQU 0A9H	:	'POINT'
300	SCRNTK:	EQU 0AAH	:	'SCREEN\$'
301	ATTRTK:	EQU 0ABH	:	'ATTRT'
302	AT:	EQU 0ACH	:	'AT'
303	TOK_FN:	EQU FN_TK	:	1ST TOKEN TO REQUIRE A SPACE AFTER
304	TAB:	EQU 0ADH	:	'TAB'
305	VALSTK:	EQU 0AEH	:	'VAL\$'
306	LO_MON:	EQU 0AFH	:	TOKEN FOR 1ST MONADIC OPTR AFTER VAL\$ (CODE)
307	BIN_TK:	EQU 0C4H	:	'BIN'
308	OR_TK:	EQU 0C5H	:	'OR' NB THE TOKENS FOR OR, AND, <=, >=, <> ARE
309			:	CONSECUTIVE IN THAT ORDER.
310	LINETK:	EQU 0CAH	:	'LINE'
311	THEN:	EQU 0CBH	:	'THEN'
312	TO:	EQU 0CCH	:	'TO'
313	STEP:	EQU 0CDH	:	'STEP'
314	DEF_TK:	EQU 0CEH	:	'DEF'
315	MIN_KW:	EQU DEF_TK	:	1ST TOKEN THAT IS A KEYWORD RATHER THAN... OPERATOR
316	CAT_TK:	EQU 0CFH	:	'CAT'
317	FORMTK:	EQU 0D0H	:	'FORMAT'
318	MOVETK:	EQU 0D1H	:	'MOVE'
319	DEL_TK:	EQU 0D2H	:	'DELETE'
320	OPN_TK:	EQU 0D3H	:	'OPEN'
321	CLO_TK:	EQU 0D4H	:	'CLOSE'
322	MGE_TK:	EQU 0D5H	:	'MERGE'
323	VFY_TK:	EQU 0D6H	:	'VERIFY'
324	BEEPTK:	EQU 0D7H	:	'BEEP'
325	ARC_TK:	EQU 0D8H	:	'ARC'
326	FOTOK:	EQU 0D9H	:	'FOREGROUND' NB THE TOKENS FOR FORE, BACK, FLASH,
327			:	BRIGHT, INVERT & OVER ARE CONSECUTIVE IN THAT
328			:	ORDER.
329	INVTOK:	EQU FOTOK+5	:	'INVERT'
330	OUT_TK:	EQU 0DFH	:	'OUT'
331	LPR_TK:	EQU 0E0H	:	'LPRINT'
332	LL_TK:	EQU 0E1H	:	'LLIST'
333	STOPTK:	EQU 0E2H	:	'STOP'
334	READTK:	EQU 0E3H	:	'READ'
335	DATATK:	EQU 0E4H	:	'DATA'
336	RESTTK:	EQU 0E5H	:	'RESTORE'
337	NEXTOK:	EQU 0F3H	:	'NEXT'
338	DUMPTK:	EQU 0FFH	:	'COPY'
339				
340	BORDPT:	EQU 0FEH	:	OUTPUT PORT FOR SETTING BORDER COLOUR

```

341 PR_IN: EQU OFBH      I FOR INPUT FROM PRINTER
342 PR_OUT: EQU OFBH     I FOR OUTPUT TO PRINTER.
343 KB_PT: EQU OFEH      I INPUT PORT FOR READING KEYBOARD
344 O_PORT: EQU OFEH     I OUTPUT PORT FOR TAPE
345 I_PORT: EQU OFEH     I INPUT PORT FOR TAPE
346 TAPE_I: EQU 6        I TAPE INPUT BIT IN (I_PORT)
347                      I***ADDITIONAL
348 DKHSPT: EQU OF4H     I DOCK HORIZONTAL SELECT PORT
349 BDATPT: EQU OFCH     I EXPANSION BANK DATA PORT
350 BCMDPT: EQU OFDH     I EXPANSION BANK COMMAND PORT
351 HREXPT: EQU OFFH     I HOME ROM EXPANSION BANK PORT
352                      I***
353
354                      I OFFSETS FROM (CHANS) OF PERMANENT CHANNELS
355
356 CHAN_K: EQU 0         I KEYBOARD
357 CHAN_S: EQU 5         I TV SCREEN (UPPER HALF)
358 CHAN_R: EQU 10        I RAM INSERTION
359 CHAN_P: EQU 15        I ZX PRINTER
360
361 CH_SET: EQU 4000H-96*8 I ADDRESS OF CHARACTER SET (STARTING WITH SPACE)
362 *EJECT
363
364                      I CALCULATOR COMMANDS. IN THE DESCRIPTIONS, T & S STAND FOR
365                      I THE TOP & SECOND FROM TOP ON THE CALCULATOR STACK.
366                      I WHERE NECESSARY, FULLER DESCRIPTIONS CAN BE FOUND AT THE
367                      I CODE FOR THE RELEVANT ROUTINES.
368
369                      I THE FOLLOWING COMMANDS HAVE THE STACK POINTERS HL & DE (BUT
370                      I NOT (STKNXT)) DECREMENTED FOR THEM BY CALCTR BEFORE THEY
371                      I ARE CALLED (STKDN).
372
373 IFJUMP: EQU 0         IS,T -> S: RELATIVE JUMP CONDITIONAL ON VALUE OF T.
374 EXCH: EQU IFJUMP+1   I (EXCHANGE) S,T -> T,S
375 LOSE: EQU EXCH+1     IS,T -> S
376 SUB: EQU LOSE+1      I (SUBTRACT) S,T -> S-T
377 TIMES: EQU SUB+1    IS,T -> S*T
378 DIV: EQU TIMES+1    I (DIVIDE) S,T -> S/T
379 POWER: EQU DIV+1    IS,T -> S**T
380 OR: EQU POWER+1     IS,T -> S OR T (SEE OR).
381 AND: EQU OR+1       IS,T -> NUMERICAL S AND T (SEE NOAND).
382 OT: EQU AND+4       IS,T -> NUMERICAL S>T
383                      I5 NUMERIC COMPARISON OPERATIONS HAVE NOT BEEN GIVEN
384                      I MNEMONICS. S,T -> S^T WHERE ^ IS <=,>=,<,>,< OR =
385                      I SEE CMPSN.
386 ADD: EQU AND+7      IS,T -> S+T
387 STGAND: EQU ADD+1   IS,T -> S& AND& T (SEE STGAND).
388                      I6 STRING COMPARISON OPERATIONS WITHOUT MNEMONICS.
389 CONCAT: EQU STGAND+7 IS,T -> S$ +$ T$
390
391                      I ORDINARY OPERATIONS WITHOUT STKDN.
392
393 VALS: EQU CONCAT+1  IT$ -> VAL$ T$
394 USRS: EQU VALS+1    IT$ -> ADDRESS OF BIT PATTERN FOR CORRESPONDING
395                      I USER-DEFINED GRAPHIC
396 INKEY: EQU USRS+1   IT -> INKEY$ #T
397 NEGATE: EQU INKEY+1 IT -> -T
398 CODE: EQU NEGATE+1 IT$ -> CODE T$
399 LO_MON: EQU CODE    I OPERATION CODE FOR LO_MON
400 VAL: EQU CODE+1     IT$ -> VAL T$
401 LEN: EQU VAL+1     IT$ -> LEN T$
402 SIN: EQU LEN+1     IT -> SIN T
403 COS: EQU SIN+1     IT -> COS T
404 TAN: EQU COS+1     IT -> TAN T
405 ASN: EQU TAN+1     IT -> ARCSIN T
406 ACS: EQU ASN+1     IT -> ARCCOS T
407 ATN: EQU ACS+1     IT -> ARCTAN T
408 LN: EQU ATN+1      IT -> LN T
409 EXP: EQU LN+1      IT -> EXP T
410 INT: EQU EXP+1     I (INTEGER PART) T -> INT T
411 ROOT: EQU INT+1    IT -> SQUARE ROOT OF T
412 SON: EQU ROOT+1    IT -> SON T

```

```

413 ABS:      EQU SGN+1      ! (ABSOLUTE) T -> \T\
414 PEEK:     EQU ABS+1      ! T -> PEEK T
415 IN:       EQU PEEK+1     ! T -> IN T
416 USR:      EQU IN+1       ! T -> USR T
417 STR:      EQU USR+1      ! T -> STR$ T
418 CHR:      EQU STR+1      ! T -> CHR$ T
419 NOT:      EQU CHR+1      ! T -> BOOLEAN (T = 0)
420 ZERO?:    EQU NOT
421 DUP:      EQU NOT+1      ! (DUPLICATE) T -> T,T
422 INTDIV:    EQU DUP+1     ! (INTEGER DIVISION) S,T -> S MOD T, INT(S/T)
423 JUMP:     EQU INTDIV+1   ! PROGRAMME CONTROL - RELATIVE JUMP BY FOLLOWING BYTE
424 LITERAL:  EQU JUMP+1     ! STACKS FOLLOWING NUMBER.
425 LOOP:     EQU LITERAL+1  ! LIKE ZILG DJNZ
426 MINUS?:   EQU LOOP+1     ! T -> BOOLEAN (T < 0)
427 PLUS?:    EQU MINUS?+1   ! T -> BOOLEAN (T > 0)
428 QUIT:     EQU PLUS?+1    ! RETURNS CONTROL TO Z80
429 ANGLE:    EQU QUIT+1     ! T -> Y WHERE -1 <= Y <= +1 & SIN T = SIN (PI/2*Y)
430          ! MEMORY 0 := TRUE IF T IN 2ND OR 3RD QUADRANT
431 TRUNC:     EQU ANGLE+1    ! (TRUNCATE) T -> INTEGER TRUNCATION OF T TOWARDS 0.
432 XEQTB:    EQU TRUNC+1    ! EXECUTES (BREG) AS A CALCULATOR INSTRUCTION
433 XEY:      EQU XEQTB+1     ! S,T -> S * 10**T
434 FLOAT:    EQU XEY+1      ! T FORCED INTO FLOATING POINT FORM
435
436          ! THE FOLLOWING COMMANDS HAVE ADDED TO THEM AN OPERAND, N.
437
438 CBSV:      EQU 80H        ! SUMS N TERMS OF CHEBYSHEV SERIES (SEE CBSV).
439 CONST:     EQU CBSV+20H   ! (CONSTANT) T -> T, NTH CALCULATOR CONSTANT
440 MINUS1:    EQU CONST+6    ! CALCTR CONSTANT EQUAL TO -1
441 COPY:      EQU CONST+20H  ! T -> T; T COPIED TO NTH CALCULATOR MEMORY
442 MEMORY:    EQU COPY+20H   ! T -> T, CONTENTS OF NTH CALCULATOR MEMORY
443
444 OP_TK:     EQU LO_MON-LO_MON ! TOKEN FOR MONADIC OPTR C IS OP_TK+C
445 HI_MON:    EQU OP_TK+CHR   ! TOKEN FOR LAST MONADIC OPTR EXCEPTING NOT
446 MONOP:     EQU LO_MON.OR.OCOH ! OPERATION CODE FOR LO_MON, TOP 2 BITS SET.
447 LONOMO:    EQU OP_TK+SIN   ! TOKEN FOR 1ST (NUMBER) NUMBER OPTR AFTER -
448 HINOMO:    EQU OP_TK+USR   ! TOKEN FOR LAST (NUMBER) NUMBER OPTR
449
450 *LIST ON

```

---

## APPENDIX C-1

### 64 COLUMN MODE

TIMEX COMPUTER CORPORATION

#### APPLICATION DEVELOPMENT LIBRARY

#### Application Software Commands 001

#### 64-COLUMN MODE

Date: 12/15/83

ASC Number: 001

Version: 002

Author: Carol Carceran

Name: 64 Column Mode Support

#### Description:

This component provides support to the application programmer for using the 64-column mode feature of the TS 2068. The services include opening/closing the second display file (leaving the machine stack, OS RAM routines and BASIC structures), PRINT position control, attribute control, clear screen and scroll screen services and display of characters. For use of use from BASIC, status is returned in the BC register pair, usually zero for successful completion and designated non-zero values for other conditions. The interface from BASIC is by means of POKing the input parameters to designated RAM locations prior to invoking the service via the USR function. See page 4 of the code listing for the memory addresses to be used from BASIC with the support code loader starting at Chunk 7 (E000h/57344).

#### Version Control

Version	Description	Date
001	Original	11/28/83
002	ADD:  1) Std. and User-Defined Graphics to WRCHAR and GTCHAR.  2) WRSTRG (Write String)	12/15/83



# APPLICATIONS INSTRUCTIONS

-----

Name: SETMCDE (SETMDB from BASIC - parameter to VIMODE)

Input: MDDE (D=normal, 6\*64 column code)

Free machine code: Register A  
From BASIC: In VIMODE

## Description:

Sets specified video code, opening or closing the second display file where needed. This involves determining if there is enough free RAM to open the second display file and if so, moving the BASIC structures and machine language variables over up-and the UDC over down to make space for the machine stack and DS RAM routines at the top of memory. The second display file is cleared to zeroes. The affected system and internal variables are updated or initialized (see Usage Section). When returning to Mode D free 64 Column code, the structures are returned to their normal locations.

Output: BC = 0 Successful  
BC = 1 Invalid parameters (not equal to 0 or 6)  
BC = 2 Not enough memory

-----

Name: CLRSCN (CLRSCB from BASIC - parameters to CLSCTL)

Input: Line Count (1-24)  
Starting Line Number (0-23)

Free Machine Code: Line Count in Register B  
Starting Line in Register C

From BASIC: Starting Line Number in CLSCTL  
Line Count in CLSCTL + 1

## Description:

Cleares to background color (PAPER) the designated number of lines, beginning with the Starting Line Number. Line 0 with a count of 24 clears the entire screen. Upon return, the cursor position is at the beginning of the first line cleared.

Output: BC = 0 for successful completion  
BC = 1 invalid parameters  
(Line Number + Line Count < 1 or > 24)

-----

Name: SETCUR (SETCUB from BASIC - parameters to LIMCOL)

Input: Line Number (0-23)  
Column Number (0-63)

Free Machine Code: Line Number in Register B  
Column Number in Register C

From BASIC: Column Number in LIMCOL  
Line Number in LIMCOL + 1

## Description:

Converts the requested position to internal format, determines display file address, and stores the values for use by the next display character operation. Note that once established, the position is updated automatically when a character is displayed so that it is only necessary to set the position when sequential display is not desired.

Output: BC = 0 for successful completion  
BC = 1 for invalid parameters (Line Number > 23,  
Column Number > Line Length-1)

-----

Name: SETATT (SETATS from BASIC - parameter to ATTCTL)

Input: Attribute Byte - bit 7 - FLASH  
bit 6 - BRIGHT  
bit 5 - P  
bit 4 - A  
bit 3 - PER  
bit 2 - I  
bit 1 - M  
bit 0 - K

From Machine Code: Register A  
From BASIC: In ATTCTL

Description:

The specified INK color (0-7) is used to set the video mode hardware and to update VIDMOD. The complementary PAPER color is fixed by the INK selection. FLASH and BRIGHT are fixed at zero by the hardware. Note that in 64 column mode the entire screen has the same attributes.

Output: BC = 0 Successful

-----

Name: SETMSK (SETMSB from BASIC - parameters to MSKCTL)

Input: Mask Byte - bit 0 - DYER  
bit 2 - INVERSE

From Machine Code: Register A

From BASIC: MSKCTL

Description:

The specified mask is stored for application to all subsequent display character operations. (DYER = 1 implies no character combined with old using an XOR operation; INVERSE = 1 implies character is inverted).

Output: BC = 0 for successful completion

-----

Name: WRCHR (WRCHB from BASIC - parameter to DATAS)

Input: Character code for character to be displayed

20H TO 7FH - Std. VT206B Character Set  
80H TO BFH - Std. Graphics Set  
90H TO A4H - User-Defined Graphics Set

From Machine Code: Register A

From BASIC: in DATAS

Description:

Displays character at current cursor position, applying current mask. Moves cursor position on to next sequential position. If character would start a new line after BOTLN (see Usage section) and the scroll count (variable SCRLCT) decrements to zero, the character will not be displayed and return will be made with BC = 3 (screen full). If the scroll count does not decrement to zero, the screen will be scrolled up one line using the information in SCRLCT and the new line started at the vacated line.

Output: BC = 0 for successful completion  
BC = 1 invalid character code  
BC = 3 for screen full

-----

-----

Name: WRSTRG (WRSTRG from BASIC - String Identifier in PARAMS)

Input: Character Code String

From machine code: Address of string in HL  
Count in BC

From BASIC: String Variable Identifier in  
System Variable PARAMS - 23747 (SCC3H)

Description:

Displays the characters from the string, beginning at the current cursor location and continuing sequentially until the count expires, or "Screen Full" is detected (see WRCHR description and Usage Section on Automatic Scrolling). For the Screen Full condition, the remaining count is stored in the internal variable STRGCT for access by the user.

NOTE: Characters within the string which are outside of the supported range (32 through 164 (20H-A4H)) will be ignored. E.g., BASIC Token codes and control codes embedded in an INPUT string will not be displayed or decoded.

From BASIC, POKE the code for the string variable identifier into PARAMS prior to invoking WRSTRG, e.g.

```
000B LET aa="-----string-----"
0010 POKE 23747,CODE "a"
0015 IFUSR(WRSTRG)<>0 THEN -----
      (continue)
```

Output: BC = 0 Successful  
BC = 2 BASIC - String not found  
BC = 3 Screen Full - Remaining Count in STRGCT  
(HL=Current Address in String)

-----

Name: SCROLL (SCRLB from BASIC - parameters to SCRCYL)

Input: Line Count (1-23)  
Starting Line Number (1-23)

From Machine Code: Line Count in B  
Starting Line in C  
From BASIC: Starting Line in SCRCYL  
Line Count in SCRCYL + 1

Description:

Scrolls the designated number of lines up 1 position, starting at the specified line number and inserts a blank line at the bottom of the scrolled area. Line 1 with a count of 23 will scroll the entire screen up 1 line. Upon return, the cursor position is at the beginning of the inserted blank line.

Note: See Usage Section on "Automatic" scrolling.

Output: BC = 0 Successful  
BC = 1 Invalid Parameters  
(Line Number + Line Count < 1 or > 24)

-----

Name: GETATT (GETATB from BASIC - parameters to GETCTL)

Input: Aa for GETCHAR

Description:

Returns in register C of the BC register pair the attribute byte for the character at the designated screen position. Note that in 64 column mode the entire screen has common attributes. The value returned will describe the current selection:

Output: BC = 1 for invalid parameters  
BC = attribute byte (as for SETATT)

-----

Name: GETCHAR (GETCHAR from BASIC - parameters to GETCTL)

Inout: Line/Column position as far as GETCUR

From Machine Code: Line Number in B  
Column Number in C

From BASIC: Column Number in GETCTL  
Line Number in GETCTL + 1

Description:

Returns in register C of the BC register pair the character code for the character at the designated screen position. If no match against the character set (including the standard and user-defined graphics) is found, zero is returned. (Character code or zero also returned in A.)

Note: Positions "printed" using the OVER technique will return zero if they do not match against any single character.

Output: BC = 0 for no find  
BC = 1 invalid parameters  
BC = character code (20H-84H)

-----

Name: GETCUR (GETCUR from BASIC)

Inout: None

Description:

Returns in the BC register pair and in the BASIC parameter location LINCOL, the current print position (where the next character would be displayed).

Output: B = Line number (0-23)  
C = Column number (0-83)

BASIC: LINCOL = Column number  
LINCOL + 1 = Line number

NOTE: If the last character was printed at Col.83 of Line 23 (last position on the screen), then Col.0/Line 23 will be returned.

-----

USAGES:

Memory Usage:

This package of machine code routines includes the following internal variables:

Name	Size	Description
----	----	-----
SCRECTL	2	Scroll Control LSB = Starting line number MSB = Number of lines to be scrolled
BOTLN	1	Bottom Line - Line number (0-23) after which test for scroll will be made.
SCRLCT	1	Scroll Count- Number of times + 1 that automatic scroll will be done. When decremented to zero, data will not be displayed and a condition code will be returned to user.
CHTBL	2	Character Table (Base Address-100H)
GRTBL	2	Std.Graphics Character Table (Base-100H)
LINLEN	1	Line Length - (64 when in 64-Col.Mode)
CURPOS	2	Current Position (Internal Format) LSB = Column Position MSB = Line Position
DFACDR	2	Current Display File Address
MASKB	1	Mask Byte (bit 0 = OVER) (bit 2 = INVERSE)

```

ATTBYT      1      Attribute Byte (bits D - 2 - INK)
                  (bits 3 - 5 - PAPER)
                  (bit 6 - BRIGHT (Set to zero
                  (bit 7 - FLASH by M/W in
                               64-col mode)

GTINDX      1      "Get" Index - Used by GTCHAR
STRGCT      2      String Count - Contains remaining byte count
;                  when BC=3 (Screen Full) is
;                  returned from the Write String
;                  service WRSTRG (WRSTRB).

```

Initial values set via SETMCDE (SETMDB) are as follows:

Variable Name	Value
SCRCTL	17CH
BOTLN	17H
SCRCLY	1H
CHTEL	3C00H
GRTSL	(Internal to Module)
LINLEN	40H
CURPCS	1841H
OFAGDR	4D00H
MASKL	0H
ATTBYT	38H
GTINDX	80H
STRGCT	0H

The following are the variables used for passing parameters in BASIC. The \* indicates those initialized by SETMDB:

Variable Name	Size	Value
* DATAB	1	0H
* LINCL	2	0H
* CLRCYL	2	1800H
* ATTCTL	1	38H
* MSKCTL	1	0H
* GETCTL	2	0H
VIMCDE	1	

In addition, VIDMOD, PARAMS and other system variables must be available to these routines. At minimum, chunks D-3 and chunk T must be enabled in the Home Bank.

#### Location:

This package can be incorporated into a machine code program within the memory range from chunk 4 through T, taking into consideration the remapping of certain structures when the second display file is open. NOTE: Machine code above RAMTOP is not moved.

The routine SETMCDE (SETMDB) cannot be executed from a cartridge because of the necessity to enable the RDM Extension which disables the DECK Bank.

#### Registers:

Other than as documented for output values, no claims are made as to preservation of any register contents except for the IV Register which must always contain the value SC3AH for access to the standard system variables.

#### Automatic Scrolling:

As initialized, test for scrolling will be made when the print position goes to the next line following the bottom line on screen (BOTLN=23=24th line). Condition Code 3 (Screen Full) will be returned since SCRCLY will decrement to zero. If SCRCLY is set to some larger value, then the parameters in SCRCTL will be used to automatically scroll the designated number of lines beginning at the specified line number. As initialized, this will scroll the entire screen up. By performing a PDKE or setting the variables BOTLN and SCRCTL to the desired values, automatic scrolling can be done using smaller sections of the screen. When working from BASIC it is recommended that BOTLN be set to line 21 (15H) and SCRCTL+1 be set to 21 (15H) to avoid conflict with the Edit Line which uses the bottom two lines of the screen. Note that once SCRCLY expires, it will be set to 1. If a different value is desired, it must be reinitialized by the user after receiving the "Screen Full" condition.

By setting the SCRCTL variable and invoking the SCROLL (SCRLB) routine any portion of the screen may be scrolled at any time.

# NOTES:

1. All screen operations done by the system ROM (PRINT, LIST, Edit line I/O, CLS, scrolling, etc.) relate only to the main Display File. This means that only the even columns on the 64-column mode screen will be effected. You will want to execute the Clear Screen function in this module to guarantee that no data in the second display file interfere with the use of a system screen service, e.g. prior to doing a LIST.
2. The COPY command will print only the even columns of the screen to the 2040 Printer.
3. During tape operations, the border will not change while in 64-column mode since this is fixed by the hardware to conform to the paper color.
4. The SAVE filename SCREEN1 will save only the main Display File data. The second can be saved by a SAVE filename CODE 24576, 6144. Be careful that you have the computer in 64-column mode when you load this data or you will overwrite the OS RAM routines and "crash" the system!! (The count for saving the display file is for the data portion only since the Attribute File area from 7600H-7AFFH (30720-31487) is not used by the video mode M/M in 64-Column mode.

AOL - ABC D01 64-COL.MODE IUPBCT  
10A4.SRC

CR180/11 version 10.30.1A

18-Mar-84 15:015

```

1      NAME AOL - ABC D01 64-COL.MODE IUPBCT
2
3
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11      *****
12      *                               *
13      *             TIMER             *
14      * APPLICATION DEVELOPMENT LIBRARY *
15      *                               *
16      * NAME: 64-COL.MODE IUPBCT      *
17      *                               *
18      * ABC D01 001                   *
19      * VERSION: 301                   *
20      * AUTHOR: C. CONCORDAN          *
21      * DATE: 12/15/83                 *
22      *                               *
23      *****
24
25      SUBTTL VERSION LEVEL CONTROL
26
27      VERSION      DATES      COMMENTS
28      -----
29      001          11/25/82      ORIGINAL
30
31      002          12/15/83      ADD STANDARD AND USER-DEFINED
32                                  GRAPHICS TO MACHAS AND GTCMAS
33
34                                  ADD MASTRO (WRITE STRING)
35                                  CAPABILITY
36
37      SUBTTL DEFINITIONS
38
39
40      *****DEFINITIONS*****
41
42      INTERM MACHAS.WRITAG.SETCUR.IETATT.SETMOS.CLRECH.ECHCLL
43      INTERM GTCMAS.GETATT.GETCUR
44      INTERM MACHAS.WRITMA.SETCUR.SETATB.IETMOS.CLRECH.SCLB
45      INTERM GTCMAS.GETATR.GETCUR
46      INTERM GETMODE.GETMODE
47
48
49      *001E      40 SCRSI      ECU      2A      1 24 LINES
50      *1701      50 SCINIT      ECU      17010  1 ECHCLL CCLYACL INITIALIZATION
51      *1800      01 CLINIY      ECU      1800H  1 CLEAR SCREEN CYL. INIT.
52      *1C05      11 CMOSBY      ECU      1C00H  1 SDP CMAA.TABLS-100H
53      *0195      15 GAPHST      ECU      ((GRPST)-150H) 1ETO.GRAP-ICI CHARE.
54
55      *053E      15 CMWCVIO      ECU      0500H  1 ROUTINE IN ROM EXTENSION
56      *1720      15 RECLCH      ECU      1720H  1 ROUTINE IN ROM ROM
57
58      *00PP      15 MRSNPT      ECU      00PM  1 MCOE ROM EXT.SELECT PORT (RTY 7)
59      *00P6      15 DMSPT      ECU      00PM  1 DDCO HORIZONTAL SELECT PORT
60
61      *SCA6      01 VAAS      ECU      SC49H  1 SYSTEM VARIABLES
62      *SC69      02 STAGND      ECU      SC03H  1
63      *SC79      03 UDS      ECU      SC780  1
64      *SC70      0A COORD01      ECU      SC70H  1
65      *SC02      05 RANTOS      ECU      SC010  1
66      *SC5A      00 PROMT      ECU      SC040  1
67      *SCC1      07 VIONCD      ECU      SCC1H  1
68      *SCC3      00 PABO=1      ECU      SCC30  1 (LOCATION 23747)

```

ADDRESS	DATA	INSTR	OPER	OPERAND	COMMENT
0000	20	DATA	DEPB	20M	: DATA BYTE FROM BASIC
0001	CS 0030	WRCMB01	JP	WRCMB0	: ENTRY TO WRITE CHARACTER
0002	CS 000C	WRCMB01	JP	WRCMB0	: ENTRY TO WRITE STRING
0003	0000	LINCOL	DEPB	0	: SET CURSOR PARAMETERS
0004	CS 0135	3ETCMB01	JP	3ETCMB0	: ENTRY TO SET CURSOR POSN.
0005	1000	CLACTL	DEPB	1000M	: CLEAR SCREEN CONTACTS
0006	CS 01P3	CL0SC01	JP	CL0SC00	: ENTRY TO CLEAR SCREEN
0007	50	ATTCTL	DEPB	50M	: ATTRIBUTE CONTROL
0008					: BIT 7=PLASM (0 IN 64-CCL.3
0009					: 0=RIGHTCB IN 64-CCL.3)
0010					: 3
0011					: 2
0012	CS 0260	3ETAT01	JP	3ETAT00	: ENTRY TO SET ATTRIBUTES
0013	00	M3ACTL	DEPB	0	: MASK CONTRCL FROM BASIC
0014	CS 0290	SETN001	JP	SETN000	: ENTRY TO SET MASK CONTRCLS
0015	0000	GETCTL	DEPB	0	: GET- CONTRCL FROM BASIC
0016	CS 02A1	GTCHMB01	JP	GTCHMB0	: ENTRY TO GET CHARACTER
0017	CS 0321	0BTAT01	JP	0BTAT00	: ENTRY TO GET ATTRIBUTE
0018	CS 0320	GETCMB01	JP	GETCMB0	: ENTRY TO GET CURSOR POSN.
0019	00	VIM000	DEPB	0	: VIDEO MODE CONTROL
0020	CS 03A5	STH001	JP	STH000	: ENTRY TO SET VIDEO MODE
0021	1701	SCRCTL	DEPB	1701M	: SCRCLL CONTRCL
0022	CS 01A5	SCR001	JP	SCR000	: ENTRY TO SCRCLL
0023					: 1= INR COLOR
0024					: 0
0025					: 1= INR COLOR
0026					: 0
0027					: 1= INR COLOR
0028					: 0
0029					: 1= INR COLOR
0030	SA 0000	LO A,(DATA0)			: HERE FROM BASIC ENTRY TO DISPLAY THE
0031	CS 0A00	WRCMB01			: CHARACTER WHOSE CODE IS IN "DATA0"
0032	CS 0A00	CALL LOP03H			: ENTRY WITH CODE IN A
0033					: LOAD REGISTERS FOR CURRENT POSITION
0034					: 8C=CURSOR POSITION PAGE "CURP3"
0035					: HL=DISPLAY FILE ADDRESS PAGE "OPADR3"
0036					: A=CODE FOR CHAR. TO BE DISPLAYED
0037					: TEST VALIO RANGE
0038					: < 20H
0039					: > 40H
0040					: SAVE CURSOR POSITION
0041					: TEST IF ASCII PRINTABLE (20H-7FH)
0042					: YES

166



00PE" EA 7"	299	AND	07P"	I TEST IF END OF VARS AREA
00PA" 2R 35	300	JR	2,NOSTRG	I NO PING - RETURN BC=2
00PC" BA	301	CP	0	I TEST IF MATCH
00PD" 2R 0E	302	JR	1,NRSTR2	I POUND STRING
00PP" 05	303	PUSH	DE	I SAVE STRING 10
0100" CC 1720	304	CALL	RECLN	I RETURNS ADDR. OF NEXT VAR. IN DE
0103" EE	305	SR	DE,HL	I ADRS. TO HL
0104" 01	306	POP	DE	I RESTORE STRING 10
0105" 1R P0	307	JR	NRSTR1	I LOOK AGAIN
0107" 23	308	INC	HL	I GET LENGTH
0108" 4E	309	LD	C,(HL)	
0109" 23	310	INC	HL	
010A" 44	311	LD	B,(HL)	
010B" 23	312	INC	HL	I FIRST TEXT CODE
010C" TE	313	LD	A,B	I TEST IF NULL STRING
010D" E1	314	OR	C	
010E" C8	315	RET	2	I RETURN IF 00
	316	I		
	317	I		
	318	I		I ENTRY FROM MACHINE CODE WITH
	319	I		I ADDRESS OF CHAR. CODE "STRING"
	320	I		I IN HL AND LENGTH IN EC
	321	I		
010P" TE	322	NRSTRG	LD A,(HL)	I GET CODE
0110" ES	323	PUSH	HL	I SAVE ADDRESS
0111" CS	324	PUSH	BC	I AND COUNT
0112" C0 0040"	325	CALL	NRCHAB	I WRITE "A"
0113" T9	326	LD	A,C	I TEST IF GOOD
0116" E0	327	OR	B	
0117" 20 09	328	JR	N2,NRSTR1	I EXIST 1P INVALSO CCOS OR
	329	I		I IF SCREEN FULL
0119" C1	330	NRSTR3	POP BC	I COUNT
011A" E1	331	POP	HL	I ADDRESS
011B" 23	332	INC	HL	I NEXT CHAR.
011C" 08	333	DEC	BC	I ADJUST COUNT
011D" T8	334	LD	A,B	I TEST IF DONE
011E" E1	335	OR	C	
011F" 20 EE	336	JR	N2,NRSTRG	I WRITE NEXT CHAR.
0121" C9	337	RET		I RETURN BC=0
	338	I		
0122" T9	339	NRSTR1	LD A,C	I SAVE ERROR
0123" PE 05	340	CP	S	I TEST SP UNRSOGRNS2EC CODE
012E" 2E P2	341	JR	1,NRSTR3	I CONTINUE
0127" E1	342	POP	HL	I REMAINING COUNT TO HL
012E" 22 003B"	343	LD	(STRGCT),HL	I STORE REMAINING COUNT
012B" E1	344	POP	HL	I ADDRESS TO HL
013C" 0E 03	345	LD	C,3	I RETURN EC=3
012S" 0A 00	346	NRSTR2	LD B,0	
0130" C9	347	RST		
	348	I		
0131" 0E 02	349	NOSTRG	LD C,2	I RETURN BC=2 SP STRING NOT POUND
0133" 50 P9	350	JR	N2,NRSTR2	
	351	I		
	352	I		
	353	I		
	354	I		
0135" 80 4E 0007"	355	SSTCBO1		I MSBE FROM BASIC SNTRY. PARAMETERS
	356	I		I IN LINCOL
	357	I		
0139" CC 043A"	358	LD	0C,(LINCOL)	
013C" C0 044B"	359	I		
013P" CC 045A"	360	SSTCUR1		I SNTRY WITH LINS/COL. IN 0C REG.
0142" C3 000D"	361	CALL	TSTPAB	I TEST PARAMETERS FOR VALIDITY
	362	CALL	CNVPP	I CONVERT TO INTERNAL FORMAT
	363	CALL	MPDPOSN	I CALCULATE AND STORE POSITION
	364	CALL	GCCORST	I RETURN BC=0
	365	I		
	366	I		
	367	I		
	368	I		
	369	I		
0143" 80 4E 0030"	370	SCRLO01		I MSBE FROM BASIC SNTRY TO SCROLL
	371	I		I SCROLL
	372	I		
0149" 80 4E 0030"	373	LD	0C,(SCRCTLS)	I 007 CONTROL INFO.
	374	I		
	375	I		
	376	I		
	377	I		
0140" T8	378	LD	0,0	I MSBE WITH CONTROLS IN 0C
014A" 67	379	AND	A	I 0=NO. OF LINES
014B" CA 00C3"	380	JP	3,SNVPAB	I C=STARTING LINS NO.
014S" 05	381	LD	A,C	I TEST VALS0177
014P" PE 05	382	CP	S	I
0121" 0A 00C3"	383	JP	C,SNVPAB	I 0000 IF COUNT=0
015A" PE 19	384	CP	3E	I 0000 IF 0+C<1
012E" 02 00C2"	385	JP	NC,SNVPAB	I 0000 IF 0+C>14
0159" CC 01E"	386	CALL	SCRLO	I 00 SCROLL
013C" C3 0053"	387	JP	GCCORST	I RETURN BC=0
	388	I		
015P" CS	389	SCRLO	PUSH 0C	I 007 STARTING LINS IN INTERNAL FORMAT
0140" 3S 5E	390	LD	A,3C031	
0161" 05	391	SUS	C	
0163" AT	392	LD	0,A	
0164" 3A 003E"	393	LD	0,(LINSLEN)	
0167" 3C	394	INC	S	I COL. 0
0168" 4P	395	LD	C,0	
0169" C0 0474"	396	CALL	LMBU	
016C" C1	397	POP	0C	
016D" CS	398	PUSH	0C	I SAVE ORIG.0C FOR SRST
016E" 7C	399	LD	A,L	I TEST SP AT START OF BLOCK
016F" 47	400	AND	A	
0170" 2R 35	401	JR	1,STRLE	I YES
0172" 07	402	RLCA		
0173" 07	403	RLCA		
0174" 07	404	RLCA		
0175" 4P	405	LD	C,0	I 007 NO. OF LINES THIS BLOCK
017A" 3S 00	406	LD	A,0	
017E" 91	407	SUB	C	
0179" 0E	408	CP	0	I TEST 0GASINST TOTAL LINES
017A" 3E 3P	409	JP	C,GRBLR	I TOTAL GRATER THAN THIS BLOCK
017C" 7E	410	LD	A,0	
017D" 0A 0E	411	LD	E,0	I REMAINING COUNT

```

017P* C9
0140* 47
0181* 01 00
0183* 70
0184* C9
0189* 0P
018A* 0P
0187* 0P
019A* 4P
0199* 7A 00
018A* 00
018C* 21 PP50
018P* 19
0190* 00
0191* C9
0192* 05
0193* 00 00
0195* 01
019A* C1
0197* 7C
0198* CA 0P
019A* 20 05
019C* 70 20
01A0* 07
019P* 10 0A
01A1* 00 0P
01A3* 47
01A4* 2A
01A5* C1
01A6* 03
01A7* 20 0A
01A9* C1
01AA* 70
01AB* 47
01AC* 20 0A
01AD* 20 00
01AE* 10 0C
01B2* C1
01B3* 70
01B4* 00
01B5* 30
01B6* 4P
01B7* 00 01
01B8* 10 02
01B9* 4P
01BC* 70
01BD* 41
01BE* 47
01BF* C9
01C0* 70
01C3* 10 00
01C3* 09
01C4* C9
01C5* 00 00
01C7* C0
01C8* 30
01C9* 21 P000
01CC* 10
01CD* 30
01CE* 01 0020
01D1* 30
01D2* 30 00
01D4* 31
01D5* 7C
01D6* C0 0P
01D8* 20 05
01DA* 7A 20
01DC* 47
01DD* 10 00
01DE* 00 0P
01E1* 47
01E2* 2A
01E3* C1
01E4* 00
01E5* 20 00
01E7* C1
01E8* 70
01E9* 47
01FA* 20 C0
01FC* 11 P020
01FD* 19
01FE* C3 0140*
01P3*
01P3* 0C 40 000C*
01P7*
01P7* 70
01P8* 47
01P9* CA 00C2*
01PC* 01
01PD* 70 01
01PP* 0A 00C2*
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020A* 02 00C2*
0207* CC 0200*
020A* C3 0000*
0200* C9
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632          SUBDTL "GET" ROUTINES
633          I
634          I
635          BTCH001      I HERE FROM BASIC ENTRY TO GET CHARACTER
636                      I (RETURNS CODE FOR CHARACTER AT
637                      I POSITION SPECIFIED IN GETCTL)
638                      I GET CONTROL INFO.
639          LD          BC,(BTCTYL)
640          I
641          BTCHAR1      I ENTRY WITH POSITION IN BC
642                      I TEST PARAMETERS
643                      CALL 757PAR      I CONVERT TO INTERNAL FORMAT
644                      CALL CONVM      I GET DISPLAY FILE ADRS.
645                      LD          DE,(CMTBL)      I CHAR.TABLE
646                      LD          0,0      I NO. OF PRINTABLE CHARACTERS
647                      LD          A,00H      I SET ADJUSTMENT INDEX
648                      BTCH1      LD          (BTINDR),A
649                      INC          D
650                      ER          DE,HL      I ADJUST TO START OF TABLE
651                      BTCH2      PUSH          BC      I OF ADRS. IN BTCHAR. SET IN HL
652                      PUSH          DE      I SAVE COUNT
653                      PUSH          HL      I SAVE ADRS. IN CP
654                      LD          A,(DE)      I SAVE ADRS. IN CHAR.TABLE
655                      ROR          (HL)      I SCAN ROW PACH OP
656                      JR          2,GTCH3      I TRY AGAINST CHAS. SET
657                      PUSH          0P      I MATCH
658                      LD          6,(BTINDR)
659                      CP          00H      I TEST IF STD. GRAPHICS
660                      JR          NS,GTCH23
661                      POP          AP
662                      BTCH23      JR          6TCH4      I DO NOT TEST FOR INVERSE
663                      INC          6
664                      JR          N2,6TCH4      I TEST INVERSE
665                      DEC          A
666                      LD          C,A      I A=-1 FOR INVERSE
667                      LD          5,T      I C=0 FOR MATCH -1 FOR INVERSE
668                      BTCH31      INC          D
669                      INC          HL      I NEXT SCAN IN OP
670                      LD          A,(DE)      I NEXT BYTE IN CHAR.SET
671                      AOR          (HL)
672                      XOR          C
673                      JB          N2,6TCH4      I NO MATCH
674                      OJN2      GTCH31      I FOR NEXT SCAN
675                      I
676                      I
677                      I
678                      I
679                      LD          A,C
680                      POP          RC
681                      POP          BC
682                      POP          BC
683                      LD          C,A      I 0=COUNT C=-1 IF MATCH ON INVERSE
684                      LD          A,(BTINDR)      I ADJUST CHAR.COD3
685                      SUB          0      I A=CHAR.CODE
686                      CP          20H      I TEST IF SPACE
687                      JR          N2,GTCH52
688                      INC          C
689                      JR          N1,GTCH52      I TEST IF MATCH ON INVERSE SPACE
690                      LD          A,0PH      I SET CODE FOR GRAPHICS BLOCK
691                      BTCH32      LD          C,A
692                      LD          0,0
693                      SET          I
694                      I
695                      BTCH4      PDP          HL      I HERE WHEN NO MATCH
696                      LD          00,0      I LOCK AT NEXT CHAR. IN SET
697                      ADD          HL,DE      I CHAR. SET
698                      PDP          DE      I CHAR. IN OP
699                      PDP          BC      I CHAR.COUNT
700                      DJNZ      GTCH2      I LOCK AGAIN
701                      I
702                      I
703                      I
704                      I
705                      BX          DE,HL      I HERE TO TEST IF DONE
706                      LD          A,(BTINDR)      I CP ADRS. TO HL
707                      CP          00H      I TEST IF STD. CHR. SET
708                      JR          NS,6TCH5      I TRY GRAPHICS
709                      LD          00,(CMTBL)      I STD. GRAPHICS TABLE
710                      LD          0,16      I NO. OF ENTRIES
711                      LD          A,00H      I INDEX
712                      JR          0TCH5
713                      BTCH3      CP          00H      I TEST IF STD. GRAPHICS
714                      JR          N2,GTCH4      I DONE IF NOT
715                      LD          DE,(UDG)      I TAY USER-DEFINED GRAPHICS AREA
716                      DEC          0
717                      LD          0,21      I ADJUST ADDRESS-100H
718                      LD          A,0A9H      I NO OP ENTRIES
719                      JR          0TCH1      I INDEX
720                      BTCH6      LD          C,0      I HERE WHEN NO MATCH ANYWHERE
721                      RDB          0
722                      SET          0C AND A=IERC
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032P' 032P'
032C' EC 4E 0034'
032C' CD 044E'
032P' 3A 0033'
032P' 09
032P' 2E 8E
032P' 08 00
032P' 3C
032P' 47
032P' P8 1E
032C' 58 02
032P' 0A 3T
0340' 0A 43 000T'
0344' C9

0345'
0345' 3A 0024'
034E'
034E' A7
0349' 28 4A
0340' PE 0A
0340' C2 00C2'
0350' 21 1701
0353' 22 0028'
035A' 3E 17
035E' 32 0020'
035E' 38 01
0350' 32 002E'
03AD' 3E 40
0382' 32 0033'
0383' 21 1800
038E' 22 000C'
038B' AP
03AC' 32 0038'
03AP' 32 0033'
0372' 32 003C'
0375' 21 3C03
0378' 22 002F'
037E' 21 0395'
037E' 22 0031'

03E1' 32 0000'
03E4' 32 0007'
03E7' 32 0008'
03E8' 32 0013'
03E0' 32 0019'
0390' 32 001A'
0593' 5E 06

0399' 47
039A' 3A 5CC2
0399' A7
039A' 20 10
039C' 80
0390' CA 0C30'
03A0' 21 12C0
03A3' 11 0840
03A4' 19
03A7' EC 58 5CA5
03A8' 15
03AC' 0A 03P6'
03AP' ED 5E 5CB2
03B3' A7
03B4' ED 52
03BA' 02 03PA'
03B9' CC 0421'
03BC' 7E
03BD' CD 03PE'
03C0' C3
03C1' 7E
03C2' CD 0EAE
03C5' CC 042E'
03CE' C1
03C9' 79
03CA' 32 3CC2
03CD' CE 77
03CP' CA 00ED'
03D2' 3E C1
03D4' 80
03D5' 2P
03DA' CB 3E
03DB' CE 38
03DA' CB 3E
03DC' 80
03DD' 32 0039'
03D0' 32 0011'
03E3' 3A 0033'
03EA' 3C
03E7' 4P
03EE' 0A 1E
03EA' 21 0000
03ED' 22 3C70
03F0' CD 043A'
03F3' C3 0080'
03F8' 0E 02
03F8' C3 008P'

738 007CUE1
73T LD BC,(CURPOSS ; GET INTERNAL POSITION
73E CALL COMVPM ; CONVERT TO USER PDMAT
739 LD A,(L3ML8N)
740 CP C ; TEST IF END OF L3NEC=643
741 JB NS,007C3 ; NO
742 LD C,8 ; NEXT POSN, START OF NEXT L3NE
743 LD A,8 ; BUMP TO NEXT L3NE
744 SMC B
745 LD B,A
746 CP 24 ; TEST SP OFF SCREEN
747 JB C,007C3 ; NO
748 LD B,25 ; POSN. 25 BT BOTTOM L3NE
749 007C3 LD (L3MCOL5,0C ; STORE POS BASIC PROGRAM
750 RST ; VALUES ALSO IN BC
751
753 SURETTL VIDEO MODE CONTROL
754
755
756 STM0001 ; HERE FROM BASIC ENTRY WITH MODE IN
757 ; "VIM00E"
758 LD A,(VIM00E)
759
760 3ETM00E1 ; ENTRY WITH MODE IN A
761
762 AND A
763 JE 2,3ETM01 ; TEST FOR VALIDITY
764 CP A
765 JP N2,INVPAR ; INVALID PARAMETERS
766 LD HL,SCIN3T ; INITIALIZE VARIABLES
767 LD (SCRCYCL3,HL) ; SCRCCL CONTROL
768 LD A,23
769 LD (ECTLN3),A ; BOTTOM L3NE
770 LD A,1
771 LD (SCPLCT3),A ; SCRCCL COUNT
772 LD A,A4
773 LD (CLIMLN3),A ; L3NE LENGTH
774 LD HL,CLINIT ;
775 LD (CLBCT3,HL) ; CLEAR SCREEN CONTROL
776 XOR A
777 LD (MASKE3),A ; MASK BYTE
778 LD (STRGCT3),A ; STRGCT REMAINING COUNT
779 LD (STRGCT3),A ;
780 LD HL,C4RSET ; STD.CHAB.SET
781 LD (CNTIL3),HL ;
782 LD HL,GRPH3T ; STD. GRAPHICS SET
783 LD (GRTEL3),HL ;
784
785
786 LD (DATAE3),A ; DATA BYTE
787 LD (L3MCOL3),A ; COLUMN
788 LD (L3MCOL3+3),A ; LINE
789 LD (MSRCYCL3),A ; MASK CONTROL
790 LD (GETCYL3),A ; "GET" COLUMN
791 LD (GETCYL3+1),A ; "GET" LINE
792 LD B,6 ; SET M/M TO 06-CCL,MO00
793
794 3ETM03 LD B,A ; SAVE MODE
795 LD A,(VIM00E)
796 AND A ; TEST CURRENT MODE
797 JB N3,3ETM02
798 OR B ; CURRENT MODE=0 TEST IF NEW MODE=0
799 JP 2,GOCDRET ; RETURN BC=0
800 LD HL,MSERT33
801 LD DE,MOVES2
802 ADD HL,DE ; HL=TOTAL ROOM NEEDED
803 LD CE,(STKEN0)
804 ADD HL,DE ; ADD MEMOBT SM USE
805 JP C,ENTHRM ; NOT ENOUGH MEMOBT TO OPEN DP2
806 LD (CRANTOP3)
807 AND A
808 SMC HL,DE
809 JP MC,EXTN3M ; NOT ENOUGH MEMOBT
810 3ETM02 CALL ENHLEXT ; ENHLE ROM EXT.
811 LD A,E ; MODE TO A
812 CALL GETVAL ; M/M VALUE TO E: V3DMOD VALUE TO C
813 PUSH BC ; SAVE
814 LD A,B
815 CALL CHNGV30 ; CALL RTN. TO OPEN/CLOS3 2ND OF
816 CALL ENHLMCH3 ; REENABLE MORE BARR
817 POP BC
818 LD A,C
819 LD (VIM00E3),A ; UPDATE VIM00E
820 UPDATE 3T A,A ; UPDATE SYSTEM VARS. 263ED ON MODE
821 JP 3,GOCDRET ; DONE IF MODE 0
822 LD A,CC3M ; 04/EO-COL, MOOE
823 OR B ; STORE ATTRIBUTE BYTE BASED
824 CPL ; ON 3MK SELECT30M
825 SCL E
826 SCL E
827 SCL B
828 OR E
829 LD (ATTET3),A ; ATTRIBUTE BYTE
830 LD (ATTCT3),A ; BASIC PARAMETERS
831 LD A,(CLIMLN3) ; GET LINE LENGTH
832 SMC A
833 LD C,A ; COLUMN
834 LD B,SCB33 ; BC=HOME POSITION (LN,0/CCL,03
835 LD HL,0
836 LD (CDOR03),HL ; INITIALIZE PLOT POSITION
837 CALL UPPOS3M ; UPDATE AND STORE HOME POSITION
838 JP GOCDRET ; RETURN EC=0
839 EXTNRM LD C,2 ; RETURN BC=2 FOR NO ROOM
840 JP E3REET
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045 SUBTTL INTERNAL SUB-RTN3.
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		947		1 RETURN3 OP ADDR3, IN HL. PRESERVES
		948		1 LINE/COLUMN POSITION IN BC
049E' CD 0470'		949	1	
049E' 30 0033'		950	CALCPDS COLL LN9U	1 GET DISPLAY FILE ADDR3.
04A1' 3C		951	LD A,(LINLEN)	1 TEST WHICH OP
04A2' 91		952	INC A	
04A3' CE 47		953	SUE C	
04A9' 2R 0A		954	EIT 0,0	
04A7' F9		955	JR 2,CALCP1	
04A9' 3E 20		956	PUSH AP	
04A0' A4		957	LD A,20H	
04AE' A7		958	DR H	1 2N DP2
04AC' F1		959	LD H,A	
04AD' A7		960	PGP AP	
04AE' 1P		961	AND A	
04AF' 9P		962	CALCP1 RA4	1 COL/2 IS POS2Y2DN IN DISPLAY FILE
0470' 1A 20		963	LD 0,A	
0472' 19		964	LD D,E	
0475' C9		965	ADD HL,DE	
		966	RET	
		967	1	
0474'		968	1	
		969	LINEU1	1 GET DISPLAY FILE ADDRESS
0474' 3E 1E		970		1 FOR START OF LINE IN E
047A' 90		971	LD A,SC052	
0477' 37		972	SUE E	
047B' 0P		973	LD D,0	
0479' 0P		974	ARCA	
047A' 0P		975	ORCA	
047E' E4 E0		976	ORCA	
047D' AP		977	AND 0E0H	
047E' 7A		978	LD L,A	
047P' EA 1R		979	LD A,D	
0481' 0A 40		980	AND 1EH	
04E3' 67		981	DR 40H	
04E4' C9		982	LD H,A	
		983	RET	
		984		
0489'		985	9TPDSH:	1 STORE CURSOR POS2Y2DN
04E5' EC 43 0034'		986	LD (CURPCS),EC	
04E9' 22 003A'		987	LD (DPAC49),HL	
04EC' C9		988	RET	
		989	1	
04ED'		990	LOPDSH:	1 LOAD CURSOR POS2Y2DN
04E5' 0C 4E 2054'		991	LD EC,(CLAFD3)	
0491' 20 0035'		992	LD HL,(CPDAS)	
0494' C9		993	RET	
		994	1	
		995	1	
		996	1	
		997	1	
		998		
		1000	SUBTYL GRAPHICS CHAR.107	
		1001	1	
		1002	1	
		1003	1	
0499' 00		1004	GRPST 00FB 00022000b	1 RAGA 00 GRAPHIRE ADDR0
0496' 00		1005	00FB 00000200b	
0497' 00		1006	00FB 00022000b	
0498' 00		1007	00FB 00022000b	
0499' 00		1008	00FB 00022000b	
049A' 00		1009	00FB 00022000b	
049E' 00		1010	00FB 00000200b	
049C' 00		1011	00FB 00022000b	
		1012	1	
049D' 0P		1013	00FB 000C1111b	1 ADDR 01 GRAPHIRE
0498' 0P		1014	00FB 000C1111b	
049P' 0P		1015	00FB 000C1111b	
040E' 0P		1016	00FB 000C1111b	
04A1' 00		1017	00FB 00022000b	
04A2' 00		1018	00FB 00022000b	
04A5' 00		1019	00FB 00000000b	
04A4' 00		1020	00FB 00000000b	
		1021	1	
		1022		
04A5' 00		1023	00FB 11110000b	1 ADDR 02 GRAPHIRE
04A6' 00		1024	00FB 11110200b	
04A7' 00		1025	00FB 11110200b	
04A8' 00		1026	00FB 11110200b	
04A9' 00		1027	00FB 000C0000b	
04AA' 00		1028	00FB 000C2000b	
04AE' 00		1029	00FB 00000000b	
04AC' 0E		1030	00FB 00000000b	
		1031		
04AD' 0P		1032	00FB 11111111b	1 ADDR 03 GRAPHIRE
04A0' 0P		1033	00FB 11111111b	
04A0' 0P		1034	00FB 11111111b	
04A0' 0P		1035	00FB 11111111b	
0401' 00		1036	00FB 00000000b	
04E2' 00		1037	00FB 00022000b	
04A5' 00		1038	00FB 00020000b	
0404' 0P		1039	00FB 000C3000b	
		1040		
		1041	1	
04A5' 00		1042	00FB 00000000b	1 ADDR 04 GRAPHIRE
04EA' 00		1043	00FB 000C2000b	
04E7' 00		1044	00FB 000C0000b	
04A8' 00		1045	00FB 00022000b	
04E1' 0P		1046	00FB 000C1111b	
04AA' 0P		1047	00FB 000C1111b	
04E2' 0P		1048	00FB 000C1111b	
040C' 0P		1049	00FB 00001111b	
		1050	1	

INSDBL	4090						
INSEXT	700	400					
INV44R	180	182	2430	379	302	384	500
		511	513	402	745	916	
LDPQSH	175	9900					
L2MCDL	870	357	7498	767R	7888		
L2MLEM	1840	267	392	822	739	7730	882
		917	829	951			
LNBU	395	525	950	R490			
LOJO	4120	442					
LOJPO	4140	441					
LOJPI	4210	435					
M40K6	1580	223	626R	777R			
MOV852	710	72	801				
MSKCTL	1150	625	76R6				
MOSTRG	305	3490					
MYTSC	432	4340					
P404MR	480	283					
Q408R8	9150	920					
PR4MT	440						
R4MTOP	650	806					
R8CLEM	540	304					
SCIMIT	500	766					
SCRCTL	1280	283	372	7670			
SCRLO	285	385	3880				
SCRLE	45	1310					
SCRLE6	131	3700					
SCRLE7	1430	273	277R	2820	771R		
SCRLE7	445	4480	401				
SCRLL	43	3740					
SCR52	490	246	389	51R	834	933	871
S870	565	8840					
S87480	114	5840					
S87478	45	1140					
S87477	43	5970					
S87C80	100	3550					
S87CU8	45	1000					
S87CU8	43	3590					
S87M80	118	4240					
S87MD1	743	7940					
S87MD2	797	8100					
S87M08	47	1270					
S87M00	47	7400					
S87M58	45	1180					
S87M5K	43	6270					
S78L4	400	4440					
S78L40	4670	487					
S78L71	4680	481					
S78L42	478	4820					
STX8H0	62	803					
STM880	127	7560					
STPD5H	258	643	7870				
STRGCT	164R	343R	7788	7798			
SX76	8220						
T85740	3880	447	494				
T5781	863	87R0					
T57P4R	366	441	9120				
T57PR1	914	7170					
TVPUL1	275	2820					
TVPULQ	214	2460					
UD6	630	186	713				
UP0478	8200						
UP0P03	271	362	561	837	9480		
UP0T1	8310						
V4R8	610	287					
V20M08	670	609	615R	785	6598		
V2M008	1240	758					
W8CH0	94	1700					
W8CH11	187	1920					
W8CH12	188	1850					
W8CH13	191	194	1860				
W8CH14	286	2130					
W8CH18	212	2140					
W8CH2	2190						
W8CH3	224	2240					
W8CH5	2330	243					
W8CH7	2440						
W8CH48	43	1740	328				
W8CH76	48	940					
W8CH77	253	2580					
W8STR6	98	2530					
W8STR1	2980	307					
W8STR2	382	3880					
W8STR3	3300	341					
W8STR6	45	850					
W8STR8	43	3220	336				
W8STR1	328	1380					
W8STR2	3460	356					



## APPENDIX C-2

### 80 COLUMN MODE

Date: 12/16/83

ASC Number: 002

Version: 001

Author: C. Corcoran/D. Boyle

Name: 80 Column Mode Support

#### Description:

This component provides support to the application programmer for using the 80-column mode feature of the TS 2068. 80-Column Mode is implemented by using the 64-Column Mode feature of the 2068 and modifying the character width from 8 to 6 pixels. The service includes opening/closing the second display file (moving the machine stack, OS RAM routines and BASIC structures), PRINT position control, attribute control, clear screen and scroll screen services and display of characters. For use of use free BASIC, status is returned in the BC register pair, usually zero for successful completion and designates non-zero values for other conditions. The interface from BASIC is by means of POKing the input parameters to designated RAM locations prior to invoking the service via the USR function. See page 4 of the code listing for the memory addresses to be used from BASIC with the support code loaded starting at Chunk T (E000H/ST344).

#### DECLARATIONS

Name: SETMODE (SETMOD from BASIC - parameter to VMODE)

Input: MODE (0=normal, 8=80 column mode)

From machine code: Register A  
From BASIC: In VMODE

#### Description:

Sets specified video mode, opening or closing the second display file when needed. This involves determining if there is enough free RAM to open the second display file and if so, moving the BASIC structures and machine language variables over and the OS area down to make space for the machine stack and OS RAM routines at the top of memory. The second display file is cleared to zeroes. The affected system and internal variables are updated or initialized (see Usage Section). When returning to Mode 0 from 80 Column mode, the structures are returned to their normal locations.

Output: BC = 0 Successful  
BC = 1 Invalid parameters (not equal to 0 or 8)  
BC = 2 Not enough memory

Name: CLRSCN (CLRSCB from BASIC - parameter to CLSCTL)

Input: Line Count (1-24)  
Starting Line Number (0-23)

From Machine Code: Line Count in Register B  
Starting Line in Register C

From BASIC: Starting Line Number in CLSCTL  
Line Count in CLSCTL + 1

#### Description:

Clears to background color (PAPER) the designated number of lines, beginning with the Starting Line Number. Line 0 with a count of 24 clears the entire screen. Upon return, the cursor position is at the beginning of the first line cleared.

Output: BC = 0 for successful completion  
BC = 1 invalid parameters  
(Line Number + Line Count < 1 or > 24)

-----

Nome: SETCUR (SETCUR from BASIC - parameters to LINCGL)

Input: Line Number (0-23)  
Column Number (0-79) or (0-84)

From Machine Code: Line Number in Register B  
Column Number in Register C

From BASIC: Column Number in LINCGL  
Line Number in LINCGL + 1

Description:

Converts the requested position to internal format, determines display file address, and stores the values for use by the next display character operation. Note that once established, the position is updated automatically when a character is displayed so that it is only necessary to set the position when sequential display is not desired.

Output: RC = 0 for successful completion  
RC = 1 for invalid parameters (Line Number > 23 ,  
Column Number > Line Length-1)

-----

Nome: SETATT (SETATS from BASIC - parameter to ATTCTL)

Input: Attribute Byte - bit 7 - FLASH  
bit 6 - BRIGHT  
bit 5 - P  
bit 4 - A  
bit 3 - PER  
bit 2 - I  
bit 1 - M  
bit 0 - K

From Machine Code: Register A  
From BASIC: in ATTCTL

Description:

The specified INK color (0-7) is used to set the video mode hardware and to update VIDMOD. The complementary PAPER color is fixed by the INK selection. FLASH and BRIGHT are fixed at zero by the hardware. Note that in 80 column mode the entire screen has the same attributes.

Output: BC = 0 Successful

-----

Nome: WACHR (WACHB from BASIC - parameter to DATAB)

Input: Character code for character to be displayed

20H TO 7FH - Std. TS2068 Character Set  
80H TO 9FH - Std. Graphics Set  
A0H TO A4H - User-Defined Graphics Set

From Machine Code: Register A

From BASIC: in DATAB

Description:

Displays character at current cursor position, applying current mask. Moves cursor position on to next sequential position. If character would start a new line after BOTLN (see Usage section) and the scroll count (variable SCRLCT) decrements to zero, the character will not be displayed and return will be made with BC = 3 (screen full). If the scroll count does not decrement to zero, the screen will be scrolled up one line using the information in SCRLCT and the new line started at the vacated line.

Note that only the first 8 bits of each byte in the User Defined Graphics area will be transferred to the display file.

Output: BC = 0 for successful completion  
BC = 1 invalid character code  
BC = 3 for screen full

```

-----
Name: SETMSK (SETMSB from BASIC - parameters to MSKCTL)

Input: Mask Byte - bit 0 - OVER
          bit 2 - INVERSE

From Machine Code: Register A
From BASIC: MSKCTL

Description:
The specified mask is stored for application to all subsequent
display character operations. (OVER = 1 implies new character
combined with old using an XOR operation; INVERSE = 1 implies
character is inverted).

Output: BC = 0 for successful completion

```

```

-----
Name: WRSTRG (WRSTRB from BASIC - String Identifier to PARAMS)

Input: Character Code String

From Machine Code: Address of string in HL
                  Count in BC

From BASIC: String Variable Identifier in
            System Variable PARAMS - 237AT (5CC3H)

Description:
Displays the characters from the string, beginning at the current
cursor location and continuing sequentially until the count
exhausts, or "Screen Full" is detected (see WRCMR description and
Usage Section on Automatic Scrolling). For the Screen Full
condition the remaining count is stored in the internal variable
STRGCT for access by the user.

NOTE: Characters within the string which are outside of the
supported range (32 through 16A (20H-AAH)) will be
ignored. E.g., BASIC Token codes and control codes
embedded in an INPUT string will not be displayed or
decoded.

From BASIC: PDKE the code for the string variable identifier into
PARAMS prior to invoking WRSTRB, e.g.

```

```

0005 LET @S="-----string-----"
0010 PDKE 237AT, CODE "e"
0015 IF USR (WRSTRB)<>0 THEN -----
      (continue)

```

```

Output: BC = 0 Successful
        BC = 2 BASIC - String not found
        BC = 3 Screen Full - Remaining Count in STRGCT
              (HL = Current Address in String)

```

```

-----
Name: SCROLL (SCRLB from BASIC - parameters to SCRCCTL)

Input: Line Count (1-23)
       Starting Line Number (1-23)

From Machine Code: Line Count in B
                  Starting Line in C
From BASIC: Starting Line in SCRCCTL
            Line Count in SCRCCTL + 1

Description:
Scrolls the designated number of lines up 1 position, starting at
the specified line number and inserts a blank line at the bottom
of the scrolled area. Line 1 with a count of 23 will scroll the
entire screen up 1 line. Upon return, the cursor position is at
the beginning of the inserted blank line.

Note: See Usage Section on "automatic" scrolling.

Output: BC = 0 Successful
        BC = 1 Invalid Parameters
              (Line Number + Line Count < 1 or > 24)

```

-----  
Name: GETCHAR (GETCHR from BASIC - parameters to GETCTL)

Input: Line/Column position as for SETCUR

From Machine Code: Line Number in B  
Column Number in C

From BASIC: Column Number in GETCTL  
Line Number in GETCTL + 1

Description:

Returns in register C of the BC register pair the character code for the character at the designated screen position. If no match against the character set (including the standard and user-defined graphics) is found, zero is returned. (Character code or zero also returned in A.)

Note: Positions "printed" using the OVER technique will return zero if they do not match against any single character.

Output: BC = 0 for no find  
BC = 1 invalid parameters  
BC = character code (2Bh-44H)

-----  
Name: GETATT (GETATB from BASIC - parameters to GETCTL)

Input: As for GETCHAR

Description:

Returns in register C of the BC register pair the attribute byte for the character at the designated screen position. Note that in 80 column mode the entire screen has common attributes. The value returned will describe the current selection:

Output: BC = 1 for invalid parameters  
BC = attribute byte (as for SETATT)

-----  
Name: GETCUR (GETCUR from BASIC)

Input: None

Description:

Returns in the BC register pair and in the BASIC parameter location LINCOL, the current print position (where the next character would be displayed).

Output: B = Line number (0-23)  
C = Column number (0-79) or (0-84)

BASIC: LINCOL = Column number  
LINCOL + 1 = Line number

NOTE: If the last character was printed at Col.79 (84) of Line 23 (last position on the screen), then Col.0/Line 23 will be returned.

-----  
MASSBI

Memory Usage:

This package of machine code routines includes the following internal variables:

Name	Size	Description
SCRCYL	2	Scroll Control LSB = Starting line number MSB = Number of lines to be scrolled
BDYLN	1	Bottom Line = Line number (0-23) after which test for scroll will be made.
SCRLCT	1	Scroll Count= Number of times + 1 that automatic scroll will be done. When decremented to zero, data will not be displayed and a condition code will be returned to user.

CMTBL	2	Character Table (Base Address=100H)
GRTBL	2	Std.Graphics Character Table (Base=100H)
LINLEN	1	Line Length - (80 or 85 when in 80-Col.Mode)
CURPOS	2	Current Position (Internal Format) LSB = Column Position MSB = Line Position
DFADDR	2	Current Display File Address
MASKB	1	Mask Byte (bit 0 = EVER) (bit 2 = INVERSE)
ATTBYT	1	Attribute Byte (bits 0 - 2 = INK) (bits 3 - 5 = PAPER) (bit 6 = BRIGHT (Set to zero (bit 7 = FLASH by M/A in 80-col.mode)
GTINDX	1	"Get" Index - Used by GETCHAR
STRGCT	2	String Count - Contains remaining byte count when EC=3 (Screen Full) is returned from the Write String service WRSTRG (WRSTRB).
MARGIN	1	Margin - Margin Adjust (0-2)
DFBIT	1	Display File Bit - Current Bit Position

Initial values set via SETMODE (SETMOB) are as follows:

Variable Name	Value
-----	-----
SCRCTL	1701H
BOTLN	17H
SCRLCY	1H
CMTBL	(Internal to Module)
GRTBL	(Internal to Module)
LINLEN	50H
CURPOS	1051H
DFADDR	4000H
MASKB	0H
ATTBYT	38H
GTINX	80H
STRGCT	0H
MARGIN	1H
DFBIT	7H

The following are the variables used for passing parameters in BASIC. The # indicates those initialized by SETMOB:

Variable Name	Size	Value
-----	-----	-----
* DATAS	1	0H
* LINCGL	2	0H
* CLRCTL	2	1800H
* ATTCTL	1	38H
* MSKCTL	1	0H
* GETCTL	2	0H
VIMGCE	1	

In addition, VIDMOD, PARAMS and other system variables must be available to these routines. At minimum, chunks 0-3 and chunk 7 must be enabled in the Home Bank.

#### Location:

This package can be incorporated into a machine code program within the memory range from chunk 4 through 7, taking into consideration the remapping of certain structures when the second display file is open. NOTE: Machine code above RAMTOP is not moved.

The routine SETMODE (SETMOB) cannot be executed from a cartridge because of the necessity to enable the RDM Extension which disables the DQCK Bank.

#### Registers:

Other than as documented for output values, no claims are made as to preservation of any Register contents except for the IV Register which must always contain the value 3C3Ah for access to the standard system variables.

#### Automatic Scrolling:

As initialized, test for scrolling will be made when the print position goes to the next line following the bottom line on screen (BOTLN=23x24th line). Condition Code 3 (Screen Full) will be returned since SCRLCT will decrement to zero. If SCRLCT is set to some larger value, then the parameter in SCRCTL will be used to automatically scroll the designated number of lines beginning at the specified line number. As initialized, this will scroll the entire screen up. By performing a POKE or setting the variables BOTLN and SCRCTL to the desired values, automatic scrolling can be done using earlier sections of the screen. When working from BASIC it is recommended that BOTLN be set to Line 21 (15H) and SCRCTL+1 be set to 21 (15H) to avoid conflict with the Edit Line which uses the bottom two lines of the screen. Note that once SCRLCT expires, it will be set to 1. If a different value is desired, it must be reinitialized by the user after receiving the 'Screen Full' condition.

By setting the SCRCTL variable and invoking the SCROLL (SCRLB) routine any portion of the screen may be scrolled at any time.

#### Margin Control:

In 80-Column Mode, there are actually 85 character positions per line. The variable MARGIN determines the offset of the beginning of the 80 column line from the left side of the screen and has valid offset values of 0, 1 or 2. An offset value of 1 centers the 80 column line on the screen; 0 begins at the extreme left side; 2 terminates at the extreme right side. The default value is 1. When MARGIN is set to 0, the variable LINLEN can be set to 85 to permit access to the 5 extra print positions. Whenever MARGIN and/or LINLEN are modified, a 'Set Cursor' operation should be done to insure the integrity of the print position.

NOTE: Since the different MARGIN values result in different pixel positions for the columns, care must be taken in mixing line length and margin values on the same line.

#### ADDITIONAL NOTES:

1. All screen operations done by the system ROM (PRINT, LIST, Edit line I/O, CLS, scrolling, etc.) relate only to the main Display File and work with standard 8-pixel wide characters. This means that every alternate 8 pixels across the screen will be affected. You will want to execute the Clear Screen function in this module to guarantee that no data in the second display file interferes with the use of a system screen service, e.g., prior to doing a LIST.
2. The COPY command will print only every other grouping of 8 pixels across the screen to the 2040 Printer.
3. During tape operations, the border will not change while in 80-column mode since this is fixed by the hardware to conform to the paper color.
4. The SAVE filename SCREENS will save only the main Display File data. The second can be saved by a SAVE filename CODE 24376,6144. Be careful that you have the computer in 80-column mode when you load this data or you will overwrite the 85 RAM routines and "crash" the system!! (The count for saving the display file is for the data portion only since the Attribute File area from 7880H-7AFFH (30720-31487) is not used by the video card M/M in 80-Column mode.



```

118 SUBSTTL D7-ES VARIABLE1
119
0020 17 127 1
120 0DTLN 0000 17M 1 BOTTOM LINE (LINE AFTER 87369
121 1 WHICH SCREEN IS CURRENTLY
122 1 FULL. AUTOMATIC SCROLL
123 1 WILL BE DONE IF SCROLL
124 1 SCREEN NOT OCCURENT TO 0.
125 1 SCROLL COUNT - 1 PLUS NO. 87290
126 1 OF TIMES AUTOMATIC SCROLL
127 1 WILL BE DONE.
128 1
0020 01 129 0000 0000 2 1 ADDRESS OF CHARACTER TABLE 87391
130 1 ADDRESS OF STC GRAPHICS TAB. 87393
131 1 LINE LENGTH (80 OR 88) 87395
132 1 CURRENT POSITION 87396
133 1 (INTERNAL FORMAT)
134 1
0020 0000 135 0000 0000 4800M 1 CURRENT DISPLAY FILE ADDR. 87398
136 1 MAIN PG BE APPLIED TO 87400
137 1
0020 00 138 0000 0000 38M 1 DISPLAY CHARACTER 87401
139 1 CURRENT ATTRIBUTE 87402
140 1 (38-ELACK ON WHITE)
141 1 INDEX FOR ADJUSTING 87403
142 1 CH. CODE IN CTC-AR
143 1 REMAINING COUNT FROM WP178
144 1 FROM "SCREEN PULL" 87405
145 1 MARGIN ADJUST (0-2) 87406
146 1 017 POSN. IN LINE (7,1,3,8) 87408
147
148 SUBSTTL WRITE CHARACTER
149
0030 149 1
150 1 HERE FROM BASIC ENTRY TO DISPLAY THE
151 1 CHARACTER WHOSE CODE IS IN "DATA"
0030 3A 0000 152 LD A,(DATA)
153
0040 154 1
155 1 ENTRY WITH CODE IN A
0040 CD 0010 156 CALL LOP2EN 157
158 1 LOAD REGISTER FOR CURRENT POSITION
159 1 AC-CURSOR POSITION FROM "CURPOS"
160 1 ML-DISPLAY FILE ADDRESS FROM "DPAOR3"
161 1 A-CODE FOR CHAR. TO BE DISPLAYED
162 1 TEST VALID RANGE
163 1 < 20H
164 1
165 1 > 46H
166 1 SAVE CURSOR POSITION
167 1 TEST IF ASCII PRINTABLE (20H-7FH)
168 1 YES
169 1 TEST IF STC GRAPHICS (80-8FH)
170 1
171 1 USER -DEPINDO GRAPHICS (90-9FH)
172 1 ADJUST ADDRESS-100H
173 1 ADJUST PC IN INTR INTO TABLE
174 1
175 1 STC GRAPHICS TABLE
176 1 ADJUST PC IN INTR INTO TABLE
177 1
178 1 CHARACTER TABLE
179 1 DE-POIN. IN DISPLAY FILE
180 1
181 1 CODE IS INTR INTO TABLE
182 1
183 1
184 1
185 1
186 1
187 1
188 1
189 1
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194 1
195 1
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000P 2E 0R
00C1 37
00C2 C8 1E
00C4 C8 19
00C6 30
00C7 20 P9
00C9 7C
00CA 40
00CB 67
00CC 70
00CD 41
00CE 6P
00CF 3A 003E
00D2 04 0T
00D4 8D 4A
00D6 83
00D7 0D A8 00
00DA 00 AA 01
00DD 4A
00DE 0E 00
00E0 P0
00E1 70
00E2 8A PC
00E4 4T
00E9 P1
00EA E1
00E7 20 00
00E9 A7
00EA C8 1E
00EC C8 19
00EE 3C
00E8 2C P9
00P1 7C
00P2 A8
00P3 AT
00P4 70
00P5 AE
00P6 AP
00P7 0C 7E 02
00P8 AT
00P9 20 1A
00PJ 3A 003E
0100 DA 0T
0102 80 4A
0104 01 PC00
0107 20 0R
0109 A7
010C C8 1E
010E C8 19
0108 30
0109 20 P9
0111 7C
0112 A8
0113 0T
011A 70
011B A9
011A AF
0117 EE
0118 73
0119 E1
011A 72
011B 01
011C C1
011D 08
011E 2A
011F 15
0120 3C
0121 C2 009R
0124 E1
0128 C1
0129 00
0127 3A 003E
0128 0A 0A
012C 32 003E
012F 30 0E
0131 CA 0E
0133 32 003E
013A 7C
013Y EE 20
0139 A7
013A C8 0P
013C 20 01
013E 23
013P CC 00E0
0142 0E 00
0144 0A 00
014A C9
0147 0E 01
0149 1E PE
0140 1E 1E
014D 90
014E 17
014P 3A 002D
0102 0A
0153 02 0107
01E6 1A 002E
0159 3C
016A 2E 00
016C 3E 01
01EE 32 002E
0161 C1
0162 C1
0163 0E 03
0164 10 05
0167 31 002E
016A 8D 40 0020
01AE C3 01E4
22E JR 2,MORR1
229 SCP
230 NERT1 RR 0
231 RR C
232 OEC A
233 JR N1,NERT1
234 MORR1 LD A,M
23E AND 0
23A LD M,A
237 LD A,L
230 AND C
239 LD L,A
240 MORR1 LD A,(0P017)
241 SUB 7
242 NEG
243 PUSH HL
244 LD L,(LH)
24E LD M,(LH+1)
240 LD 0,(HL)
24T LD C,3
240 PUSH AP
249 LD A,0
2E0 AND 0PCH
211 LD 0,A
212 POP AP
2E3 POP HL
2E4 JR 2,MCAR2
2EE AND A
25A NERT2 RR 0
2ET RR C
2EE OEC A
209 JR N1,NEXT2
2A0 MORR2 LD A,M
2A1 RDR 0
2A2 LD M,A
2A3 LD A,L
2A4 RDR C
265 LD L,A
2AA LD A,(LH+2)
207 AND A
260 JR 1,NOINVERT
269 LD A,(0P017)
270 SUB 7
271 NEG
272 LD 0C,0P00H
273 JR 2,MGR3
274 AND A
27E NERT3 RR 0
27A RR C
277 DEC A
27E JO N1,NEXT3
279 MORR3 LD A,M
280 RDR 0
281 LD M,A
282 LD A,L
283 RDR C
284 LD L,A
280 NOINVERT EX DE,HL
284 LD (HL),0
287 POP HL
280 LD (HL),0
28E POP DE
291 POP 0C
291 EX AP,AP
292 INC H
293 INC D
294 DEC A
290 JR 02,MGR4
296 WICHT POP HL
297 POP 0C
290 OEC C
299 LD A,(0P017)
300 SUB 0
301 LD (0P017),A
302 JR NC,MGR4
305 ADD A,0
304 LD (0P017),A
30E LD A,M
30A RDR 20H
307 LD M,A
300 BIT S,A
30E JO N1,MGR4
310 INC HL
311 WRCMT CALL STPCM
312 GODDRE? LD C,0
313 ERARE? LD 0,0
314 RET
315
316 INVPAR LD C,1
317 ERARE?
318
319 TVPULQ LD A,1CR11
320 SUB 0
321 LD 0,A
322 LD A,(0D7LH)
323 CP 0
324 JP NC,UPDPDSH
32E
326 LD A,(1CR17)
327 DEC A
328 JR N1,TVPUL1
329 LD A,1
330 LD (1CR17),A
331 POP 0C
332 POP 0C
333 LD C,3
334 ERARE?
335 TVPUL1 LD (1CR17),A
336 LD 0C,(1CR17)
337
338 JP SCRL0
339
I NO NEED TO ADJUST(CHAR.START) AT BIT 7)
I EMPTY NASH "A" TIMES
I CD 0'S START AT BIT 1 OF 0, BIT 1 OF 0,
I OR BIT 0 OF 0)
I SCAN LINE FROM DP
I RETAIN ADJACENT CHAR,PIXELS
I CLEAR CURRENT CHAR,PIXELS
I NL=OLD BIT ROW
I GET STARTING BIT POSN.
I SAVE SCANS
I PTR. TO CHAR.SET
I SCAN LINE FROM CHAR.SET TO 0C
I (PIXELS IN 0. BITS 7-2)
I SAVE A AND PLGS
I PIXELS POP CHAR. SET
I LIMIT TO 0 PIXELS
I
I RESTORE A AND PLGS
I RESTORE SCANS
I NO IMPT NEEDED
I CLEAR CARRY
I EMPTY "A" TIMES
I SCAN FROM DP
I INSERT/COMBINE NEW CHAR.
I OF RDR CH.SET->HL
I TEST IF INVERSE ACTIVE
I NASH TO INVERT
I SHIFT NASH "A" TIMES
I INVERT CHARACTER
I DP ADDR. TO NL-SCAN LINE IN DE
I WRITE SCAN LINE TO CP
I ALTERNATE DP
I WRITE SCAN LINE TO CP
I CHAR.SET
I OVER/INVERSE INDICATORS
I RESTORE SCAN COUNT
I NEXT SCAN IN DP
I NEXT BYTE IN CHAR.PILE
I TEST IF MORE SCANS
I TMP SCAN IN DP
I CURSOR POSITION
I ADJUST CURSOR POSITION
I ADJUST BIT POSITION
I STORE UPDATED POSITION
I NEXT CHAS.IN SAME BYTE
I NEXT CHAS.IN NEXT BYTE
I STORE ADJUSTED VALUE
I ADJUST DP ADDR TO NEXT BYTE
I TOGGLE TO ALTERNATE DP
I TEST WHICH DP
I DONE IF DP2
I NEXT BYTE IN DP1
I STORE NEW POSITION
I RETURN SC=0
I ENTER HERE WITH C NON-ZERO
I RETURN SC=1 FOR INVALID PARAMETERS
I TEST IF NEW LINE IS OFF SCREEN
I A-LINE NO.
I SAVE IN D
I GET BOTTOM LINE
I ON SCREEN - RETURN TO WRITE CHAR.
I VIA UPDATE AND STORE POSITION
I TEST 1CR11 COUNT
I DO AUTOMATIC SCROLL USING SCRL
I REINITIALISE TO 1
I DISCARD RETURN TO WRITE CHAR.
I RETURN SC=3 FOR SCREEN FULL
I UPDATE VARIABLE
I GET SCROLL CONTROL (STARTING LINE
I AND LINE COUNT)
I RETURN TO WRITE CHAR. VIA SCROLL

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		341	SUB77L NR170 STRING	
		342	I	I NR08 FROM BASIC ENTRY TO
		343	I	I NR170 CHARACTER STRING TO SCREEN.
		344	I	I STRING IDENTIFIER (CH.CCDB) IN
		345	I	I SYSTEM VARIABLE PARAMETER AT SCC3H
0171	3A 3CC3	346	NRSTR0 LD A,(PAR0N0)	I MASK OFF UPPER BITS
0174	EA 1P	347	AND 1FH	I STRING VARIABLE IDENTIFIER
0179	P9 40	348	OR 40H	I SAVE IN D
0178	ST	349	LD D,A	I PING STRING
0179	2A 3C40	350	LD HL,(VARS)	I
017C	70	351	NRSTR1 LD A,(L)	I
017D	09 7P	352	AND 07FH	I
017F	20 33	353	JR 1,NC37RG	I
0181	09	354	CP 0	I
0182	20 00	355	JB 1,NRSTR2	I
0184	05	356	PUSH 00	I
0185	C0 172D	357	CALL 00CL0H	I
0188	00	358	OR 00,HL	I
0189	D1	359	POP 00	I
018A	10 P0	360	JR NRSTR1	I
018C	23	361	NRSTR2 INC HL	I
018D	40	362	LD C,(HL)	I
018E	23	363	INC HL	I
018F	49	364	LD 0,(HL)	I
0190	23	365	INC HL	I
0191	70	366	LD A,0	I
0192	01	367	OR C	I
0193	C0	368	RET 2	I
		369	I	I
		370	I	I
		371	I	I
		372	I	I
		373	I	I
0194	70	374	NRSTRG LD A,(HL)	I
0195	05	375	PUSH HL	I
0196	C9	376	PUSH 9C	I
0197	C0 0D4I	377	CALL NRCHAR	I
019A	79	378	LD A,C	I
019B	00	379	OR 9	I
019C	20 09	380	JB NI,NRSTR1	I
		381	I	I
0198	C1	382	NRSTR3 POP 0C	I
019P	01	383	POP HL	I
01A0	23	384	INC HL	I
01A1	00	385	DEC 0C	I
01A2	70	386	LD A,0	I
01A3	01	387	OR C	I
01A4	20 0E	388	JR NI,NRSTRG	I
01A9	C9	389	RET	I
		390	I	I
01A7	79	391	NRSTR1 LD R,C	I
01A8	P0 01	392	CP 1	I
01AA	20 P2	393	JB 2,NRSTR3	I
01AC	01	394	POP HL	I
01AC	22 0033	395	LD (STRGCT),HL	I
019D	01	396	POP HL	I
0191	00 03	397	LD C,3	I
0103	09 00	398	NRSTR2 LD 0,0	I
0105	C9	399	RET	I
		400	I	I
0100	00 02	401	NRSTR0 LD C,2	I
0100	10 P9	402	JR NRSTR2	I
		403	I	I
		404	I	I
		405	I	I
		406	I	I
		407	SUB77L POSITION CONTROL	I
		408	I	I
010A		409	S07C001	I
		410	I	I
010A	0D 40 0D07	411	LD 0C,(LINC0L)	I
		412	I	I
0100		413	S07C001	I
0100	C0 0400	414	CALL TSTPAB	I
01C1	CC 04PC	415	CALL CCMVPH	I
01C4	C0 0307	416	CALL UPCPDIN	I
01C7	C3 0142	417	JP G00DR0T	I
		418	I	I
		419	I	I
		420	I	I
		421	SUB77L SCADLL	I
		422	I	I
01CA		423	SCRLOG1	I
		424	I	I
01CA	0C 40 0020	425	LD 0C,(SCRECTL)	I
		426	I	I
01C0		427	SCRCLL1	I
		428	I	I
		429	I	I
01C0	70	430	LD 9,9	I
01CP	47	431	AND 9	I
01D0	CA 0147	432	JP 2,INVPAR	I
01D3	01	433	ADD A,C	I
0104	P0 01	434	CP 1	I
0109	DA 0147	435	JP C,INVPAB	I
01D9	P0 19	436	CP 25	I
0100	D2 0147	437	JP NC,INVPAR	I
0100	LD 0:04	438	CALL SCRLO	I
0101	C3 0142	439	JP G00DR0T	I
		440	I	I
0104	C9	441	SCBL0 PUSH 0C	I
0105	3E 10	442	LD A,SCRSI	I
0107	91	443	SUB C	I
01EE	47	444	LD 0,A	I
0103	3A D011	445	LD A,(LINL0H)	I
010C	3C	446	INC A	I
010D	4P	447	LD C,A	I
0100	CC 053P	448	CALL LNB0	I
01P1	C1	449	POP 0C	I
01P2	C3	450	PUSH 0C	I
01P3	70	451	LD A,L	I
01P4	47	452	9H0 A	I
01P5	20 51	453	JB 2,ST0LR	I
01P7	07	454	RICA	I
01P8	07	455	RLE9	I
01P9	07	456	RLEA	I
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		688		

01PR*	4P	437	LD	C,A	I GET NO. OP LINES THIS BLOCK
01PE*	3E 00	438	LD	R,E	
01PD*	3I	439	BUE	C	
01PI*	RR	440	CP	E	I TEST AGAINST TOTAL LINES
01PP*	50 5P	441	JA	C,GUELA	I TOTAL GREATER THAN THIS BLOCK
0201*	TR	442	INSOBLK	LD	R,R
0202*	EE EO	443	LD	R,E	
0204*	C5	444	PUSH	EC	I REMAINING COUNT
0205*	4T	445	LCOP	LD	E,A
0206*	03 DS	446	LD	C,B	I E=LINES THIS BLOCK
0208*	TR	447	LCOPD	LD	A,B
0209*	C5	448	PUSH	EC	I C=SCRN COUNT
020A*	OP	449	RACA		
020E*	OP	450	RACA		
020C*	OP	451	RRCR		I SAVE SCRN COUNT
020D*	4P	452	LD	C,R	I CALCULATE NO. OP BYTES
020E*	84 EO	453	LD	R,E	
0210*	EE	454	LOOPS	EX	DS,HL
0215*	21 PPSD	455	LD	HL,-2EH	I GET ADRS. OP PREV. LINE
0214*	19	456	RDO	HL,DE	
0215*	ER	457	EX	DS,HL	I TO DS
0216*	C5	458	PUSH	EC	I SAVE COUNT
0217*	85	459	PUSH	HL	I SAVE ADDRESS
021R*	EC EO	460	LDIA		I DO MOVE
021A*	E1	461	POP	HL	
021R*	C1	462	POP	EC	
021C*	7C	463	LD	A,M	
021D*	CR 6P	464	BIT	R,R	I TEST IF OP2
021P*	20 DS	465	JR	NZ,NXTSC	I NO
0221*	P4 20	466	DR	2RM	
0225*	4T	467	LD	M,R	I DO OP2
0224*	1R EA	468	JR	LOCP1	
0226*	84 DP	469	NATSC	RND	SDPH
022E*	4T	470	LD	M,A	I ERCA TO DP1
0229*	24	471	INC	M	
022R*	C1	472	POP	EC	I NEXT SCAN LINS
0220*	EO	473	DEC	C	I SCRN COUNT
022C*	20 DA	474	JA	NZ,LODPD	
022E*	C1	475	POP	EC	I MORE SCANS
022P*	7E	476	LD	6,B	I RESOURCE LINE COUNT
023E*	RT	477	RND	R	
0231*	2E 04	478	JA	Z,SCRLAT	I DONE
0235*	25 00	479	LD	L,D	I START OP NEXT BLOCK
0235*	1E EC	480	JR	TSSTAD	I DO REMAINING LINE(S)
0237*	C1	481	SCRLAT	POP	EC
0238*	79	482	LD	A,C	I STARTING LINE
0239*	R0	483	600	R,E	I RDO NO. OP LINES MOVED
023A*	3D	484	DEC	A	I LAST LINS SCADLLO
023R*	4P	485	LD	C,A	
023C*	84 01	486	LD	E,1	I CLEAR 1 LINS
023E*	1E E2	487	JR	CLRECE	I CLSRA VCRCTD LINS RND RTUAN
0240*	4P	488	ER		
0241*	7E	489	ORERL	LD	C,A
0242*	3I	490	LD	6,B	
0243*	4T	491	BUE	C	I ADJUST TOTAL LINE COUNT RT NO.
0244*	C5	492	LD	R,A	I OP LINS THIS BLOCK
0245*	79	493	PUSH	EC	
024E*	1E EO	494	LD	A,C	I LOAD R NO. OP LINS THIS BLOCK
0248*	1E EO	495	JR	LODP	
0249*	05	496	I		
024A*	C5	497	STOLH	OSC	B
024B*	08 00	498	LD	PUSH	EC
024C*	C1	499	LD	C,R	
024D*	50	500	STOLAC	PUSH	EC
0245*	21 PPSD	501	STOLAI	EX	01,HL
0211*	19	502	LD	HL,-720H	
0292*	8R	503	RDO	HL,CS	
0293*	01 0020	504	EA	DE,HL	I ADRS. OP PREV. LINE
0294*	85	505	LD	RC,92	I BYTE COUNT
0295*	ED 80	506	PUSH	HL	
0299*	3I	507	LDIA		I DO MOVE
020R*	7C	508	POP	HL	
029R*	CR 6P	509	LD	A,M	
029D*	20 DS	510	BIT	5,A	
029P*	P4 20	511	JR	NZ,STOLAZ	
0261*	4T	512	DR	2RM	I DO OP2
0262*	1R E9	513	LD	M,A	
0264*	84 DP	514	JA	STOLAI	
0266*	4T	515	AND	SDPH	I ERCA TO DP1
0267*	24	516	LD	M,A	
026E*	C1	517	INC	M	I TO NEXT SCAN LINS
0269*	00	518	POP	EC	
026A*	20 EO	519	DEC	C	I TEST IF MORE SCANS
026C*	C1	520	JE	NZ,STELAO	
026D*	70	521	POP	EC	I REMAINING LINE COUNT
026E*	AT	522	LD	6,B	
026P*	2E C4	523	RND	R	
0271*	11 P020	524	JR	1,SCALXT	
0274*	19	525	LD	DE,-750H	I ADJUST TO LINE 1
027E*	C3 01P3*	526	ADD	HL,DS	
		527	JP	TESTRD	I SC=LINS COUNT
		528			I HL=OP ADDRESS
		529	I		
		530	BUECTL	CLRR SCREEN	
		531	I		
		532	I		
027E*		533	I		
027E*	ED 4E 000C*	534	CLRS9E1	LD	EC,(CLRCTL)
		535			I HERE FROM BASIC ENTRY TO CLRR SCREEN
027C*		536	I		I GET CONTROL INPD.
		537	CLRS9E1		
		538	I		
027C*	7E	539			I ENTRY WITH SC=LINS COUNT AND
027D*	RT	540	LD	R,E	I STATING LINE NO. PDA CLSRA
027E*	CR 014T*	541	AND	4	
02R1*	E1	542	JP	2,INVPAR	I TEST VALIDITY
02R2*	PE 01	543	RDO	R,C	I ERROR IF COUNT=0
02R4*	04 014T*	544	CP	1	
02R7*	P3 19	545	JP	C,INVPAR	I ERROR IF E=C<5
02R9*	02 014T*	546	CP	25	I ERROR IF R=C>20
		547	JP	NC,INVPAR	

028C" C0 0292"	547	CALL	CLRSO	I DO SERVICE
028P" C3 0142"	548	JP	GOORBT	I RETURN SC=0
	549			
	550			
0292" C5	551	CLRSO	PUSH 6C	
0295" 50 10	552	LD	A,3CH32	I CONVERT TO INTERNAL FORMAT
0295" 01	553	SUB	C	
0296" 47	554	LD	B,A	
0297" 5A 0035"	555	LD	A,(CLSHLN)	
029A" 3C	556	INC	A	I 307 TO COL-0
029B" 4P	557	LD	C,A	
029C" C0 053P"	558	CALL	LM6U	I 007 ADDRESS
029P" 50	559	LD	D,B	
02A0" 59	560	LD	B,C	I 3A90 POSITION
02A1" C1	561	POP	BC	I 041G, BC
02A2" 05	562	PUSH	08	
02A3" 70	563	CLRSO	LD A,L	I GET 0 OP LINES THIS BLOCK
02A4" 07	564	RLCA		
02A5" 07	565	RLCA		
02A6" 07	566	RLCA		
02A7" 4P	567	LD	C,A	
02A8" 30 00	568	LD	A,B	
02AA" 91	569	SUB	C	
02AB" 40	570	CP	B	I COMPARE TO TOTAL LINE COUNT
02AC" 30 30	571	JR	C,CLRSCT	
02AE" 70	572	CLRSO	LD A,B	I NO. OF LINES
02AP" 06 00	573	LD	B,0	I REMAINING LINES
02B1" C5	574	PUSH	6C	
02B2" 47	575	CLRSO	LD B,A	
02B3" 00 0E	576	LD	C,6	I SCAN COUNT
02B5" 70	577	CLRSO	LD A,B	
02B6" C5	578	PUSH	6C	
02B7" E6 07	579	AND	7	
02B9" 0P	580	RRCA		
02BA" 6P	581	RRCA		
02BB" 6P	582	RRCA		
02BC" 4P	583	LD	C,A	I BC=52=NO. OF LINES
02BD" 0A 00	584	LD	B,0	I C=0 IF 256 FOR 0 LINES
02BP" 00	585	DEC	C	I ADJUST
02C0" C5	586	CLRSO	PUSH 6C	
02C1" 05	587	PUSH	HL	
02C2" 54	588	LD	D,H	
02C3" 50	589	LD	E,L	
02C4" 56 00	590	LD	(HL),00	I CLEAR OP
02CA" 15	591	INC	DB	
02C7" E0 E0	592	LDIR		
02C9" E1	593	POP	HL	
02CA" C1	594	POP	6C	
02CB" 7C	595	LD	A,H	
02CC" C6 4P	596	BIT	5,A	
02CB" 20 05	597	JR	M2,CLRSO6	
02D0" PE 20	598	DE	20H	
02D2" 67	599	LD	M,A	I DO OP2
02D5" 10 00	600	JE	CLRSO5	I 003005 DPI
02D5" E6 0P	601	AND	00FH	
02D7" 07	602	LD	M,A	
02D8" 24	603	INC	H	I NEXT SCAN ROW
02D9" C1	604	POP	6C	I POP SCAN COUNT
02DA" 00	605	DEC	C	
02DB" 20 00	606	JR	M2,CLRSO3A	
02DD" C1	607	POP	6C	I TOTAL LINE COUNT
02DE" 7E	608	LD	A,6	
02DF" 47	609	AND	A	
02E0" 2E 05	610	JR	I,CLASXT	
02E2" 2E 00	611	LD	L,C	I HL=START OF NEXT BLOCK
02E4" C5 02A3"	612	JP	CLRSO1	I 4=REMAINING LINE COUNT
02E7" C1	613	CLASXT	POP BC	I POSITION
02E8" C5 0507"	614	JP	UPOPSM	I RETURN VIA UPDATES AND STORE POSITION
02E8" 4P	615	CLRSO	LD C,A	I 3A90 NO. LINES THIS BLOCK
02EC" 70	616	LD	A,6	
02ED" 91	617	SUB	C	I ADJUST TOTAL LINE COUNT
02EE" 47	618	LD	B,A	
02EP" C5	619	PUSH	BC	I REMAINING LINE COUNT
02F0" 79	620	LD	A,C	I LINES THIS BLOCK
02F1" 30 0P	621	JP	CLRSO3	
	622			
	623			
	624			
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02P3" 5A 0011"	648	SBTASO1	LD A,(ATTCTCL)	I HERE FROM BASIC ENTRY TO SET ATTRIBUTES
02P5" 5A 0011"	649			I GET CONTACT INFO.
	650			
02P6" E6 07	651	SBTATT1	AND T	I ENTRY WITH ATTRIBUTES 6YTB IN A
02P8" 4P	652	LD	C,A	I GET INA SELECTION
02P9" 5A 00C2	653	LD	A,(V10MOD)	I 3A90
02PC" 47	654	AND	A	I TEST IF OP2 OPBM
02PD" CA 0147"	655	JP	2,INVPA8	I INVALID IF OP2 NOT OPBM
0300" 79	656	LD	A,C	
0301" 17	657	OLA		
0302" 17	658	RLA		
0303" 17	659	RLA		
0304" P0	660	PUSH	AP	I SHIFT TO M/W POSITION
0305" H6 00	661	OR	0	I SAVE ROTATED VALUE
0307" 47	662	LD	B,A	I SET M/W MODE
0308" 08 PP	663	2H	A,(HRSXPT)	I VALUE PDA M/W
030A" 06 C0	664	AND	0COM	
030C" 60	665	DA	H	I PRESERVE BITS T AND A
030D" 03 PP	666	DUT	(HRSXPT),A	I SET VIDEO MODE VALUE
030F" P6 40	667	DR	40H	I SET M/W
0311" 32 SCC2	668	LD	(V10MOD),A	I UPDATES V10MOD
0314" P1	669	POP	AP	
0315" 2P	670	CPL		I INA SEL IN BITS 5-5
0316" 01	671	DE	C	I GET COMPLEMENTARY CCLDA
0317" 52 0039"	672	LD	(ATTATT),A	I COMBINE WITH INA
031A" C3 0142"	673	JP	GOORBT	I SAVE IN VARIABLE
	674			I RETURN 6C=0
	675			
	676			
	677			
0310" 5A 0013"	678	SBTHSO1	LD A,(HSACTCL)	I HERE FROM BASIC ENTRY TO SET HASA
0310" 5A 0013"	679			I GET CONTACT INFO.
	680			
0320" 32 005E"	681	SBTHSA1	LD (HASA),A	I ENTRY WITH HASA VALUE IN A
0323" C3 0142"	682	JP	GOORBT	I STORE HASA FOR APPLICATION TO FUTURE
	683			I DISPLAYS

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08C9* P1
03C7* 32 0050*
08CA* 40
03C0* 9P
03CC* C9

03CD*
03C0* 3A 0050*
0300* 4P
0301* 09 00
0303* C9

0304*
0304*
0304*
0304* 00 40 0050*
0300* CC 04PC*
0300* 0A 0051*
0300* 09
0300* 20 00
0301* 00 00
0303* 70
0304* SC
0300* 47
0304* PE 10
0300* 50 02
0300* 04 17
0300* 06 45 0007*
0300* C9

03P5*
03P5* 39 0024*
03P4*
03P4* A7
03P0* 20 4P
03P7* P0 00
03P9* C2 0147*

03PC* 21 1701
03PP* 22 0020*
0402* SE 17
0404* 32 0020*
0407* SE 01
0409* 32 0020*
040C* SE 00
0400* 32 5051*
0411* SE 01
0413* 32 0050*
0419* 21 1000
0419* 22 000C*
041C* AP
0410* 32 0050*
0420* 32 0050*
0423* 32 005C*
0424* 21 0475*
0429* 22 002P*
042C* 21 0770*
042P* 22 3051*

0432* 32 0000*
0430* 32 0007*
0430* 32 0050*
0430* 32 0019*
0430* 32 0019*
0441* 32 0019*
0444* 30 04
0440* 47
0447* 3A 0CC2
044A* A7
0440* 20 10
0440* 00
0440* CA 0142*
0451* 21 12C0

0464* 11 0040
0407* 10
0400* EC 50 0C05
040C* 19
0400* 04 0447*
0400* 00 50 1C02
0494* A7
0490* 00 32
0497* 02 0447*
044A* C0 0402*
0490* 70
0460* C0 044C*
0471* C)
0472* 70
0-75* CC 0000
0479* CC 040P*
0479* C1
047A* 70
0470* 32 5CC2
0470* C0 77
0400* CA 0142*
0405* SE C1
0409* 00
0409* 2P

709 GTCNB POP AP ; DELT000 ORIG. DP017
790 LD (CP017),A
791 LD C,0 ; 0C=0
792 POP 9 ; A=0
793 PET
794
795 ; GET ATTRIBUTE BYTE
796 ; NOTE THAT IN ED-CCL,MODE ALL SCREEN POSITIONS
797 ; HAVE THE SAME ATTRIBUTE, THEREFORE IT IS
798 ; NOT NECESSARY TO USE THE "GETCTL" POSITION
799 ; PARAMETER
800
801 ;
802 GETA001 ; H003 FROM BASIC ONTOV TO GET ATTRIBUTE
803
804 ;
805 GET9771 LD A,(ATT0V7) ; GET ATTRIBUTE BYTE
806 LD C,A ; PUT IN 0C
807 LD 0,0
808 PET
809
810 ;
811 GETC001 ; GET CURSOR POSITION
812 GETCUA1 ; H003 FROM BASIC ONTOV
813
814 LD 0C,(CUPP05) ; GET INTERNAL POSITION
815 C0LL COMVPM ; CONVERT TO USER PARAM
816 LD A,(CLINLEN) ; TEST IF END OF LINE
817 CP C ; IF =00 OF SE
818 JP M2,GETC1 ; NO
819 LD C,0 ; NEXT POSN, START OF NEXT LINE
820 LD A,0
821 INC A ; BUMP TO NEXT LINE
822 LD 0,A
823 CP 24 ; TEST IF OFF SCREEN
824 JB C,GETC1 ; NO
825 LD 0,25 ; POSN. IS AT BOTTOM LINE
826 LD (LINCOL),EC ; STORE FOR BASIC PROGRAM
827 PET ; VALUES ALSO IN 0C
828
829 ;
830 SUB77L V2050 H003 CONTPOL
831
832 ;
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0487* C8 3E
0488* C8 58
0489* C8 78
0490* 80
0491* 32 0039*
0492* 52 0011*
0493* 5A 0033*
0494* 3C
0495* 4P
0496* 04 IE
0497* 21 8080
0498* 11 5C7D
0499* C8 8527*
04A0* C3 0142*
04A1* 0E 02
04A2* C3 0144*

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04AC* 4P
04AD* C8 47
04AE* 20 1A
04AF* FE 80
04B0* 28 1A
04B1* C8 7P
04B2* C0
04B3* 47
04B4* PB 01
04B5* CE
04B6* 84 C6
04B7* EE 0A
04B8* C0
04B9* 3E 4D
04BA* E1
04BB* 4P
04BC* AP
04BD* C9
04BE* PE 01
04BF* C1
04C0* 0E 01
04C1* 0E 01
04C2* C9
04C3* AP
04C4* 47
04C5* C9

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04G2* P5
04G3* 08 PP
04G4* P4 80
04G5* 05 PP
04G6* 3E 03
04G7* 03 P4
04G8* P1
04G9* C9

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04H0* P1
04H1* AP
04H2* 03 P4
04H3* 08 PP
04H4* 5A 3P
04H5* 03 PP
04H6* P1
04H7* C9

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04E8* 3E 17
04E9* 88
04EA* 3D 84
04EB* P1
04EC* C1 0147*
04ED* 3A 0033*
04EE* 3D
04EF* 89
04F0* 38 P1
04F1* C9

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04PC* 3A 0033*

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902      SRL      8
903      SRL      8
904      SRL      8
905      OR        R
906      LD        (ATTBYT),A      I ATTRIBUTE BYTE
907      LD        (ATTCTL),A      I BASIC PARAMETER
908      LD        A,(CLINLEN)
909      INC        A
910      LD        C,A
911      LD        0,I8H            I BC=HOME POSITION CLN-8/COL-8
912      LD        HL,0
913      LD        C,CORRDS),HL     I PLOT POSITION
914      CALL       UPOROSH         I UPOROSH AND STORE HOME POSITION
915      JP        GOCORET         I RETURN BC=0
916      LD        C,2             I RETURN BC=1 FOR NO ROOM
917      JP        EORSET
918      1
919      SUBTTL     INTERNAL SUB-ROUTS.
920      1
921      1
922      1
923      1
924      I INPUT: VIDEO MODE IN R
925      I OUTPUT: M/W SETTING IN A
926      I VIDEO MODE SETTING IN C
927      I RETURNS M1 STATUS IF MODE INVALID
928      1
929      I VALID VALUES: INPUT M/W VIDEO
930      I -----
931      I **DISPLAY FILE ACTIVE AT SCREEN
932      I
933      I DPL ONLY      0      0      0
934      I DPL0 DPL1     00      0      00
935      I DPL2 DPL3     1      1      01
936      I HIGH RES.CO. 1      2      2
937      I 84/80-COLUMN 08 - 3E 06 - 1E 48 - 70
938
939      GETVAL     LD        C,A
940      817      0,A
941      JB        M2,TST01
942      CP        80H
943      JR        2,5870
944      817      7,A
945      807      M1
946      LD        0,A
947      CP        1
948      807      1
949      RND       0C6H
950      X0B       4
951      8E7      M2
952      LD        4,40H
953      08        C
954      LD        C,A
955      ROR       4
956      807      1
957      TST01     CP        1
958      8E7      M1
959      LD        0,I
960      LD        C,81H
961      8E7      1
962      1E7D      X0R      A
963      LD        0,R
964      RET
965      1
966      1
967      I ROUTINES TO SHARE CHUNK 0 IN ROM EXT.
968      I FOR ACCESS TO CHNGVID ROUTINE
969
970      ENBLEXT    PUSH     AP
971      IN         4,(CHREXT)
972      OR        80H
973      OUT        (CHREXT),A      I SELECT ROM EXT.
974      LD        A,3
975      OUT        (CHMSPT),R
976      POP       RP
977      RET
978
979      I ENBLNCHG  PUSH     AP
980      X0R       A
981      OUT        (CHMSPT),R
982      IN         A,(CHREXT)
983      RND       83FH
984      OUT        (CHREXT),R
985      POP       AP
986      8E7      1
987      1
988      I TEST PARAMETER VALIDITY
989      I IF NOT VALID, DISCARD RETURN
990      I AND ERITS VIR INVPAR
991      I TEST LINE >15
992
993      T1TPRR     LD        4,33
994      CP        8
995      JR        NC,T1TPRI
996      PARERR     POP       AP
997      JP        INVPAR
998      T1TPRI     LD        A,(CLINLEN)
999      ORC       A
1000      CP        C
1001      JB        C,PABERR        I TEST CCL. >LINE LENGTH-1
1002      8E7      1
1003      1
1004      I CONVERT LINE/COL. FROM USER TO
1005      I INTERNAL FORMAT AND VICE VERSA
1006      I USER FORMAT: LINES 8-23
1007      I CCL. 0-T9(84)
1008      I INTERNAL PCFORMAT:
1009      I LINES 24-1
1010      I COL. C06)81-2
1011      I CCL.1 AND OP LINE)
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1121
1122
1123 1 Set patterns for characters on the TV screen:
1124 1 character with code = 10 at offsets 6x (see line) to 8x7 (see line)
1125 1 0 = white, 1 = black, no margin between character codes either
1126 1 vertically, or horizontally
1127
0370 00 1126 CHR57 defb 00000000b 1 code 20 0000
0371 00 1129 defb 00000000b
0380 00 1130 defb 00000000b
0381 00 1131 defb 00000000b
0382 00 1132 defb 00000000b
0383 00 1133 defb 00000000b
0384 00 1134 defb 00000000b
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1136
0386 00 1137 defb 00000000b 1 code 21 1
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0392 10 1143 defb 00010000b
0393 10 1144 defb 00010000b
0394 10 1145
0395 00 1146 defb 00000000b 1 code 22 0
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0397 00 1148 defb 01001000b
0398 00 1149 defb 00000000b
0399 00 1150 defb 00000000b
0400 00 1151 defb 00000000b
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0402 00 1153 defb 00000000b
1154
0403 00 1155 defb 00000000b 1 code 23 0
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0406 00 1158 defb 00000000b
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0408 00 1160 defb 00000000b
0409 00 1161 defb 00000000b
0410 00 1162 defb 00000000b
1163
0411 00 1164 defb 00000000b 1 code 24 0
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0413 00 1166 defb 00000000b
0414 00 1167 defb 00000000b
0415 00 1168 defb 00000000b
0416 00 1169 defb 00000000b
0417 00 1170 defb 00000000b
0418 00 1171 defb 00000000b
1172
0419 00 1173 defb 00000000b 1 code 25 0
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0421 00 1175 defb 00000000b
0422 00 1176 defb 00000000b
0423 00 1177 defb 00000000b
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0426 00 1180 defb 00000000b
1181
0427 00 1182 defb 00000000b 1 code 26 0
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0432 00 1187 defb 00000000b
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1191
0436 00 1192 defb 00000000b 1 code 27 0
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0439 00 1195 defb 00000000b
0440 00 1196 defb 00000000b
0441 00 1197 defb 00000000b
0442 00 1198 defb 00000000b
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1200
0444 00 1200 defb 00000000b 1 code 28 0
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0446 00 1202 defb 00000000b
0447 00 1203 defb 00000000b
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0455 00 1212 defb 00000000b
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0457 00 1214 defb 00000000b
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0477 00 1235 defb 00000000b
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0481 00 1239 defb 00000000b
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0486 00 1244 defb 00000000b
1245
0487 00 1245 defb 00000000b 1 code 33 0
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0501 00 1259 defb 00000000b
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0503 00 1261 defb 00000000b
0504 00 1262 defb 00000000b
0505 00 1263 defb 00000000b
1264
0506 00 1264 defb 00000000b 1 code 35 0
0507 00 1265 defb 00000000b
0508 00 1266 defb 00000000b
0509 00 1267 defb 00000000b
0510 00 1268 defb 00000000b
0511 00 1269 defb 00000000b
0512 00 1270 defb 00000000b
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1273
0515 00 1273 defb 00000000b 1 code 36 0
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0517 00 1275 defb 00000000b
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0519 00 1277 defb 00000000b
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0522 00 1280 defb 00000000b
0523 00 1281 defb 00000000b
1282
0524 00 1282 defb 00000000b 1 code 37 0
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0526 00 1284 defb 00000000b
0527 00 1285 defb 00000000b
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0529 00 1287 defb 00000000b
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0531 00 1289 defb 00000000b
0532 00 1290 defb 00000000b
1291
0533 00 1291 defb 00000000b 1 code 38 0
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0545 00 1303 defb 00000000b
0546 00 1304 defb 00000000b
0547 00 1305 defb 00000000b
0548 00 1306 defb 00000000b
0549 00 1307 defb 00000000b
0550 00 1308 defb 00000000b
1309
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0554 00 1312 defb 00000000b
0555 00 1313 defb 00000000b
0556 00 1314 defb 00000000b
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0558 00 1316 defb 00000000b
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1318
0560 00 1318 defb 00000000b 1 code 41 0
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0562 00 1320 defb 00000000b
0563 00 1321 defb 00000000b
0564 00 1322 defb 00000000b
0565 00 1323 defb 00000000b
0566 00 1324 defb 00000000b
0567 00 1325 defb 00000000b
0568 00 1326 defb 00000000b
1327
0569 00 1327 defb 00000000b 1 code 42 0
0570 00 1328 defb 00000000b
0571 00 1329 defb 00000000b
0572 00 1330 defb 00000000b
0573 00 1331 defb 00000000b
0574 00 1332 defb 00000000b
0575 00 1333 defb 00000000b
0576 00 1334 defb 00000000b
0577 00 1335 defb 00000000b
1336
0578 00 1336 defb 00000000b 1 code 43 0
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0581 00 1339 defb 00000000b
0582 00 1340 defb 00000000b
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0584 00 1342 defb 00000000b
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0586 00 1344 defb 00000000b
1345
0587 00 1345 defb 00000000b 1 code 44 0
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0594 00 1352 defb 00000000b
0595 00 1353 defb 00000000b
1354
0596 00 1354 defb 00000000b 1 code 45 0
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0601 00 1359 defb 00000000b
0602 00 1360 defb 00000000b
0603 00 1361 defb 00000000b
0604 00 1362 defb 00000000b
1363
0605 00 1363 defb 00000000b 1 code 46 0
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0607 00 1365 defb 00000000b
0608 00 1366 defb 00000000b
0609 00 1367 defb 00000000b
0610 00 1368 defb 00000000b
0611 00 1369 defb 00000000b
0612 00 1370 defb 00000000b
0613 00 1371 defb 00000000b
1372
0614 00 1372 defb 00000000b 1 code 47 0
0615 00 1373 defb 00000000b
0616 00 1374 defb 00000000b
0617 00 1375 defb 00000000b
0618 00 1376 defb 00000000b
0619 00 1377 defb 00000000b
0620 00 1378 defb 00000000b
0621 00 1379 defb 00000000b
0622 00 1380 defb 00000000b
1381
0623 00 1381 defb 00000000b 1 code 48 0
0624 00 1382 defb 00000000b
0625 00 1383 defb 00000000b
0626 00 1384 defb 00000000b
0627 00 1385 defb 00000000b
0628 00 1386 defb 00000000b
0629 00 1387 defb 00000000b
0630 00 1388 defb 00000000b
0631 00 1389 defb 00000000b
1390
0632 00 1390 defb 00000000b 1 code 49 0
0633 00 1391 defb 00000000b
0634 00 1392 defb 00000000b
0635 00 1393 defb 00000000b
0636 00 1394 defb 00000000b
0637 00 1395 defb 00000000b
0638 00 1396 defb 00000000b
0639 00 1397 defb 00000000b
0640 00 1398 defb 00000000b
1399
0641 00 1399 defb 00000000b 1 code 50 0
0642 00 1400 defb 00000000b
0643 00 1401 defb 00000000b
0644 00 1402 defb 00000000b
0645 00 1403 defb 00000000b
0646 00 1404 defb 00000000b
0647 00 1405 defb 00000000b
0648 00 1406 defb 00000000b
0649 00 1407 defb 00000000b
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0650 00 1408 defb 00000000b 1 code 51 0
0651 00 1409 defb 00000000b
0652 00 1410 defb 00000000b
0653 00 1411 defb 00000000b
0654 00 1412 defb 00000000b
0655 00 1413 defb 00000000b
0656 00 1414 defb 00000000b
0657 00 1415 defb 00000000b
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1417
0659 00 1417 defb 00000000b 1 code 52 0
0660 00 1418 defb 00000000b
0661 00 1419 defb 00000000b
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0664 00 1422 defb 00000000b
0665 00 1423 defb 00000000b
0666 00 1424 defb 00000000b
0667 00 1425 defb 00000000b
1426
0668 00 1426 defb 00000000b 1 code 53 0
0669 00 1427 defb 00000000b
0670 00 1428 defb 00000000b
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0674 00 1432 defb 00000000b
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0676 00 1434 defb 00000000b
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0677 00 1435 defb 00000000b 1 code 54 0
0678 00 1436 defb 00000000b
0679 00 1437 defb 00000000b
0680 00 1438 defb 00000000b
0681 00 1439 defb 00000000b
0682 00 1440 defb 00000000b
0683 00 1441 defb 00000000b
0684 00 1442 defb 00000000b
0685 00 1443 defb 00000000b
1444
0686 00 1444 defb 00000000b 1 code 55 0
0687 00 1445 defb 00000000b
0688 00 1446 defb 00000000b
0689 00 1447 defb 00000000b
0690 00 1448 defb 00000000b
0691 00 1449 defb 00000000b
0692 00 1450 defb 00000000b
0693 00 1451 defb 00000000b
0694 00 1452 defb 00000000b
1453
0695 00 1453 defb 00000000b 1 code 56 0
0696 00 1454 defb 00000000b
0697 00 1455 defb 00000000b
0698 00 1456 defb 00000000b
0699 00 1457 defb 00000000b
0700 00 1458 defb 00000000b
0701 00 1459 defb 00000000b
0702 00 1460 defb 00000000b
0703 00 1461 defb 00000000b
1462
0704 00 1462 defb 00000000b 1 code 57 0
0705 00 1463 defb 00000000b
0706 00 1464 defb 00000000b
0707 00 1465 defb 00000000b
0708 00 1466 defb 00000000b
0709 00 1467 defb 00000000b
0710 00 1468 defb 00000000b
0711 00 1469 defb 00000000b
0712 00 1470 defb 00000000b
1471
0713 00 1471 defb 00000000b 1 code 58 0
0714 00 1472 defb 00000000b
0715 00 1473 defb 00000000b
0716 00 1474 defb 00000000b
0717 00 1475 defb 00000000b
0718 00 1476 defb 00000000b
0719 00 1477 defb 00000000b
0720 00 1478 defb 00000000b
0721 00 1479 defb 00000000b
1480
0722 00 1480 defb 00000000b 1 code 59 0
0723 00 1481 defb 00000000b
0724 00 1482 defb 00000000b
0725 00 1483 defb 00000000b
0726 00 1484 defb 00000000b
0727 00 1485 defb 00000000b
0728 00 1486 defb 00000000b
0729 00 1487 defb 00000000b
0730 00 1488 defb 00000000b
1489
0731 00 1489 defb 00000000b 1 code 60 0
0732 00 1490 defb 00000000b
0733 00 1491 defb 00000000b
0734 00 1492 defb 00000000b
0735 00 1493 defb 00000000b
0736 00 1494 defb 00000000b
0737 00 1495 defb 00000000b
0738 00 1496 defb 00000000b
0739 00 1497 defb 00000000b
1498
0740 00 1498 defb 00000000b 1 code 61 0
0741 00 1499 defb 00000000b
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0743 00 1501 defb 00000000b
0744 00 1502 defb 00000000b
0745 00 1503 defb 00000000b
0746 00 1504 defb 00000000b
0747 00 1505 defb 00000000b
0748 00 1506 defb 00000000b
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0749 00 1507 defb 00000000b 1 code 62 0
0750 00 1508 defb 00000000b
0751 00 1509 defb 00000000b
0752 00 1510 defb 00000000b
0753 00 1511 defb 00000000b
0754 00 1512 defb 00000000b
0755 00 1513 defb 00000000b
0756 00 1514 defb 00000000b
0757 00 1515 defb 00000000b
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0758 00 1516 defb 00000000b 1 code 63 0
0759 00 1517 defb 00000000b
0760 00 1518 defb 00000000b
0761 00 1519 defb 00000000b
0762 00 1520 defb 00000000b
0763 00 1521 defb 00000000b
0764 00 1522 defb 00000000b
0765 00 1523 defb 00000000b
0766 00 1524 defb 00000000b
1525
0767 00 1525 defb 00000000b 1 code 64 0
0768 00 1526 defb 00000000b
0769 00 1527 defb 00000000b
0770 00 1528 defb 00000000b
0771 00 1529 defb 00000000b
0772 00 1530 defb 00000000b
0773 00 1531 defb 00000000b
0774 00 1532 defb 00000000b
0775 00 1533 defb 00000000b
1534
0776 00 1534 defb 00000000b 1 code 65 0
0777 00 1535 defb 00000000b
0778 00 1536 defb 00000000b
0779 00 1537 defb 00000000b
0780 00 1538 defb 00000000b
0781 00 1539 defb 00000000b
0782 00 1540 defb 00000000b
0783 00 1541 defb 00000000b
0784 00 1542 defb 00000000b
1543
0785 00 1543 defb 00000000b 1 code 66 0
0786 00 1544 defb 00000000b
0787 00 1545 defb 00000000b
0788 00 1546 defb 00000000b
0789 00 1547 defb 00000000b
0790 00 1548 defb 00000000b
0791 00 1549 defb 00000000b
0792 00 1550 defb 00000000b
0793 00 1551 defb 00000000b
1552
0794 00 1552 defb 00000000b 1 code 67 0
0795 00 1553 defb 00000000b
0796 00 1554 defb 00000000b
0797 00 1555 defb 00000000b
0798 00 1556 defb 00000000b
0799 00 1557 defb 00000000b
0800 00 1558 defb 00000000b
0801 00 1559 defb 00000000b
0802 00 1560 defb 00000000b
1561
0803 00 1561 defb 00000000b 1 code 68 0
0804 00 1562 defb 00000000b
0805 00 1563 defb 00000000b
0806 00 1564 defb 00000000b
0807 00 1565 defb 00000000b
0808 00 1566 defb 00000000b
0809 00 1567 defb 00000000b
0810 00 1568 defb 00000000b
0811 00 1569 defb 00000000b
1570
0812 00 1570 defb 00000000b 1 code 69 0
0813 00 1571 defb 00000000b
0814 00 1572 defb 00000000b
0815 00 1573 defb 00000000b
0816 00 1574 defb 00000000b
0817 00 1575 defb 00000000b
0818 00 1576 defb 00000000b
0819 00 1577 defb 00000000b
0820 00 1578 defb 00000000b
1579
0821 00 1579 defb 00000000b 1 code 70 0
0822 00 1580 defb 00000000b
0823 00 1581 defb 00000000b
0824 00 1582 defb 00000000b
0825 00 1583 defb 00000000b
0826 00 1584 defb 00000000b
0827 00 1585 defb 00000000b
0828 00 1586 defb 00000000b
0829 00 1587 defb 00000000b
1588
0830 00 1588 defb 00000000b 1 code 71 0
0831 00 1589 defb 00000000b
0832 00 1590 defb 00000000b
0833 00 1591 defb 00000000b
0834 00 1592 defb 00000000b
0835 00 1593 defb 00000000b
0836 00 1594 defb 00000000b
0837 00 1595 defb 00000000b
0838 00 1596 defb 00000000b
1597
0839 00 1597 defb 00000000b 1 code 72 0
0840 00 1598 defb 00000000b
0841 00 15
```

0300	10	1222	defb 00010000b		
032C	10	1233	defb 00010300b		
0003	00	1234	defb 00002000b		
		1230			
0000	00	1230	defb 000C0000b	1 code 2C	.
0000	00	1237	defb 003C0000b		
0300	00	1230	defb 000C0000b		
0001	00	1230	defb 00002000b		
0002	00	1200	defb 000C0000b		
0303	10	1241	defb 00010000b		
0024	10	1202	defb 00010000b		
0003	20	1242	defb 00100000b		
		1200			
0320	00	1200	defb 00000000b	1 code 20	-
0007	00	1204	defb 004C0200b		
0000	00	1207	defb 00020000b		
0400	00	1240	defb 000C3000b		
0330	7C	1200	defb 01111100b		
0300	00	1200	defb 00000300b		
030C	00	1261	defb 00000000b		
0300	00	1262	defb 00030000b		
		1203			
0322	00	1230	defb 000C0000b	1 code 22	.
002F	00	1233	defb 00002000b		
03F0	00	1234	defb 00030000b		
00F1	00	1207	defb 00000000b		
00F2	00	1200	defb 000C0300b		
03F3	2C	1260	defb 00110000b		
03F0	33	1260	defb 00110300b		
03F3	00	1261	defb 000C0000b		
		1262			
03F0	00	1263	defb 000C0000b	1 code 2F	/
00F7	00	1260	defb 00602000b		
03F0	00	1203	defb 00C00100b		
00F0	00	1260	defb 000D1000b		
00F0	10	1267	defb 00013300b		
00F0	20	1260	defb 00100000b		
03FC	40	1200	defb 01002030b		
00F0	00	1270	defb 00002000b		
		1271			
00F0	00	1272	defb 00000000b	1 code 20	0
00F0	30	1273	defb 00111300b		
0000	4C	1176	defb 01001100b		
0001	04	1273	defb 01010100b		
0002	04	1276	defb 01010100b		
3001	04	1277	defb 01100100b		
0004	33	1276	defb 00111000b		
0405	00	1270	defb 00C00000b		
		1200			
0400	00	1201	defb 00000300b	1 code 31	1
0407	20	1202	defb 00110000b		
0000	30	1203	defb 01010000b		
0409	10	1200	defb 00010000b		
0400	10	1263	defb 00010000b		
0400	10	1200	defb 00010000b		
040C	7C	1207	defb 01111100b		
0400	00	1260	defb 00020000b		
		1200			
0400	00	1260	defb 00000000b	1 code 32	2
0000	30	1201	defb 00111000b		
0610	04	1292	defb 01000100b		
0611	00	1263	defb 00000100b		
0612	30	1264	defb 00111000b		
0613	40	1203	defb 01000000b		
0610	7C	1260	defb 01111100b		
0613	00	1207	defb 00000000b		
		1200			
0616	00	1260	defb 00000000b	1 code 33	3
0617	30	1300	defb 00111000b		
0610	44	1301	defb 01000100b		
0610	10	1302	defb 00010000b		
0610	00	1303	defb 00020100b		
0610	44	1304	defb 01000100b		
061C	30	1303	defb 00111000b		
0610	00	1304	defb 00020000b		
		1307			
061E	00	1300	defb 000C2000b	1 code 30	4
0610	03	1304	defb 00021000b		
0620	10	1310	defb 00011000b		
0621	20	1311	defb 001C1000b		
0622	40	1312	defb 01021000b		
0623	7C	1313	defb 01111100b		
0620	00	1314	defb 02001000b		
0623	00	1315	defb 00020000b		
		1316			
0620	00	1317	defb 00000000b	1 code 33	3
0627	7C	1310	defb 01111100b		
0620	00	1310	defb 01000000b		
0620	70	1320	defb 01111000b		
0620	04	1321	defb 00000100b		
0623	44	1322	defb 01020100b		
062C	30	1323	defb 00111000b		
0620	00	1324	defb 000C0000b		
		1323			
0020	00	1326	defb 00020000b	1 code 30	0
0620	30	1327	defb 00111000b		
0630	40	1320	defb 01020000b		
0631	70	1324	defb 01111000b		
0632	40	1330	defb 010C2100b		
0633	04	1331	defb 010C2100b		
0634	33	1332	defb 00111000b		
0633	00	1333	defb 00003000b		
		1334			
0630	00	1333	defb 00000000b	1 code 37	7

0b37' 7C	1336	0e7b 01111100b		
0b38' 04	1337	0e7e 00000100b		
0b39' 0b	133b	0e7b 00000100b		
0b3A' 10	1350	0e7b 00010000b		
0b3B' 20	1340	0e7e 00100000b		
0b3C' 30	1301	0e7e 00100000b		
0b3D' 00	1343	0e7b 00000000b		
	1343			
0b3E' 00	1344	0e7b 00000000b	1 code 30	0
0b3F' 50	1345	0e7b 00111000b		
0b40' 44	1340	0e7e 01000100b		
0b41' 33	1347	0e7e 00111000b		
0b43' 44	1340	0e7e 01000100b		
0b45' 44	1340	0e7b 01000100b		
0b46' 30	1300	0e7b 00111000b		
0b48' 00	1351	0e7b 00300000b		
	1353			
0b4b' 00	1303	0e7b 00000000b	1 code 39	9
0b47' 30	1304	0e7b 00111000b		
0b48' 44	1305	0e7b 01000100b		
0b48' 44	1350	0e7b 01000100b		
0b4b' 3C	1307	0e7b 00111000b		
0b48' 04	1350	0e7e 00000100b		
0b4C' 30	1300	0e7b 00111000b		
0b4D' 00	1300	0e7e 00000000b		
	1361			
0b48' 00	13b3	0e7b 00000000b	1 code 3b	1
0b49' 00	1343	0e7b 00000000b		
0b50' 00	1344	0e7b 00000000b		
0b01' 10	1300	0e7b 00000000b		
0b02' 00	1300	0e7b 00000000b		
0b03' 00	1347	0e7e 00000000b		
0b04' 10	1300	0e7b 00000000b		
0b05' 00	1360	0e7b 00000000b		
	1370			
0b50' 00	1371	0e7e 00000000b	1 code 30	1
0b57' 00	1373	0e7b 00000000b		
0b58' 1C	1373	0e7b 00000000b		
0b59' 03	1374	0e7b 00000000b		
0b5A' 00	1370	0e7b 00000000b		
0b5B' 10	137b	0e7e 00000000b		
0b5C' 13	1377	0e7b 00000000b		
0b5D' 30	1370	0e7b 00100000b		
	1370			
0b00' 00	1300	0e7b 00000000b	1 code 3C	C
0b0F' 00	13b1	0e7b 00000000b		
0b00' 00	1303	0e7e 00000000b		
0b41' 10	1305	0e7b 00000000b		
0b03' 20	1304	0e7b 00100000b		
0b03' 10	13b0	0e7b 00000000b		
0b44' 00	1300	0e7b 00000000b		
0b00' 00	1307	0e7e 00000000b		
	13b0			
0b4b' 00	1309	0e7e 00000000b	1 code 30	0
0b07' 00	1300	0e7b 00000000b		
0b0b' 00	1391	0e7b 00000000b		
0b00' 7C	1303	0e7b 01111100b		
0b0A' 40	1303	0e7b 00000000b		
0b40' 7C	1304	0e7b 01111100b		
0b4C' 00	1305	0e7b 00000000b		
0b06' 00	1306	0e7b 00000000b		
	1307			
0b00' 00	1390	0e7b 00000000b	1 code 30	3
0b00' 40	1300	0e7b 00000000b		
0b70' 20	1400	0e7b 00100000b		
0b71' 10	1401	0e7b 00000000b		
0b73' 00	1403	0e7b 00000000b		
0b73' 10	1403	0e7b 00000000b		
0b74' 20	1404	0e7b 00000000b		
0b75' 00	1400	0e7b 00000000b		
	1406			
0b70' 00	1407	0e7b 00000000b	1 code 3F	7
0b77' 30	1400	0e7b 00111000b		
0b78' 44	1400	0e7b 01000100b		
0b78' 00	1410	0e7b 00000000b		
0b7A' 10	1411	0e7e 00000000b		
0b70' 00	1412	0e7b 00000000b		
0b7C' 10	1413	0e7b 00000000b		
0b76' 00	1414	0e7b 00000000b		
	1410			
0b7E' 00	1436	0e7b 00000000b	1 code 40	0
0b70' 30	1417	0e7b 00111000b		
0b00' 44	1410	0e7b 01000100b		
0b01' 5C	1410	0e7b 01000100b		
0b03' 5C	1420	0e7e 01000100b		
0b03' 40	1431	0e7b 01000100b		
0b04' 3C	1433	0e7b 00111000b		
0b05' 00	1433	0e7b 00000000b		
	1434			
0b00' 04	1420	0e7b 00000000b	1 code 41	0
0b07' 30	1430	0e7e 00111000b		
0b00' 40	1437	0e7e 01000100b		
0b09' 44	1430	0e7b 01000100b		
0b0A' 7C	1430	0e7b 01111100b		
0b00' 44	1430	0e7b 01000100b		
0b0C' 44	1431	0e7b 01000100b		
0b00' 40	1432	0e7b 00000000b		
	1433			
0b00' 00	1434	0e7b 00000000b	1 code 43	0
0b0F' 70	1430	0e7b 01111000b		
0b00' 44	1436	0e7b 01000100b		
0b01' 70	1437	0e7b 01000100b		
0b03' 44	1433	0e7b 01000100b		
0b05' 40	1439	0e7e 01000100b		

0694	70	1440	defb 01111000b		
0698	00	1441	defb 00000000b		
		1442			
0696	00	1443	defb 00000000b	1 code 43	C
0807	30	1444	defb 00111000b		
0808	44	1445	defb 01000100b		
0809	40	1446	defb 01000000b		
0808	40	1447	defb 01000000b		
0808	44	1448	defb 01000100b		
080C	30	1449	defb 00111000b		
0890	00	1450	defb 00000000b		
		1451			
0808	00	1452	defb 00000000b	1 code 44	D
080F	70	1453	defb 01111000b		
0848	40	1454	defb 01000100b		
08A1	44	1455	defb 01000100b		
0802	44	1456	defb 01000100b		
0803	00	1457	defb 01000100b		
0844	70	1458	defb 01111000b		
0808	00	1459	defb 00000000b		
		1460			
0804	00	1461	defb 00000000b	1 code 45	E
0807	7C	1462	defb 01111000b		
0848	40	1463	defb 01000100b		
08A8	70	1464	defb 01111000b		
0844	40	1465	defb 01000100b		
0806	40	1466	defb 01000100b		
080C	7C	1467	defb 01111000b		
0840	00	1468	defb 00000000b		
		1469			
084E	00	1470	defb 00000000b	1 code 46	F
080F	7C	1471	defb 01111000b		
0808	40	1472	defb 01000100b		
0801	70	1473	defb 01111000b		
0802	40	1474	defb 01000100b		
0803	40	1475	defb 01000100b		
0804	40	1476	defb 01000100b		
0808	00	1477	defb 00000000b		
		1478			
0808	00	1479	defb 00000000b	1 code 47	G
0807	30	1480	defb 00111000b		
0808	44	1481	defb 01000100b		
0808	40	1482	defb 01000100b		
0804	0C	1483	defb 01000100b		
0808	44	1484	defb 01000100b		
080C	30	1485	defb 00111000b		
0800	00	1486	defb 00000000b		
		1487			
0808	00	1488	defb 00000000b	1 code 48	H
080F	44	1489	defb 01000100b		
0808	44	1490	defb 01000100b		
08C1	7C	1491	defb 01111000b		
		1492			
08C2	44	1493	defb 01000100b		
08C3	44	1494	defb 01000100b		
08C4	44	1495	defb 01000100b		
08C8	00	1496	defb 00000000b		
		1497			
08C8	7C	1498	defb 01111000b	1 code 49	I
08C8	10	1499	defb 00010000b		
08C8	10	1500	defb 00010000b		
08C4	10	1501	defb 00010000b		
08C8	10	1502	defb 00010000b		
08CC	7C	1503	defb 01111000b		
08C0	00	1504	defb 00000000b		
		1505			
08C8	00	1506	defb 00000000b	1 code 40	J
08CF	04	1507	defb 00000100b		
0808	04	1508	defb 00000100b		
0801	04	1509	defb 00000100b		
0802	44	1510	defb 01000100b		
0803	44	1511	defb 01000100b		
0804	3C	1512	defb 00111000b		
0408	00	1513	defb 00000000b		
		1514			
04C8	00	1515	defb 00000000b	1 code 40	K
0427	40	1516	defb 01000100b		
0808	00	1517	defb 01000100b		
0808	00	1518	defb 01000100b		
0804	00	1519	defb 01000100b		
0808	40	1520	defb 01000100b		
040C	44	1521	defb 01000100b		
0800	00	1522	defb 00000000b		
		1523			
0808	00	1524	defb 00000000b	1 code 4C	L
080F	40	1525	defb 01000100b		
0800	40	1526	defb 01000100b		
0401	40	1527	defb 01000100b		
0802	40	1528	defb 01000100b		
0403	40	1529	defb 01000100b		
0804	7C	1530	defb 01111000b		
0408	00	1531	defb 00000000b		
		1532			
0804	00	1533	defb 00000000b	1 code 40	M
0807	44	1534	defb 01000100b		
0808	0C	1535	defb 01000100b		
0808	04	1536	defb 01000100b		
0804	44	1537	defb 01000100b		
0808	44	1538	defb 01000100b		
080C	44	1539	defb 01000100b		
0405	00	1540	defb 00000000b		
		1541			
081E	00	1542	defb 00000000b	1 code 4E	N
040F	44	1543	defb 01000100b		

06C1*	44	1403	00fb 017001000		
06C3*	44	1403	00fb 010001000		
06C4*	44	1404	00fb 013001000		
06C5*	00	1400	00fb 000000000		
06C6*	00	1407	00fb 000000000	1 code 43	C
06C7*	7C	1400	00fb 011111000		
06C8*	10	1409	00fb 000100000		
06C9*	10	1500	00fb 000100000		
06CA*	10	1501	00fb 000100000		
06CB*	10	1502	00fb 000100000		
06CC*	7C	1503	00fb 011111000		
06CD*	00	1504	00fb 000000000		
06CE*	00	1505	00fb 000000000	1 code 46	D
06CF*	04	1506	00fb 000000000		
06D0*	04	1507	00fb 000000000		
06D1*	00	1508	00fb 000000000		
06D2*	44	1509	00fb 000000000		
06D3*	44	1510	00fb 010001000		
06D4*	3C	1511	00fb 010001000		
06D5*	00	1512	00fb 000000000		
06D6*	00	1513	00fb 000000000		
06D7*	40	1514	00fb 000000000	1 code 45	E
06D8*	40	1515	00fb 010001000		
06D9*	30	1516	00fb 010001000		
06DA*	00	1517	00fb 010001000		
06DB*	30	1518	00fb 010001000		
06DC*	40	1519	00fb 010001000		
06DD*	44	1520	00fb 010001000		
06DE*	00	1521	00fb 010001000		
06DF*	00	1522	00fb 000000000		
06E0*	00	1523	00fb 000000000		
06E1*	40	1524	00fb 000000000	1 code 46	F
06E2*	40	1525	00fb 010001000		
06E3*	40	1526	00fb 010001000		
06E4*	40	1527	00fb 010001000		
06E5*	40	1528	00fb 010001000		
06E6*	40	1529	00fb 010001000		
06E7*	7C	1530	00fb 011111000		
06E8*	00	1531	00fb 000000000		
06E9*	00	1532	00fb 000000000	1 code 47	G
06F0*	44	1533	00fb 010001000		
06F1*	0C	1534	00fb 011001000		
06F2*	34	1535	00fb 010101000		
06F3*	44	1536	00fb 010001000		
06F4*	44	1537	00fb 010001000		
06F5*	44	1538	00fb 010001000		
06F6*	44	1539	00fb 010001000		
06F7*	00	1540	00fb 000000000		
06F8*	00	1541	00fb 000000000	1 code 48	H
06F9*	00	1542	00fb 010001000		
06FA*	04	1543	00fb 011001000		
06FB*	04	1544	00fb 010001000		
06FC*	34	1545	00fb 010001000		
06FD*	4C	1546	00fb 010001000		
06FE*	44	1547	00fb 010001000		
06FF*	00	1548	00fb 010001000		
06FA*	00	1549	00fb 000000000		
06FB*	00	1550	00fb 000000000	1 code 49	I
06FC*	30	1551	00fb 000000000		
06FD*	44	1552	00fb 000000000		
06FE*	44	1553	00fb 010001000		
06FF*	44	1554	00fb 010001000		
06FA*	44	1555	00fb 010001000		
06FB*	44	1556	00fb 010001000		
06FC*	30	1557	00fb 010001000		
06FD*	00	1558	00fb 000000000		
06FE*	00	1559	00fb 000000000	1 code 50	J
06FF*	70	1560	00fb 011111000		
0700*	44	1561	00fb 010001000		
0701*	44	1562	00fb 010001000		
0702*	70	1563	00fb 010001000		
0703*	40	1564	00fb 011111000		
0704*	40	1565	00fb 010001000		
0705*	40	1566	00fb 010001000		
0706*	00	1567	00fb 000000000		
0707*	00	1568	00fb 000000000	1 code 51	K
0708*	30	1569	00fb 000000000		
0709*	44	1570	00fb 000000000		
070A*	44	1571	00fb 010001000		
070B*	44	1572	00fb 010001000		
070C*	44	1573	00fb 010001000		
070D*	34	1574	00fb 010001000		
070E*	30	1575	00fb 000000000		
070F*	00	1576	00fb 000000000		
0710*	00	1577	00fb 000000000	1 code 52	L
0711*	70	1578	00fb 011111000		
0712*	44	1579	00fb 010001000		
0713*	44	1580	00fb 010001000		
0714*	70	1581	00fb 011111000		
0715*	40	1582	00fb 010001000		
0716*	44	1583	00fb 010001000		
0717*	00	1584	00fb 010001000		
0718*	00	1585	00fb 000000000		
0719*	00	1586	00fb 000000000	1 code 53	M
071A*	30	1587	00fb 000000000		
071B*	40	1588	00fb 000000000		
071C*	30	1589	00fb 000000000		
071D*	04	1590	00fb 000000000		
071E*	44	1591	00fb 010001000		
071F*	30	1592	00fb 000000000		
0720*	00	1593	00fb 000000000		
0721*	00	1594	00fb 000000000		
0722*	00	1595	00fb 000000000		
0723*	00	1596	00fb 000000000		
0724*	00	1597	00fb 000000000		
0725*	00	1598	00fb 000000000		
0726*	00	1599	00fb 000000000		
0727*	00	1600	00fb 000000000		

0710° 00	1004	defb 00000000	1 code 04	7
0710° 7C	1007	defd 01111100		
0720° 30	1008	defb 00010000		
0721° 30	1009	defd 00010000		
0722° 10	1009	defd 00010000		
0723° 30	1009	defb 00010000		
0724° 30	1002	defd 00010000		
0725° 00	1003	defb 00000000		
	1004			
0726° 00	1005	defb 00000000	1 code 05	U
0727° 44	1006	defd 01000100		
0728° 44	1007	defd 01000100		
0729° 44	1008	defd 01000100		
0730° 44	1009	defd 01000100		
0731° 44	1010	defd 01000100		
0732° 44	1011	defd 01110000		
0733° 30	1012	defd 00000000		
0734° 00	1013			
	1014	defd 00000000	1 code 06	V
0735° 44	1015	defd 01000100		
0736° 44	1016	defd 01000100		
0737° 44	1017	defd 01000100		
0738° 44	1018	defd 01000100		
0739° 20	1019	defd 00010000		
073A° 30	1020	defd 00010000		
073B° 00	1021	defd 00000000		
	1022			
073C° 00	1023	defd 02000000	1 code 07	W
073D° 44	1024	defb 01000100		
073E° 44	1025	defd 01000100		
073F° 44	1026	defb 01000100		
0740° 34	1027	defb 01000100		
0741° 34	1028	defd 01000100		
0742° 20	1029	defd 00010000		
0743° 00	1030	defb 00000000	1 code 08	X
	1031			
0744° 00	1032	defd 00000000		
0745° 44	1033	defd 01000100		
0746° 20	1034	defd 00010000		
0747° 30	1035	defb 00010000		
0748° 30	1036	defb 00010000		
0749° 20	1037	defb 00010000		
074A° 44	1038	defb 01000100		
074B° 00	1039	defb 00000000		
	1040			
074C° 00	1041	defb 00000000	1 code 09	Y
074D° 44	1042	defb 01000100		
074E° 44	1043	defd 01000100		
074F° 20	1044	defb 00010000		
0750° 1C	1045	defb 02000000		
0751° 10	1046	defb 00010000		
0752° 30	1047	defb 00010000		
	1048	defd 00000000		
0753° 00	1049			
	1050			
0754° 00	1051	defb 00000000	1 code 0A	Z
0755° 7C	1052	defd 01111100		
0756° 04	1053	defb 00000100		
0757° 00	1054	defd 00000100		
0758° 30	1055	defd 00000100		
0759° 30	1056	defd 00010000		
075A° 20	1057	defb 00000000		
075B° 7C	1058	defd 01111100		
075C° 00	1059	defd 00000000		
	1060			
075D° 00	1061	defb 00000000	1 code 0B	[
075E° 00	1062	defd 00110000		
075F° 20	1063	defb 00000000		
0760° 00	1064	defb 00000000		
	1065			
0761° 00	1066	defd 00000000	1 code 0C	\
0762° 00	1067	defb 00000000		
0763° 00	1068	defd 01000100		
0764° 00	1069	defd 01000100		
0765° 20	1070	defb 00000000		
0766° 10	1071	defb 00000000		
0767° 10	1072	defb 00000000		
0768° 00	1073	defd 00000000		
0769° 04	1074	defd 00000000		
076A° 00	1075	defd 00000000		
	1076			
076B° 00	1077	defb 00000000	1 code 0D	]
076C° 70	1078	defb 01110000		
076D° 00	1079	defb 00010000		
076E° 00	1080	defb 00010000		
076F° 10	1081	defb 00010000		
0770° 10	1082	defb 00010000		
0771° 10	1083	defb 00010000		
0772° 10	1084	defb 00010000		
0773° 00	1085	defb 00000000		
	1086			
0774° 00	1087	defb 00000000	1 code 0E	^
0775° 10	1088	defb 00010000		
0776° 30	1089	defb 00110000		
0777° 04	1090	defd 01000100		
0778° 10	1091	defb 00010000		
0779° 10	1092	defb 00010000		
077A° 30	1093	defb 00010000		
077B° 00	1094	defb 00000000		
	1095			
077C° 00	1096	defb 00000000	1 code 0F	_
077D° 00	1097	defd 00000000		
077E° 00	1098	defb 00000000		
077F° 00	1099	defd 00000000		

0778' 00	1700	defb 00000000b		
077C' 00	1701	defb 00000000b		
077D' FC	1702	defb 11111100b		
	1703			
0778' 00	1704	defb 00000000b	1 code 60	round sign
0779' 10	1705	defb 00011000b		
0780' 24	1706	defb 00100100b		
0781' 70	1707	defb 01110000b		
0782' 20	1708	defb 00100000b		
0783' 20	1709	defb 00100000b		
0784' 7C	1710	defb 01111100b		
0783' 00	1711	defb 00000000b		
	1712			
0788' 00	1713	defb 00000000b	1 code 61	"
0787' 00	1714	defb 00000000b		
0788' 30	1715	defb 00111000b		
0789' 04	1716	defb 00000100b		
078A' 3C	1717	defb 00111100b		
078B' 04	1718	defb 01000100b		
078C' 3C	1719	defb 00111100b		
078D' 00	1720	defb 00000000b		
	1721			
0788' 00	1722	defb 00000000b	1 code 62	b
078F' 20	1723	defb 00100000b		
0790' 20	1724	defb 00100000b		
0791' 20	1725	defb 00111000b		
0792' 24	1726	defb 00100100b		
0793' 24	1727	defb 00100100b		
0794' 30	1728	defb 00111000b		
0793' 00	1729	defb 00000000b		
	1730			
0794' 00	1731	defb 00000000b	1 code 62	b
0797' 00	1732	defb 00000000b		
0798' 1C	1733	defb 00011100b		
0799' 20	1734	defb 00100000b		
079A' 20	1735	defb 00100000b		
079B' 20	1736	defb 00100000b		
079C' 1C	1737	defb 00011100b		
079D' 00	1738	defb 00000000b		
	1739			
0798' 00	1740	defb 00000000b	1 code 64	d
079F' 08	1741	defb 00001000b		
07A0' 0b	1742	defb 00001000b		
07A1' 20	1743	defb 00111000b		
07A2' 40	1744	defb 01001000b		
07A3' 40	1745	defb 01001000b		
07A4' 20	1746	defb 00111000b		
07A3' 00	1747	defb 00000000b		
	1748			
07A4' 00	1749	defb 00000000b	1 code 63	e
07A7' 00	1750	defb 00000000b		
07A8' 30	1751	defb 00111000b		
	1752			
07A9' 44	1753	defb 01000100b		
07AA' 78	1754	defb 01111000b		
07AB' 40	1755	defb 01000000b		
07AC' 3C	1756	defb 00111100b		
07AD' 00	1757	defb 00000000b		
	1758			
07A8' 00	1759	defb 00000000b	1 code 66	f
07AF' 10	1760	defb 00010000b		
07B0' 20	1761	defb 00101000b		
07B1' 70	1762	defb 00100000b		
07B2' 20	1763	defb 00100000b		
07B3' 20	1764	defb 00100000b		
07B4' 20	1765	defb 00000000b		
07B3' 00	1766			
	1767			
07B4' 00	1768	defb 00000000b	1 code 67	g
07B7' 00	1769	defb 00000000b		
07B8' 30	1770	defb 00111000b		
07B9' 40	1771	defb 01001000b		
07BA' 40	1772	defb 00111000b		
07BB' 20	1773	defb 00001000b		
07BC' 08	1774	defb 00110000b		
07BD' 30	1775			
	1776			
07B8' 00	1777	defb 00000000b	1 code 68	h
07BF' 40	1778	defb 01000000b		
07C0' 40	1779	defb 01000000b		
07C1' 70	1780	defb 01110000b		
07C2' 40	1781	defb 01001000b		
07C3' 40	1782	defb 01001000b		
07C4' 40	1783	defb 00000000b		
07C5' 00	1784			
	1785			
07C6' 00	1786	defb 00000000b	1 code 69	i
07C7' 10	1787	defb 00010000b		
07C8' 00	1788	defb 00000000b		
07C9' 20	1789	defb 00110000b		
07CA' 10	1790	defb 00010000b		
07CB' 10	1791	defb 00010000b		
07CC' 38	1792	defb 00111000b		
07CD' 00	1793			
	1794			
07C8' 00	1795	defb 00000000b	1 code 6A	j
07CF' 08	1796	defb 00000000b		
07D0' 00	1797	defb 00000000b		
07D1' 08	1798	defb 00000000b		
07D2' 08	1799	defb 00000000b		
07D3' 08	1800	defb 00000000b		
07D4' 48	1801	defb 00100000b		
07D5' 30	1802			
	1803			
07D6' 00	1804	defb 00000000b	1 code 6B	k

0707*	20	1000	00fb 00177000		
0708*	20	1009	00fb 00171000b		
0709*	20	1006	00fb 00173000b		
070A*	30	1007	00fb 00173000b		
070B*	20	1008	00fb 001C1000b		
070C*	24	1009	00fb 00173100b		
070D*	00	1010	00fb 00020000b		
		1011			
070E*	00	1012	00fb 00033000b	1 code 4C	1
070F*	20	1011	00fb 00133000b		
0710*	20	1014	00fb 001C3000b		
0711*	20	1010	00fb 00130000b		
0712*	20	1016	00fb 001C3000b		
0713*	20	1017	00fb 001C3000b		
0714*	10	1010	00fb 00011000b		
0715*	00	1010	00fb 000E3000b		
		2020			
0716*	00	1021	00fb 0003C000b	1 code 4D	n
0717*	00	1022	00fb 000C3000b		
0718*	20	1022	00fb 00171000b		
0719*	30	1020	00fb 0101C100b		
071A*	00	1020	00fb 01013100b		
071B*	04	1026	00fb 01C17100b		
071C*	04	1027	00fb 01013100b		
071D*	00	2020	00fb 00CC3000b		
		1020			
071E*	00	1020	00fb 000C0000b	1 code 4E	n
071F*	00	1021	00fb 000C3000b		
0720*	70	1012	00fb 01113000b		
0721*	00	1021	00fb 01001300b		
0722*	40	1020	00fb 01001300b		
0723*	60	1020	00fb 010C1000b		
0724*	40	1026	00fb 010C1000b		
0725*	00	1017	00fb 000C0000b		
		1020			
0726*	00	1029	00fb 000C3000b	1 code 4F	d
0727*	00	1000	00fb 0002C000b		
0728*	10	1041	00fb 00111300b		
0729*	04	1042	00fb 01033100b		
072A*	44	2002	00fb 0100C100b		
072B*	44	1044	00fb 01003100b		
072C*	20	1040	00fb 00111300b		
072D*	00	1046	00fb 00000000b		
		1007			
072E*	00	1000	00fb 00000300b	1 code 70	d
072F*	00	1000	00fb 000C3000b		
0800*	70	1000	00fb 01117000b		
0801*	40	1001	00fb 010C1300b		
0802*	40	1002	00fb 010C1300b		
0803*	70	1009	00fb 01110000b		
0804*	00	1010	00fb 01C01000b		
0805*	40	1019	00fb 010C0300b		
		1019			
0806*	00	1007	00fb 00000000b	1 code 71	s
0807*	00	2000	00fb 00000300b		
0808*	20	1019	00fb 00111000b		
0809*	00	1040	00fb 01001000b		
080A*	40	1041	00fb 010C1000b		
080B*	20	1002	00fb 00111000b		
080C*	00	1002	00fb 00001000b		
080D*	0C	1044	00fb 00001100b		
		1040			
080E*	00	1000	00fb 00000300b	1 code 72	r
080F*	00	1047	00fb 00000300b		
0810*	10	1040	00fb 00011000b		
0811*	20	1049	00fb 00103000b		
0812*	20	1070	00fb 00100000b		
0813*	20	1071	00fb 00103000b		
0814*	20	1072	00fb 00100300b		
0815*	00	1072	00fb 00000300b		
		1074			
0816*	00	1070	00fb 00000300b	1 code 73	b
0817*	00	1076	00fb 00000300b		
0818*	10	1077	00fb 0011C000b		
0819*	40	1070	00fb 01003000b		
081A*	10	1079	00fb 00110000b		
081B*	00	1000	00fb 00001000b		
081C*	70	1001	00fb 01110000b		
081D*	00	1002	00fb 00000000b		
		1001			
081E*	00	1000	00fb 00000300b	1 code 74	1
081F*	20	1000	00fb 00100300b		
0820*	70	1000	00fb 01110300b		
0821*	20	1007	00fb 00103000b		
0822*	20	1000	00fb 00100000b		
0823*	20	1000	00fb 00103000b		
0824*	10	1090	00fb 00011000b		
0825*	00	1091	00fb 00003000b		
		1092			
0826*	00	1001	00fb 00000000b	1 code 75	u
0827*	00	1094	00fb 00000000b		
0828*	40	1000	00fb 01001000b		
0829*	40	1090	00fb 01001000b		
082A*	40	1097	00fb 01001000b		
082B*	40	1000	00fb 01001000b		
082C*	10	1090	00fb 00110000b		
082D*	00	2000	00fb 00003000b		
		1901			
082E*	00	1902	00fb 00000000b	1 code 76	v
082F*	00	1901	00fb 00033000b		
0830*	04	1904	00fb 0100C100b		
0831*	44	1000	00fb 01000100b		
0832*	20	1004	00fb 00101000b		
0833*	20	1007	00fb 00101000b		



0034" 10	1908	defb 00C1C300b		
0035" 00	1909	defb 000C3000b		
	1910			
0036" 00	1911	defb 000C3300b	1 code 77	"
0037" 00	1912	defb 000C3000b		
0038" 44	1913	defb 010C3100b		
0039" 54	1914	defb 010C3100b		
003A" 54	1915	defb 010C3100b		
003B" 54	1916	defb 010C3100b		
003C" 20	1917	defb 00101000b		
003D" 00	1918	defb 000C0000b		
	1919			
003E" 00	1920	defb 000C3000b	1 code 78	"
003F" 00	1921	defb 000C3000b		
0040" 44	1922	defb 010C3100b		
0041" 20	1923	defb 001C3100b		
0042" 10	1924	defb 000C3000b		
0043" 20	1925	defb 001C3100b		
0044" 44	1926	defb 010C3100b		
0045" 00	1927	defb 000C3000b		
	1928			
0046" 00	1929	defb 000C3000b	1 code 79	"
0047" 00	1930	defb 000C3000b		
0048" 44	1931	defb 01001300b		
0049" 44	1932	defb 01001300b		
004A" 44	1933	defb 010C3100b		
004B" 30	1934	defb 00111300b		
004C" 00	1935	defb 000C3000b		
004D" 70	1936	defb 01110000b		
	1937			
004E" 00	1938	defb 000C0000b	1 code 78	"
004F" 00	1939	defb 000C3000b		
0050" 7C	1940	defb 01111100b		
0051" 00	1941	defb 000C1000b		
0052" 1C	1942	defb 00010300b		
0053" 20	1943	defb 001C3000b		
0054" 7C	1944	defb 01111100b		
0055" 00	1945	defb 000C3000b		
	1946			
0056" 00	1947	defb 00000300b	1 code 78	"
0057" 1C	1948	defb 00011100b		
0058" 10	1949	defb 0001C300b		
0059" 60	1950	defb 011C3000b		
005A" 10	1951	defb 0001C300b		
005B" 10	1952	defb 00C10000b		
005C" 1C	1953	defb 00011100b		
005D" 00	1954	defb 000C0000b		
	1955			
005E" 00	1956	defb 00000000b	1 code 7C	"
005F" 10	1957	defb 00310030b		
0060" 10	1958	defb 0001C300b		
0061" 10	1959	defb 00010000b		
0062" 10	1960	defb 00010000b		
0063" 10	1961	defb 00010000b		
0064" 10	1962	defb 00010300b		
0065" 00	1963	defb 00000300b		
	1964			
0066" 00	1965	defb 00000000b	1 code 78	"
0067" 70	1966	defb 01110000b		
0068" 10	1967	defb 00010000b		
0069" 00	1968	defb 000C1300b		
006A" 10	1969	defb 00010300b		
006B" 10	1970	defb 0001C000b		
006C" 70	1971	defb 01110000b		
006D" 00	1972	defb 00000000b		
	1973			
006E" 00	1974	defb 000E0000b	1 code 78	"
006F" 20	1975	defb 001C3100b		
0070" 50	1976	defb 0101C000b		
0071" 00	1977	defb 00000300b		
0072" 00	1978	defb 000C0000b		
0073" 00	1979	defb 00000300b		
0074" 00	1980	defb 0000C300b		
0075" 00	1981	defb 00000000b		
	1982			
0076" 70	1983	defb 01111000b	1 code 7F	COPYRIGHT
0077" 04	1984	defb 10000100b		
0078" 94	1985	defb 10010100b		
0079" 44	1986	defb 10100100b		
007A" 44	1987	defb 1010C300b		
007B" 94	1988	defb 1001C300b		
007C" 04	1989	defb 1000C300b		
007D" 70	1990	defb 01111000b		
	1991			
007E" 00	1992	defb 00003000b	1 code 80	graphics codes
007F" 00	1993	defb 00000300b		
0080" 00	1994	defb 0000C000b		
0081" 00	1995	defb 00000000b		
0082" 0C	1996	defb 00000000b		
0083" 0C	1997	defb 0000C000b		
0084" 00	1998	defb 000C0000b		
0085" 00	1999	defb 00000000b		
	2000			
0086" 1C	2001	defb 00011100b	1 code 81	graphics
0087" 1C	2002	defb 00011100b		
0088" 1C	2003	defb 00011100b		
0089" 1C	2004	defb 00011100b		
008A" 00	2005	defb 000C3000b		
008B" 00	2006	defb 00000000b		
008C" 00	2007	defb 00000000b		
008D" 00	2008	defb 00000000b		
	2009			
008E" 0C	2010			
	2011	defb 11100000b	1 code 82	graphics

000F	00	2012	defb 11100000b		
0090	00	2013	defb 11100000b		
0091	00	201b	defb 11100000b		
0092	00	2010	defb 00000000b		
0093	00	201A	defb 00000000b		
0094	00	2017	defb 00000000b		
0095	00	2010	defb 00000000b		
		2019			
0096	PC	2020	defb 11111100b	1 code 03	graphics
0097	PC	2021	defb 11111100b		
0098	PC	2022	defb 11111100b		
0099	PC	2023	defb 11111100b		
000A	00	202b	defb 00000000b		
0090	00	2020	defb 00000000b		
009C	00	2020	defb 00000000b		
009D	00	2027	defb 00000000b		
		2020			
		2029			
000E	00	2030	defb 00000000b	1 code 04	graphics
000F	00	2031	defb 00000000b		
0000	00	2032	defb 00000000b		
0001	00	2033	defb 00000000b		
0002	1C	2034	defb 00011100b		
0003	1C	2030	defb 00011100b		
0004	1C	2030	defb 00011100b		
0000	1C	2037	defb 00011100b		
		2030			
0006	1C	2039	defb 00011100b	1 code 03	graphics
00A7	1C	2040	defb 00011100b		
0000	1C	2041	defb 00011100b		
00A9	1C	2042	defb 00011100b		
00BA	1C	2043	defb 00011100b		
00A0	1C	2044	defb 00011100b		
00AC	1C	2040	defb 00011100b		
00A0	1C	2040	defb 00011100b		
		2047			
		2040			
00A0	00	2049	defb 11100000b	1 code 00	graphics
00AF	00	2000	defb 11100000b		
0000	00	2001	defb 11100000b		
0001	00	2002	defb 11100000b		
0002	1C	2003	defb 00011100b		
0003	1C	2004	defb 00011100b		
0004	1C	2009	defb 00011100b		
0003	1C	2000	defb 00011100b		
		2007			
0000	PC	2000	defb 11111100b	1 code 07	graphics
0007	PC	2009	defb 11111100b		
0000	PC	2000	defb 11111100b		
0003	PC	20A1	defb 11111100b		
000A	1C	20b2	defb 00011100b		
0000	1C	20b3	defb 00011100b		
000C	1C	2004	defb 00011100b		
0000	1C	2000	defb 00011100b		
		2000			
0000	00	20b7	defb 00000000b	1 code 00	graphics
0000	00	2000	defb 00000000b		
00C0	00	20b9	defb 00000000b		
00C1	00	2070	defb 00000000b		
00C2	00	2071	defb 11100000b		
00C3	00	2072	defb 11100000b		
00C0	00	2073	defb 11100000b		
00C5	00	2070	defb 11100000b		
		2073			
00C0	1C	2070	defb 00011100b	1 code 09	graphics
00C7	1C	2077	defb 00011100b		
00C0	1C	2070	defb 00011100b		
00C0	1C	2070	defb 00011100b		
00CA	00	2000	defb 11100000b		
00C0	00	2001	defb 11100000b		
00CC	00	2002	defb 11100000b		
00C0	00	2003	defb 11100000b		
		2004			
00C0	00	2000	defb 11100000b	1 code 0A	graphics
00C0	00	2000	defb 11100000b		
0000	00	2007	defb 11100000b		
0001	00	2000	defb 11100000b		
0002	00	2000	defb 11100000b		
0003	00	2070	defb 11100000b		
0004	00	2071	defb 11100000b		
0003	00	2072	defb 11100000b		
		2073			
0000	PC	2074	defb 11111100b	1 code 0B	graphics
0007	PC	2073	defb 11111100b		
0000	PC	2070	defb 11111100b		
0009	PC	2077	defb 11111100b		
000A	00	2070	defb 11100000b		
0000	00	2079	defb 11100000b		
000C	00	2100	defb 11100000b		
0000	00	2101	defb 11100000b		
		2102			
0000	00	2103	defb 00000000b	1 code 0C	graphics
0000	00	210b	defb 00000000b		
0000	00	2100	defb 00000000b		
0001	00	2100	defb 00000000b		
0002	PC	2107	defb 11111100b		
0003	PC	2100	defb 11111100b		
0104	PC	2109	defb 11111100b		
01E5	PC	2110	defb 11111100b		
		2111			
0000	1C	2112	defb 00011100b	1 code 0D	graphics
0007	1C	2113	defb 00011100b		
0010	1C	2114	defb 00011100b		
00E9	1C	2113	defb 00011100b		

```

0000" PC 2116 Refb 11111100b
0000" PC 2117 Refb 11111100d
0000" PC 2118 defd 11111100d
0000" PC 2119 defd 11111100d
2120 I
0000" EQ defd 11100000d 1000 00 graph500
0000" EQ defd 11100000d
0000" EQ 2125 defd 11100000d
0001" EQ 2124 defd 11100000d
0002" PC 2129 defd 11111100b
0003" PC 2120 defd 11111100d
0004" PC 2127 defd 11111100d
0005" PC 2120 defd 11111100d
2129 I
0006" PC 2130 defd 11111100d 1000 00 graph500
0007" PC 2131 defb 11111100d
0008" PC 2132 defb 11111100d
0009" PC 2133 defd 11111100b
000A" PC 2134 defd 11111100d
000B" PC 2135 Refd 11111100d
000C" PC 213d Refd 11111100d
000D" PC 2137 defd 11111100d
2150 END
Additional Input after END statement ignored

```

```

6 Reserve ATTRY7 0030 ATTCYL 0011 8 Reserve 80TLN 0020
C Reserve CALCP0 053C CALCP1 0526 CALCP2 013A Reserve CMWGI 0000
CHSET 047E CMST 057E CM7BL 002P CLINI7 1800 CL0CTL 000C
CL4S00 0270 CL4SC0 002E IN CL4SCW 027C IN CLP5C0 0292 CLP5C1 02A3
CL4SC2 0280 CLP5C5 02R2 CL4SC4 0245 CLP5C0 02C3 CLP5C0 0205
CL4SC7 0280 CL4SXT 02E7 COMVPM 04PC C00R05 5C70 CURPOS 0054
0 Reserve 0770 0770 0770 0770 0770 0770 0770 0770 0770 0770
DRHSPT 0004 0004 0004 0004 0004 0004 0004 0004 0004 0004
DRHSPT 0144 0144 0144 0144 0144 0144 0144 0144 0144 0144
GETY70 0010 IN GETATY 03C0 IN GETCRO 0304 GETCTL 0019 GETY80 03C0
GETYU0 0304 IN GETC1 05EC GETVAL 04AC G000R8 0142 GETYU8 002I IN
GSPHST 077E GSPST 0070 GRT6L 0031 GTCW22 0340 GRLP 0240
GTCN00 0010 IN GTCW1 035P GTCM2 0345 GTCW2 0340 GTCM0 0526
GTCN5 0544 GTCN3I 0347 GTCM2 0591 GTCM4 8304 GTCM2 035P
GTCN0 05C4 GTCN3I 0575 GTCM2 0591 GTCM4 8304 GTCM2 035P
GTINDK 003A 4 Reserve GTINDK 003A GTINDK 003A GTINDK 003A GTINDK 003A
INVP40 0147 L Reserve INVP40 0147 INVP40 0147 INVP40 0147 INVP40 0147
LNU 013P LCDP 0201 LNU 013P LNU 013P LNU 013P LNU 013P
MARGIN 0030 MARGIN 0030 MARGIN 0030 MARGIN 0030 MARGIN 0030
NEXT2 008A NEXT2 008A NEXT2 008A NEXT2 008A NEXT2 008A
NORR5 0111 NORR5 0111 NORR5 0111 NORR5 0111 NORR5 0111
PAR0PR 04P3 PAR0PR 04P3 PAR0PR 04P3 PAR0PR 04P3 PAR0PR 04P3
SCINIT 1701 SCINIT 1701 SCINIT 1701 SCINIT 1701 SCINIT 1701
SCALY 0257 SCALY 0257 SCALY 0257 SCALY 0257 SCALY 0257
SETY70 0012 IN SETY7T 02P4 IN SETCRO 0144 SETCRO 0144 SETCRO 0144
SETN00 0510 SETN00 0510 SETN00 0510 SETN00 0510 SETN00 0510
SETN50 0016 IN SETN50 0016 IN SETN50 0016 IN SETN50 0016 IN SETN50 0016 IN
ST0LX0 024C ST0LX1 0240 ST0LX2 0264 ST0LX3 0264 ST0LX4 0264
STPDSN 0590 STPDSN 0590 STPDSN 0590 STPDSN 0590 STPDSN 0590
TSTP01 04P4 TSTP01 04P4 TSTP01 04P4 TSTP01 04P4 TSTP01 04P4
UPDA7E 047E UPDA7E 047E UPDA7E 047E UPDA7E 047E UPDA7E 047E
WRCM0 0042 IN WRCM0 0042 IN WRCM0 0042 IN WRCM0 0042 IN WRCM0 0042 IN
WRCM12 0049 WRCM12 0049 WRCM12 0049 WRCM12 0049 WRCM12 0049
WRCM5 0095 WRCM5 0095 WRCM5 0095 WRCM5 0095 WRCM5 0095
WSTRG 0104 IN WSTRG 0104 IN WSTRG 0104 IN WSTRG 0104 IN WSTRG 0104 IN
WSTRI 01dT WSTRI 01dT WSTRI 01dT WSTRI 01dT WSTRI 01dT

```

No errors detected

#### Cross reference listing (WRSP version 4.7)

```

Symbol Refs (0 = definition 1 = write <blank> = read)
ATTRY7 1340 6725 903 9060
ATTCYL 880 448 9075
80TLN 1180 322 8448
CALCP1 1042 10490
CALCP2 1019 10620
CALCP0 498 1023 10310
CMWGI 420 892
CMWST 390 125 897
CMWST 39 11200
CM7BL 1260 170 499 8580
CLINI7 390 811
CLRCTL 840 911 8528
CL4S00 87 9140
CL4SC0 907 967 9710
CL4SC1 9830 932
CL4SC2 992P
CL4SC3 9950 641
CL4SC4 9970 426
CL4SC5 4040 420
CL4SC6 617 4210
CL4SC7 991 4110
CL4SC8 30 870
CL4SC9 27 1970
CL4SXT 830 4130
COMVPM 411 697 811 10100
C00R05 110 9115
CURPOS 1290 810 10844 1089
047AR 740 147 8440
0577 590 60
0P4005 1310 10850 1090
0P0IT 1410 224 240 249 289 3018 3048
685 7605 7805 10459 1107
DRHSPT 460 9730 9795
DRIV85 540 57 58
ENBL04 887 9800
ENBLM0 895 9770
ER0R87 1130 317 334 917
8XTWRM 882 882 9168
PIX 400
PIX70L 420

```

GET400	105	8010					
GET070	51	1030					
GET477	20	5030					
GETC1	814	920	8220				
GETC00	104	8080					
GETCTL	1020	891	8680	8690			
GETCUB	31	1040					
GETCUB	20	8090					
GETVAL	889	9380					
GOODRE	5120	417	439	500	475	482	870
		890	915				
GRBLK	461	5090					
GRPHST	400	127	859				
GRPST	40	19220					
GRTEL	1270	167	776	8600			
GTC201	1104	11040					
GTC202	1110	11150					
GTC20C	707	732	10980				
GTCN1	7020	779	784				
GTCN2	7040	771					
GTCN22	7090						
GTCN23	720	7230					
GTCN3	716	7260					
GTCN51	7200	743					
GTCN32	753	755	7870				
GTCN4	722	725	742	7640			
GTCN5	775	7800					
GTCN6	781	7890					
GTCN40	20	6930					
GTCN00	104	6880					
GTCN00	31	1040					
GTCNMP	11110	1114					
GTIN04	1360	7020	710	750	773		
HR0KPT	450	663	8660	969	9710	980	9820
INS00L	4620						
INS00T	574	877					
INVP40	155	157	316	432	435	437	560
		564	566	655	838	993	
L0PD0N	150	10800					
LINCOL	800	411	8220	8650	8660		
LINL0N	1200	182	445	575	812	8400	900
		994	1010	1032			
LNB0U	440	570	1031	10670			
LDDP	4080	510					
LDDP0	4670	494					
LDDP1	4740	480					
MA0GIN	1400	8500	1051				
MA0K0	1320	194	6010	8540			
MDV051	500	59	870				
MSKCTL	980	670	8670				
NEKT1	2500	255					
NEKT2	2560	259					
NEKT5	2750	278					
NOINVE	260	2850					
ND001	228	2540					
ND002	254	2600					
ND005	275	2790					
ND0T0G	553	4010					
NDX00	223	2400					
NK7SC	485	4850					
PARAM0	550	546					
P40000	9920	997					
PRAHT	530						
QANTDP	520	885					
RECL0N	450	357					
SCIN17	570	841					
SC0CTL	1110	550	425	8420			
SC0L0	330	450	4410				
SC0L0	50	1140					
SC0L00	114	4250					
SC0LCT	1250	526	5500	3350	8466		
SC0LKT	490	5010	544				
SC0GLL	27	4270					
SC0SI	560	519	442	572	1014	1560	
0070	942	9610					
007A00	97	6470					
007070	38	970					
007A77	27	6500					
007C00	85	4090					
007CU0	50	850					
SETCUN	27	4150					
SETM00	101	6770					
SETMD1	836	8710					
SETMD2	874	8870					
SETMD0	33	1100					
SETM00	33	8530					
SETM50	30	1010					
SETM5K	27	6800					
ST0L4	455	5170					
ST0L40	5200	540					
ST0LX1	5210	554					
ST0LX2	531	5550					
STK0N0	490	880					
STMD00	110	8290					
STPD0N	511	1024	10830				
ST0GCT	1580	3950	8550	8880			
SKT4	8990						
T00T40	4510	500	547				
TST01	940	9560					
TSTP00	414	694	9890				
TSTP01	501	9540					
TV0UL1	320	3350					
TV0UL0	191	3190					
UD0	500	163	782				
UPD070	8970						
UPD005	324	416	834	914	10210		
V405	480	350					
V10MCD	540	653	6600	872	8560		
VIM000	1070	831					

WRCM0	77	1450	
WRCM11	162	1670	
WRCM12	160	1700	
WRCM13	166	169	1710
WRCM14	183	1865	
WRCM15	187	1890	
WRCM2	1940		
WRCM3	199	2010	
WRCM5	2060	209	
WRCM6	217	2190	
WRCM7	2960		
WRCM8	27	1490	377
WRCM9	30	770	
WRCM17	302	309	3110
WRJTR0	76	3460	
WRJTR1	3315	340	
WRJTR2	335	3610	
WRJTR3	1820	393	
WRJTR6	30	760	
WRJTR8	27	3740	366
WRJTR11	169	3910	
WRJTR2	3980	402	

## APPENDIX C-3

### 40 COLUMN MODE

Name: 40 Column Mode Support

#### Description:

This component provides support to the application programmer for using the 40-column mode feature of the TS 2068. 40-column mode is implemented by modifying the character width from 8 to 6 pixels. The services include position control, clear screen and scroll screen services and display of characters. For ease of use from BASIC, status is returned in the BC register pair, usually zero for successful completion and designated non-zero values for other conditions. The interface from BASIC is by means of PDKE'ing the input parameters to designated RAM locations prior to invoking the service via the USR function. See page 4 of the code listing for the memory addresses to be used from BASIC with the support code loaded starting at Chunk 7 (E000H/87344).

Attribute and other display controls such as inverse are taken from the standard TS2068 System Variables (see Usage Section.)

This component is designed to permit use in normal video mode (Display File 1 only) or, in conjunction with ASC004 - Dual Screen Mode Support, to permit use of Display File 1 and/or Display File 2. The value of the System Variable V10MDD is used to determine which display file is the target of the requested service.

#### Application Services

Name: INIT40 (INIT4B from BASIC)

Input: None

#### Description:

Initializes the internal variables to their default values for 40-column mode (see Usage Section).

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Uses: CLRSCN (CLRSCB from BASIC - parameters to CLSCTL)

Input: Line Count (1-24)  
Starting Line Number (0-23)

From Machine Code: Line Count in Register B  
Starting Line in Register C

From BASIC: Starting Line Number in CLSCTL  
Line Count in CLSCTL + 1

Description:

Clears to background color (PAPER) the designated number of lines, beginning with the Starting Line Number. Line 0 with a count of 24 clears the entire screen. Upon return, the cursor position is at the beginning of the first line cleared.

Output: BC = 0 for successful completion  
BC = 1 invalid parameters  
(Line Number + Line Count < 1 or > 24)

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Uses: SETCUR (SETCUB from BASIC - parameters to LIMCOL)

Input: Line Number (0-23)  
Column Number (0-39) or (0-41)

From Machine Code: Line Number in Register B  
Column Number in Register C

From BASIC: Column Number in LIMCOL  
Line Number in LIMCOL + 1

Description:

Converts the requested position to internal format, determines display file address, and stores the values for use by the next display character operation. Note that once established, the position is updated automatically when a character is displayed so that it is only necessary to set the position when sequential display is not desired.

Output: BC = 0 for successful completion  
BC = 1 for invalid parameters (Line Number > 23,  
Column Number > Line Length-1)

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Uses: WRCMB (WRCMB from BASIC - parameter to DATAB)

Input: Character code for character to be displayed

20H TO 7FH - Std. TS206B Character Set  
80H TO BFH - Std. Graphics Set  
90H TO A4H - User-Defined Graphics Set

From Machine Code: Register A

From BASIC: in DATAB

Description:

Displays character at current cursor position, displaying current attributes and mask. Moves cursor position on to next sequential position. If character would start a new line after BOTLN (see Usage section) and the scroll count (variable SCRLCT) decrements to zero, the character will not be displayed and return will be made with BC = 3 (screen full). If the scroll count does not decrement to zero, the screen will be scrolled up one line using the information in SCRLCT and the new line started at the nexted line.

Note that only the first 6 bits of each byte in the User Defined Graphics set will be transferred to the display file.

Output: BC = 0 for successful completion  
BC = 1 invalid character code  
BC = 3 for screen full

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Name: WRSTRG (WRSTRB from BASIC - String Identifier in @PARAMS)

Inout: Character Code String

From machine code: Address of string in HL  
Count in BC

From BASIC: String Variable Identifier in  
System Variable PARAMS - 2374Y (SCC3M)

Description:

Displays the characters from the string, beginning at the current cursor location and continuing sequentially until the count expires, or "Screen Full" is detected (see WRCHR description and Usage Section on Automatic Scrolling). For the Screen Full condition, the remaining count is stored in the internal variable STRGCT for access by the user.

NOTE: Characters within the string which are outside of the supported range (32 through 164 (20H-A4H)) will be ignored. E.g., BASIC Token codes and control codes embedded in an INPUT string will not be displayed or decoded.

From BASIC, POKE the code for the string variable identifier into @PARAMS prior to invoking WRSTRB, e.g.

```
0005 LET @B="-----string-----"
0010 POKE 2374Y,CODE "a"
0015 IFUSR(WRSTRB)<>0 THEN -----
      (continue)
```

Output: BC = 0 Successful  
BC = 2 BASIC - String not found  
BC = 3 Screen Full - Remaining Count in STRGCT  
(HL=Current Address in String)

-----  
Name: SCROLL (SCRLB from BASIC - parameters to SCRCYL)

Inout: Line Count (1-23)  
Starting Line Number (1-23)

From Machine Code: Line Count in B  
Starting Line in C  
From BASIC: Starting Line in SCRCYL  
Line Count in SCRCYL + 1

Description:

Scrolls the designated number of lines up 1 position, starting at the specified line number and inserts a blank line at the bottom of the scrolled area. Line 1 with a count of 23 will scroll the entire screen up 1 line. Upon return, the cursor position is at the beginning of the inserted blank line.

Note: See Usage Section on "automatic" scrolling.

Output: BC = 0 Successful  
BC = 1 Invalid Parameters  
(Line Number + Line Count < 1 or > 24)

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Name: GTCMAR (GTCMRB from BASIC - parameters to GETCTL)

Input: Line/Column position as for SETCUR

Free Machine Code: Line Number in B  
Column Number in C

From BASIC: Column Number in GETCTL  
Line Number in GETCTL + 1

Description:

Returns in register C of the BC register pair the character code for the character at the designated screen position. If no match against the character set (including the standard and user-defined graphics) is found, zero is returned. (Character code or zero also returned in A.)

Note: Positions "printed" using the DVER technique will return zero if they do not match against any single character.

Output: BC = 0 for no find  
BC = 1 invalid parameters  
BC = character code (20H-46H)

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Name: GETATT (GETATB from BASIC - parameters to GETCTL)

Input: As for GTCMAR

Description:

Returns in register C of the BC register pair the attribute byte for the character at the designated screen position. Note that in 40 column mode the 6-pixel character width may cross attribute byte boundaries in the display file (e.g. the character may have 2 pixels in one byte and 4 in the next). The attribute bytes for these two locations may be different. The value of the attribute byte controlling the starting location of the character will be returned.

Output: BC = 1 for invalid parameters  
BC = attribute byte

Bit 7 - FLASH  
Bit 6 - BRIGHT  
Bit 5  
Bit 4 - PAPER  
Bit 3 /  
Bit 2  
Bit 1 - INK  
Bit 0 /

-----

Name: GETCUR (GETCUB from BASIC)

Input: None

Description:

Returns in the BC register pair and in the BASIC parameter location LINCCL, the current print position (where the next character could be displayed).

Output: B = Line number (0-23)  
C = Column number (0-39) or (0-41)

BASIC: LINCCL - Column number  
LINCCL + 1 - Line number

NOTE: If the last character was printed at Col.39 (41) of Line 23 (last position on the screen), then Col.0/Line 23 will be returned.

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## Usas21

### Memory Usage:

This package of machine code routines includes the following internal variables:

Name	Size	Description
-----	-----	-----
SCRCTL	2	Scroll Control LSB = Starting line number MSB = Number of lines to be scrolled
BOTLN	1	Bottom Line - Line number (0-23) after which test for scroll will be made.
SCRLCT	1	Scroll Count- Number of times + 1 that automatic scroll will be done. When decremented to zero, data will not be displayed and a condition code will be returned to user.
CHTBL	2	Character Table (Base Address-100H)
GRTBL	2	Std.Graphics Character Table (Base-100H)
LINLEN	1	Line Length - (40 or 42 when in 40-Col.Mode)
CURPOS	2	Current Position (Internal Format) LSB = Column Position MSB = Line Position
DFACDR	2	Current Display File Address
MASKB	1	Working Byte - (P FLAG Shifted Right 1) (bit 0 = GVER ) (bit 2 = INVERSE) (bit 4 = INK Complement of PAPER (bit 6 = PAPER Complement of INK
ATTBYT	1	Working Byte - (Copy of ATTR P)
GTINDX	1	'Get' Index - Used by GTCHAR
STRGCT	2	String Count - Contains remainino byte count when EC=3 (Screen Full) is returned from the Write String service WRSTRG (WRSTRB).
MARGIN	1	Margin - Margin Adjust (0/1)
DFBIT	1	Display File Bit - Current Bit Position
ATTMSK	1	Working Byte - (Copy of MASK P)

Initial values set via INIT40 (INIT4B) are as follows:

Variable Name	Value
SCRCTL	1701H
BDTLN	17H
SCRCLT	1H
CMTBL	(Internal to Module)
GRTBL	(Internal to Module)
LINLEN	28H
CLRPDS	1B29H
DFADDR	4000H
GTINDX	80H
STRGCT	0H
MARGIN	1H
DFBIT	7H

The following are the variables used for passing parameters in BASIC and their values as initialized by INIT4B:

Variable Name	Size	Value
DATAB	1	0H
LINCDL	2	0H
CLRCTL	2	1B00H
GETCTL	2	0H

In addition, VIDMDD, PARAMS and other system variables must be available to these routines. At minimum, chunks 0-3 and chunk 7 must be enabled in the Home Bank.

#### Location:

This package can be incorporated into a machine code program within the memory range from chunk 4 through 7, taking into consideration the remapping of certain structures when the second display file is open (Dual Screen Mode only). NOTE: Machine code above RAMTDP is not moved.

#### Registers:

Other than as documented for output values, no claims are made as to preservation of any register contents except for the IY Register which must always contain the value 5C3AH for access to the standard system variables.

#### Automatic Scrolling:

As initialized, test for scrolling will be made when the print position goes to the next line following the bottom line on screen (BDTLN=23=24th line). Condition Code 3 (Screen Full) will be returned since SCRCLT will decrement to zero. If SCRCLT is set to some larger value, then the parameters in SCRCTL will be used to automatically scroll the designated number of lines beginning at the specified line number. As initialized, this will scroll the entire screen up. By performing a POKE or setting the variables BDTLN and SCRCTL to the desired values, automatic scrolling can be done using smaller sections of the screen. When working from BASIC it is recommended that BDTLN be set to line 21 (15H) and SCRCTL+1 be set to 21 (15H) to avoid conflict with the Edit Lines which use the bottom two lines of the screen. Note that once SCRCLT expires, it will be set to 1. If a different value is desired, it must be reinitialized by the user after receiving the "Screen Full" condition.

By setting the SCRCTL variable and invoking the SCROLL (SCRLB) routine any portion of the screen may be scrolled at any time.

#### Margin Control:

In 40-Column Mode, there are actually 42 character positions per line. The variable MARGIN determines the offset of the beginning of the 40 column line from the left side of the screen and has valid offset values of 0 or 1. An offset value of 1 centers the 40 column line on the screen; 0 begins at the extreme left side. The default value is 1. When MARGIN is set to 0, the variable LINLEN can be set to 42 to permit access to the 2 extra print positions. Whenever MARGIN and/or LINLEN are modified, a "Set Cursor" operation should be done to insure the integrity of the print position.

NOTE: Since the different MARGIN values result in different pixel positions for the columns, care must be taken in mixing line length and margin values on the same line.

#### Attribute Control:

Attribute and masking (Inverse/Over) control information will be taken from the system variables ATTR P, MASK P and P FLAG as defined below. These variables contain the "permanent" attribute controls set via the BASIC commands PAPER, INK, BRIGHT, FLASH, INVERSE, and OVER, or by directly writing to the specified locations.

In 40-Column Mode, the 6-pixel character width results in characters crossing attribute byte boundaries in the display file. Every four columns across a line are controlled by three attribute bytes. This constraint must be taken into consideration when mixing attributes within a line. Inverse and Over are applied to individual characters and are therefore not subject to the above limitation.

NAME ----	ADDRESS -----	CONTENTS -----
ATTR P	23693	Bit 7 - FLASH 6 - BRIGHT 5 4 - PAPER 3/ 2 1 - INK 0/
MASK P	23694	SAME FORMAT AS ATTR P. USED FOR "TRANSPARENT" DISPLAY: For each bit that is set to 1, the corresponding information will be taken from the current screen position instead of from ATTRP.
P FLAG	23697	BIT 7 PAPER=COMPLEMENT OF INK 5 INK=COMPLEMENT OF PAPER 3 - INVERSE 1 - OVER (New Characters XOR'd with Old)

#### ADDITIONAL NOTES:

1. All screen operations done by the system ROM (PRINT, LIST Edit line I/O, etc.) work with the standard 8-pixel wide characters.

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003E" 0000
0030" 01
0038" 07
003F" 00

114
119 STRGCT DEPN 0
120
121 MARGIN DDPB 1
122 DPRINT DMPR 1
123 DYTMR DEPE 0
124
125 SUETTL WRITE CHARACTER
126
127 MRCN01
128
129 LD A,(CATAR)
130
131 MRCN01
132
133 CALL LDATTA
134
135
136 MRCN01 CALL LDPOSN
137
138
139 CP SDN
140 JP C,INVRAR
141 CP DASH
142 JP NC,INVRAR
143 PUSH RC
144 CR RDM
145 JR C,MRCM15
146 CP 90H
147 JR C,MRCM11
148 LD BC,(UDGC)
149 DEC B
150 JR SUE
151 MRCM13
152 LD RC,(CATAL)
153 SUE
154 JR MRCM15
155 MRCM12 LD EC,(CMTEL)
156 MRCM15 BA DE,HL
157 LD M,D
158 LD L,A
159 ADD HL,HL
160 ADD HL,HL
161 ADD HL,HL
162 ADD HL,HL
163 ADD HL,HL
164 RDR RC
165 SX DE,HL
166 LD O,C
167 DEC A
168 LD A,(L1NLON)
169 JR NS,MRCM14
170 INC A
171 DEC B
172 LD C,A
173 JR MRCM14
174 INC K
175 CP C
176 PUSH BB
177 CALL Z,TVPULD
178 POP BB
179 MRCM2 PUSH BC
180 PUSH HL
181 LD A,(CHASRE)
182 LD B,-1
183 RRA
184 JR C,MRCM5
185 INC A
186 RRA
187 RRA
188 SEC
189 LD C,A
190 LD A,E
191 EX AR,AR
192 PUSH RC
193 PUSH DE
194 LD IR,B
195 ADD IA,SM
196 LD O,(HL)
197 PUSH HL
198 INC HL
199 MRCM6 LD S,(HL)
200 OR BB,HL
201 LD O,B
202 AND B
203 JR NS,MRCADR
204 LD A,(CORBIT)
205 SUR T
206 NEG
207 LD EC,R3PPH
208 JR I,MGRRI1
209 SCF
210 MERT1 RR B
211 RR C
212 DEC A
213 JR NI,MERT1
214 LD A,M
215 AND R
216 LD M,A
217 LD A,L
218 AND C
219 LD L,A
220 LD A,(C3PRIT)
221 SUB 7
222 NEG
223 PUSH HL
224 LD L,(IX)
225 LD M,(IX+1)
226 LD R,(HL)
227 LD C,B
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000C* P5          225      PUSH  AP          ; SAVE A AND PLGS
000C* 78          226      LD    A,B          ; PIRELS FROM CHAR.SET
000C* 00 PC       227      AND    BPC=         ; LIMIT TO 8 PIRELS
000C* 47          228      LD    8,A          ;
000C* PE          229      POP  AP          ; RESTORE A AND PLGS
000C* E1          230      POP  HL          ; RESTORE SCANS
000C* 20 00       231      JR    1,NORR2      ; NO SHIPT NEEDED
000C* 47          232      AND    A          ; CLEAR CARRY
000C* C0 1E       233      RR    8          ; SHIPT "A" TIMES
000C* C0 19       234      RR    C          ;
000C* 3C          235      DEC  A          ;
000C* 20 P9       236      JR    NZ,NERT2      ;
000C* 7C          237      LD    A,P          ; SCAN FROM OP
000C* A8          238      RDR    0          ; INSERT/COMBINE NEW CHAR.
000C* 07          239      LD    M,B          ;
000C* 70          240      LD    A,L          ;
000C* A9          241      RDR    C          ;
000C* 0P          242      LD    L,A          ; OP RDR CM.SET->HL
000C* CC 7E 02    243      LD    A,(I+2)      ; TEST IF INVERSE ACTIVE
000C* AT          244      AND    A          ;
000C* 28 1A       245      JR    I,NOINVERT    ;
000C* 3A 003E*    246      LD    A,(O+BIT)      ;
000C* 08 07       247      SUB  T          ;
000C* 00 44       248      NEG             ;
000C* 81 PC00     249      LD    BC,B*COOH      ; MASK TO INVERT
000C* 20 0E       250      JR    I,NORR3      ;
000C* A7          251      AND    A          ;
000C* C0 1E       252      RR    8          ; SHIPT MASA "A" TIMES
000C* C0 19       253      RR    C          ;
000C* 3C          254      DEC  A          ;
000C* 20 P9       255      JR    NI,NERT3      ;
000C* 7C          256      LD    A,M          ;
000C* A8          257      RDR    0          ; INVERT CHARACTER
000C* 67          258      LD    M,A          ;
000C* 70          259      LD    A,L          ;
000C* A9          260      RDR    C          ;
000C* 6P          261      LD    L,A          ;
000C* 88          262      NOINVERT OR DE,HL      ; OP ADRS. TO HL-SCAN LINE IN DE
000C* 73          263      LD    (HL),E      ; WRITE SCAN LINE TO OP
000C* E1          264      POP  HL          ; PREVIOUS BYTE
000C* 72          265      LD    (HL),D      ; WRITE SCAN LINE TO OP
000C* 01          266      POP  DE          ; CHAR.SET
000C* C1          267      POP  BC          ; OVER/INVERSE INDICATORS
000C* 08          268      ER    AP,AP*        ; RESTORE SCAN COUNT
000C* 2A          269      INC  M          ; NEXT SCAN IN OP
000C* 13          270      INC  DE          ; NEXT BYTE IN CHAR.PILE
000C* 30          271      DEC  A          ; TEST IF MORE SCANS
000C* C2 009P*    272      JP    NZ,MRCHE      ;
000C* 29          273      MRCW7 DEC  M          ; LAST CHAR.SCAN
000C* C0 0A9*     274      CALL UPDATY      ; UPDATE ATTRIBUTE BYTE
000C* E1          275      POP  HL          ; STARTING ADRS.
000C* C1          276      POP  BC          ; CUESCE POSITION
000C* 00          277      DEC  C          ; ADJUST CURSOR POSITION
000C* 3A 003E*    278      LD    A,(O+BIT)      ; ADJUST BIT POSITION
000C* 06 00       279      SUB  0          ;
000C* 32 003E*    280      LD    (O+BIT),A      ; STORE UPDATED POSITION
000C* 30 0A       281      JR    NC,MPCMR7      ; NEXT CHAR. IN SAME BYTE
000C* C4 08       282      ADD  A,B          ; NEXT CHAR. IN NEXT BYTE
000C* 32 0030*    283      LD    (O+BIT),A      ; STORE ADJUSTED VALUE
000C* 23          284      INC  HL          ; ADJUST OP ADRS.
000C* CC 0A99*    285      MRCW7 CALL STPOSN      ; STORE NEW POSITION
000C* 0E 00       286      GOODRET LD  C,D          ; RETURN BC=D
000C* 00 00       287      ERRRET LD  E,D          ; ENTER HERE WITH C NON-ZERO
000C* C9          288      RET             ;
000C* 0E 01       289      I             ;
000C* 10 P9       290      INVPAR LD  C,1          ; RETURN BC=1 PCE INVALID PARAMETERS
000C* 30 1E       291      JE    ERRRET      ;
000C* 90          292      I             ;
000C* E7          293      TYPULG LD  A,SCR51      ; TEST IF NEW LINE IS OPP SCREEN
000C* 3A 0020*    294      SUB  0          ; A=LINE NO.
000C* 8A          295      LD    0,A          ; SAVE IN D
000C* 02 0A92*    296      LD    A,(O+TLN) ; GET BOTTOM LINE
000C* 5A 002E*    297      CP    D          ;
000C* 50          298      JP    NC,UPOPOSN      ; ON SCREEN - RETURN TO WRITE CHAR.
000C* 20 00       299      LD    6,(3CCLCT)      ; VIA UPDATE AND STORE POSITION
000C* 3E 01       300      DEC  A          ; TEST SCROLL COUNT
000C* 52 002E*    301      JE    NI,TYPUL1      ; DO AUTOMATIC SCROLL USING 3CCLCT
000C* C1          302      LD    A,1          ; REINITIALIZE TO 1
000C* 0E 05       303      LD    (3CPLCT),A      ;
000C* 10 00       304      POP  BC          ;
000C* 32 002E*    305      POP  BC          ; DISCARD RETURN TO WRITE CHAR.
000C* 80 40 0028* 306      LD    C,S          ; RETURN BC=3 FOR SCREEN PULL
000C* C3 018C*    307      JR    ERRRET      ;
000C* 30 00       308      TYPUL1 LD  (3CRLCT),A      ; UPDATE VARIABLE
000C* 80 40 0028* 309      LD    EC,(3CRLCT) ; GET SCROLL CONTROLS (STARTING LINE
000C* C3 018C*    310      JP    SCRLD      ; AND LINE COUNT)
000C* 30 00       311      JSTYL W3TE STRING ; RETURN TO WRITE CHAR. VIA SCROLL
000C* C0          312      I             ;
000C* 30 00       313      I             ; HERE FROM BASIC ENTRY TO
000C* C0          314      I             ; WRITE CHARACTER STRING TO SCREEN.
000C* 30 00       315      I             ; STRING IDENTIFIER (CM.CODE) IN
000C* C0          316      I             ; SYSTEM VARIABLE PARAM AT SCC3H
000C* 30 00       317      MSTRD CALL DEF3TRC ; GET STRING VAR. ADRS. TO HL
000C* C0          318      RET    1          ; NO STRING FOUND OR NULL STRING
000C* 30 00       319      I             ; (CC CONTAINS STATUS)
000C* C0          320      I             ;
000C* 30 00       321      I             ; ENTRY FROM PACKING CODE WITH
000C* C0          322      I             ; ADDRESS OF CHAR.CCODE "STRING"
000C* 30 00       323      I             ; IN HL AND LENGTH IN BC
000C* C0          324      I             ;
000C* 30 00       325      I             ;
000C* C0          326      I             ; GET ATTRIBUTE AND MASA CONTRCLS
000C* 30 00       327      MSTRC CALL LOATTE      ; GET CODE
000C* C0          328      MSTRLP LD  A,(HL)      ; SAVE ADRS. AND
000C* 30 00       329      PUSH  HL          ; COUNT
000C* C0          330      PUSH  BC          ;
000C* C0          331      CALL WPC=01      ; WRITE "A"
000C* 30 00       332      LD    A,C          ; TEST IF CODE
000C* C0          333      OR    0          ;
000C* 30 00       334      JE    NI,M3TR1      ; EXIT IF INVALID CODE OR
000C* C0          335      OR    0          ; IF SCREEN PULL
000C* 30 00       336      JE    NI,M3TR1      ; COUNT
000C* C0          337      MSTR3 POP  BC          ;

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0208* 29 484 ADD HL,HL
020C* 46 485 LD R0,M
020D* 4D 486 LD C0,L
020E* 62 487 LD M0,M
020F* 68 488 LD L0,E
0210* C0 04ED* 489 CALL CALC477
0211* 68 490 DE,HL
0212* 21 PPER 491 LD HL,-TDM
0213* 19 492 ADD HL,DE
0214* 68 493 EX DE,HL
0215* 8C 05 494 LDIR
0216* C5 495 POP BC
0217* 79 496 LD A,C
0218* 80 497 ADD A,0
0219* 30 498 DEC A
021A* 4P 499 LD C,A
021B* 8A D1 49A LD R1,I
021C* 10 4B 49B JR CLOCCE
021D* 4P 49C
021E* 70 49D LD C,A
021F* 91 49E LD C,A
0220* 47 49F LD R0,A
0221* C9 497 PUSH BC
0222* 70 498 LD A,C
0223* 10 02 499 JR LOOP
0224* 00 49A
0225* C0 49B
0226* 00 00 49C
0227* C0 49D
0228* 00 49E
0229* 00 49F
022A* 21 PPER 49A
022B* 10 49B
022C* 00 49C
022D* 00 49D
022E* 00 49E
022F* 00 49F
0230* 00 49A
0231* 00 49B
0232* 21 PPER 49C
0233* 10 49D
0234* 00 49E
0235* 00 49F
0236* 00 49A
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0239* 00 49D
023A* 00 49E
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0240* 00 49E
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024D* 00 49F
024E* 00 49A
024F* 00 49B
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025D* 00 49D
025E* 00 49E
025F* 00 49F
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0262* 00 49C
0263* 00 49D
0264* 00 49E
0265* 00 49F
0266* 00 49A
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026C* 00 49A
026D* 00 49B
026E* 00 49C
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0270* 00 49E
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0299* 00 49D
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0392* 00 49A
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0395* 00 49D
0396* 00 49E
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0398* 00 49A
0399* 00 49B
039A* 00 49C
039B* 00 49D
039C* 00 49E
039D* 00 49F

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0340* 3E 0F      600      LD      R,EPH      ; LOAD CODE FOR GRAPHICS BLOCK
0341* 4F          601      LD      C,R      ; RETURN IN REG. C OF EC PAIR
0342* 06 00      602      LD      R,0      ; AND IN REG. A
0343* 01          603      POP     R      ;
0344* 33 003E*    604      LD      (DP017),A    ; RESTORE DP017
0345* 79          605      LD      A,C      ; CHAS.CODE TO A
0346* C9          606      SET     ;
0347*            607      1
0348*            608      GTCM4  POP     HL      ; PTR. TO CHAS. SET
0349* 11 0006      609      LD      R,R      ; MOVE ON TO NEXT CHARACTER
0350* 19          610      ADD     HL,DE
0351* 90          611      LD      R,L
0352* 94          612      LD      R,M
0353* C1          613      POP     HL      ; DP ADDRESS
0354* 10 90      614      POP     BC      ; CHRS. COUNT
0355*            615      DJNZ    GTCM2    ; DECREMENT CHAS.COUNT AND LOOP AGAIN
0356*            616      1
0357* 3A 003A*     617      LD      A,(GTIND05) ; TEST IF DONE
0358* PE 40      618      CP      R0M      ; TEST IF STD.CHRS.SET
0359* 30 0A      619      JR      NZ,GTCM5    ; TRY GRAPHICS
0360* 57 50 0031* 620      LD      DE,(GRT06L) ; GRAPHICS TABLE
0361* 06 10      621      LD      R,1A      ; NO. OF ENTRIES
0362* 35 90      622      LD      R,R0M
0363* 56 09      623      JR      GTCM1
0364* PE 90      624      GTCM5  CP      R0M      ; TEST IF STD.GRAPHICS
0365* 30 0C      625      JR      NZ,GTCM4    ;
0366* 00 30 5C70 626      LD      DE,(U06) ; TRY USER-DEFINED GRAPHICS
0367* 15          627      DEC     R      ; ADJUST ADDRESS-106H
0368* 06 39      628      LD      R,0,31 ; NO. OF ENTRIES
0369* 3E AS      629      LD      A,R0A0M ; INDEX ADJUST
0370* C3 02P0*    630      JP      GTCM1
0371*            631      1
0372*            632      GTCM6  POP     AP      ; NO MATCH AT ALL
0373* P1          633      LD      (DP017),A    ; RESTORE DP017
0374* 32 003E*    634      LD      C,0      ; AC=6
0375* 40          635      ROR     A      ; AND
0376* 0F          636      SET     ;
0377* C9          637      1
0378*            638      1
0379*            639      1
0380*            640      1
0381*            641      1
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0740*            1000     1

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03P8" 21 0000
03P8" 22 5C7D
0A01" 38 0033"
0R0A" 3C
0A03" 4P
0A0R" 0R 18
0A0B" CC 0A52"
0A0B" C3 015A"

781      LO      HL,0
782      LO      (C00RDS),HL
783      LO      A,(CLINL5N)
784      INC      B
785      LO      C,A
786      LO      B,SCRS1
787      CALL     UPDPOSN
788      JP      G000R5T
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SUBTTL INTERNAL SUB-RTNS.

0R0R" 3A 5CC3
0A11" 8R 1P
0A13" P8 R0
0A13" 5T
0A1R" 2A 3C48
0R10" 7E
0A1A" 80 TP
0A1C" 2E 13
0A1E" 8A
0A1P" 2E 08
0A21" 05
0A22" C0 1720
0A23" 8E
0A28" 01
0A2T" 1E P0
0A28" 23
0A2A" AE
0A28" 25
0A2C" AR
0A20" 25
0A28" 7E
0A2P" 81
0R30" C8

0A31" 05 02
0A33" 08 00
0A35" C8

0A36" 3E 1T
0A38" 88
0A38" 50 0A
0A38" P1
0A3C" C3 013P"
0A3P" 5A 0033"
0A42" 30
0A43" 88
0A44" 55 P3
0A4R" C8

0A47" 3A 0033"
0A4A" 3C
0A48" 81
0A4C" 4P
0R40" 35 18
0A4P" 90
0A30" 4T
CA31" C8

0R52"
0A52" C0 0A5T"
0A55" 1E 42

0R37" C0 0R7E"
0A3A" 3A 0033"
0A30" 3C
0A3E" 91
0A3P" P3
0A40" 5P
0A41" 5T
0A42" 83
0A43" CE 3P
0A45" C8 3P
0A47" 5P
0A48" 3A 0050"
0A4E" 83
0A4C" 5P
0A60" 18 80
0A6P" 18 0T
0A70" 1E 0T
0A72" P1
0A73" 8E 03
0A75" 2E 02
0R7T" 3P
0A75" 30
0A78" 43
0A7R" 32 805E"

781      LO      HL,0
782      LO      (C00RDS),HL
783      LO      A,(CLINL5N)
784      INC      B
785      LO      C,A
786      LO      B,SCRS1
787      CALL     UPDPOSN
788      JP      G000R5T
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998      I
999      I
1000     I

GET3TRG LO      A,(PARAM8)
AND      1PH
OR      40H
LO      0,A
LO      HL,(VARS)
GTSTR1 LO      A,(HL)
AND      0TPH
JR      2,NCSTRG
CP      0
JR      2,GTSTR2
PUSH     DE
CALL     RSCL5N
EX      05,HL
POP      DE
JR      GTSTR1
GT3T82 INC      HL
LO      C,(HL)
INC      HL
LO      B,(HL)
INC      HL
LO      A,B
OR      C
RST
NDSTRG LO      C,2
LO      B,0
RST
T3TPAR LO      A,25
CP      B
JR      NC,T3TPR1
PARSRR POP      AP
JP      INVPAR
T3TPR1 LO      A,(CLINL5N)
DEC      A
CP      C
JR      C,PARSRR
RST
CONVPM LO      A,(CLINL5N)
INC      A
SUB      C
LO      C,A
LO      B,SCRS1
SUB      B
LO      B,4
RST
UPDPOSN1
CALL     CALCPDS
JR      3TPCSN
CALCPDS CALL     LMBU
LO      A,(CLINL5N)
INC      A
SUB      C
PUSH     AP
LO      5,A
ADD      A,A
ADD      A,5
SRL      A
SRL      A
LO      5,A
LO      A,(MARGIN)
ADD      A,B
LO      B,A
LO      0,0
LO      HL,05
LO      B,7
POP      AP
AND      3
LO      2,CALCP2
JR      B,R
CEC      A
ADD      A,5
LO      C,(OP81T),A

```

220

```

0508' 32 003A' 1023 LO (MASKR),A 1
0511' 01 1024 POP AP
0512' C9 1025 RET
1026 1
1027 1 1 RTN. USED BY GTCHAR TO PUT
1028 1 1 4 PIXELS FROM OP 1M R REG.BITS 7-2
1029 1 1 FOR WATCHING AGAINST CHAR.SET
1030 1
0513' P9 1031 GTC2EC PUSH ML 1 SAVE OP ADDR.
0514' 4A 1032 LO R,(ML) 1 SCAN LINE 000H 00
0515' 23 1033 INC ML 1 NEXT BYTE
0516' 4E 1034 GTC2B1 LO C,(ML) 1 SC CONTAINS CHAR.AT OPEIT
0517' 3A 003E' 1035 LO A,(OPBIT)
0518' D6 07 1036 SUB 7
0519' EC 4A 1037 NEG 1 A=NO.OP BITS OPPER IN RC
0520' 26 07 1038 JR 2,GTC2B2 1 STARTS AT BIT 7
0521' CE 11 1039 GTC5MPT RL C
0522' CR 10 1040 RL E 1 SHIFT 4 PIXELS FOR CHARACTER
0523' 30 1041 DEC A 1 TO E. BITS 7-2
0524' 20 P9 1042 JR NI,GTC5MPT
0525' E1 1043 GTC2E2 POP ML 1
0526' C9 1044 RET 1 CHAR.IN E, BITS 7-2
1045 1
1046
1047 INCLUDE CMSTRO
1048 SUEYTL CHAR.SET FOR 40/80 COL.MODES
1049
1050 1
1051 1 Get patterns for characters on the TV screen!
1052 1 character with code = 1a at offsets 8x (top line) to 8x+7 (bottom line)
1053 1 0 = white, 1 = black, no merging between character spaces either
1054 1 vertically or horizontally
1055
0529' 00 1056 CHRST defb 00000000b 1 code 20 apoc
0530' 00 1057 defb 00000000b
0531' 00 1058 defb 00000000b
0532' 00 1059 defb 00000000b
0533' 00 1060 defb 00000000b
0534' 00 1061 defb 00000000b
0535' 00 1062 defb 00000000b

```

See APPENDIX C-2 for Character Set

AOL - ASC 003 40-COL.MODE SUPPORT CPIO/11 version 10.36.14  
 CHAP.SET FOR 40/80 COL.MODES CMSTRO.SRC

1A-Mar-84 12143123

Symbol	RefP (P = definition R = write <blank> = read)	Symbol	RefP (P = definition R = write <blank> = read)	Symbol	RefP (P = definition R = write <blank> = read)	Symbol	RefP (P = definition R = write <blank> = read)
ROTLM	0020	ATTSTR	0039	ATTMSK	003P	ATTRSP	SCRO
CALCP2	04T9	CHRSET	0429	CALCAT	04ED	CALCA1	04PR
CLRCTL	000C	CLRSRO	024P	CHRST	0529	CHYRL	002P
CLRSC1	0270	CLRSC2	02EB	CLPSCB	000E 1M	CLPSCN	0253 1M
CLRSC2	02CP	CLPSKT	02CR	CLRSC3	02RC	CLRSCA	02RP
0	Reserved	0ATAR	0000	COMVPM	044T	COOROS	SC70
0RRRET	013C	0ATAR	0000	OPADR3	0036	OPR17	003E
GETCTL	0019	GETAB0	03TE	GETATE	001E 1M	GETATT	03E2 1M
GOORRE	013A	GETCUB	0021 1M	GETCUR	039C 1M	GETC1	0384
GTCHAR	020R 1M	GRRLK	0224	GRPHST	0729	GRPST	0R29
GTCH22	02PA	GTCH80	020T	GTCHRB	001R 1M	GTCH1	02P0
GTCHA	034B	GTCH23	0310	GTCH3	0315	GTCH31	0318
GTC2E1	0516	GTCH5	036T	GTCH6	03TT	GTCSMP	0520
M	Reserved	GTC2E2	0527	GTINOK	003A	GTSTR1	0419
L	Reserved	INITAR	0025 1M	INIT40	0389 1M	INSOEL	010A
LNBU	047E	LOATTR	04PD	LOQDSM	04A1	LINCGL	000T
MARGIN	0030	LOOP	010E	LOOPI	01E1	LOOPI	01E9
MERT3	0176	MASKR	003E	MASKSP	00RE	MERT1	00E8
NOSTRG	0431	NOIMVE	0113	NORR1	00C5	NORR2	00E0
PSPLAG	SC91	NOXJR	00CR	PARAM	SCC3	PARERR	043B
SCRLE0	01A2	RECLEM	1720	SCIM17	1701	SCRCTL	002R
SCR51	001B	SCPLCY	002E	SCRKLT	0201	SCRLO	01RC
SPARE0	0011	SETCBO	0192	SETCUB	0009 1M	SETCUR	0196 1M
STELR	022C	SPARE1	0013	SPARE2	0015	SPARE3	001T
TESTAO	01CC	STELR0	0230	STELK1	0231	STPOSM	049E
UOC	SC7B	TSTPAR	0436	YSTPRI	043P	TVPULQ	0143
UPDAT3	04EB	UPDAT7	04A9	UPDAT0	04RC	UPDAT1	04CC
WRCHRE	0001 1M	UOPOS	0452	VER5	SC4B	V10M00	SCC2
WRCH12	00A0	WRCH4T	0137	WRCH0	0040	WRCH01	0046
WRCH3	0099	WRCH13	00T1	WRCH1A	00ET	WRCH15	00EE
WRSTR8	0004 1M	WRCH5	009P	WRCH6	00AE	WRCHT	0120
		WRSTRG	0160 1M	WRSTRQ	0169	WRSTR3	01TA
							WRSTK1
							01E3

No errors detected

CropR reference listing (CREF version 4.7)

Symbol	RefP (P = definition R = write <blank> = read)	Symbol	RefP (P = definition R = write <blank> = read)	Symbol	RefP (P = definition R = write <blank> = read)	Symbol	RefP (P = definition R = write <blank> = read)
ATTBTT	111P	SET	955	101E8			
ATTMSK	1190	PST	1020R				
ATTR_P	4E8	1017					
ROTLM	950	296	T67R				
CALCA1	100R	10100					
CALCAT	459	574	730	952	1001P		
CALCP2	902	9050					
CALC00	622	729	ETS	8830			
CHRSET	400	103	779				
CHRST	40	10560					
CHTEL	1030	152	423	780s			
CLINIT	390	774					
CLRCTL	730	511	775R				
CLRSRO	T4	5100					
CLRSC0	AT1	523	527P				
CLRSC1	5400	597					
CLRSC2	5490						
CLRSC3	5520	404					
CLRSCA	5540	573					
CLRSC5	565A						
CLRSCT	54E	4000					

CLR3C8	31	760					
CLR3CN	28	9130					
CLR3X7	595	5980					
CDMWPM	388	821	728	766	8828		
CDOR01	470	7928					
CURP03	1040	743	9330	938			
DE708	840	125	7898				
DP4083	1080	9340	939				
DP817	1180	201	117	246	278	2808	2838
		619	6848	7148	726	7358	9088
		993	978	9848	9918	1035	
		291	308				
ERRR0T	2878	2220					
GBT490	84	840					
GBT478	32	840					
GBT477	29	7240					
GBTCL	747	793	7590				
GBTCS0	89	7410					
GBTCTL	810	619	712	7888	7898		
GBTCS8	32	890					
GBTCSR	29	7428					
GBT37R	319	8060					
GDOR08	2460	379	392	926	799		
GRBLK	419	4730					
GRPHST	410	104	783				
GRP3T	63	10200					
GR78L	1040	149	709	7828			
GTC281	10300						
GTC282	1038	10430					
GTC28C	633	856	10330				
GTCM3	6260	703	718				
GTCM2	6280	689					
GTCM22	6318						
GTCM23	644	6470					
GTCM3	640	6500					
GTCM31	6520	667					
GTCM32	677	679	6810				
GTCM4	666	669	666	6880			
GTCM5	689	7040					
GTCM6	709	7130					
GTCMAR	28	6170					
GTCM80	83	6120					
GTCM88	32	830					
GTCMHP	30380	1042					
G71NCR	1110	6260	642	674	697		
G757R1	8110	820					
G737R2	919	9210					
INI740	34	87	7630				
INI749	34	870					
IN308L	4160						
INVPAR	137	139	2900	389	388	390	937
		529	522	843			
LD67TR	128	327	927	10130			
LDPO5M	132	9370					
LINC0L	690	364	7990	7860	7878		
LINLEN	1030	164	398	532	745	7710	793
		846	862	884			
LN9U	401	535	883	9100			
LOOP	4198	479					
LOOP8	4230	438					
LOCP1	4280						
MRRGTH	1170	7730	394				
M8348	1080	178	939	10238			
M838_P	490	1018					
M8371	2070	210					
M8372	2310	236					
M8373	2320	259					
M83NV8	245	2620					
M88R1	205	2110					
M88R2	231	2370					
M88R3	230	2560					
M837RG	813	8100					
M88DR	200	2170					
PAR6M6	320	806					
PAR88R	8440	840					
P_PL6G	900	1021					
R8CL6M	430	817					
SCIN17	380	764					
3CRCTL	880	310	378	7698			
3CRL0	312	391	3940				
3C0L8	31	810					
3CRL80	91	3760					
3CRLC7	1000	300	3048	3099	7698		
3CRL87	442	4490	900				
3CROLL	28	3800					
3CRS1	370	293	383	929	796	888	912
387C80	72	3420					
387C88	31	720					
387CUR	28	3660					
SPAR80	770						
SPAR81	780						
SPAR82	780						
SPAR83	800						
SPAR84	860						
ST8LK	407	4810					
ST8LK0	4844	496					
ST8LK1	4850						
STPD3M	289	876	328				
STRGCT	1150	1400	7778	7788			
TE3760	4030	444	303				

TSTPAR	367	418	725	8418
TSTPR1	863	8468		
TVFUL1	302	3098		
TVFULQ	173	2938		
UDG	468	145	706	
UPDAT0	9618	985		
UPDAT1	966	969	9718	
UPDAT2	972	975	9778	
UPDAT3	980	9908		
UPDAT7	274	9438		
UPDP05	298	369	599	797 8738
VARS	438	817		
V10H00	518	926		
WRCH0	87	1238		
WRCH01	1328	331		
WRCH11	164	1498		
WRCH12	142	1928		
WRCH13	148	151	1538	
WRCH16	165	1708		
WRCH15	169	1718		
WRCH2	1768			
WRCH3	181	1838		
WRCH5	1888	272		
WRCH6	1968			
WRCH7	2738			
WRCH8	28	1278		
WRCH8	31	878		
WRCH8T	281	2858		
WRSTLP	3288	342		
WRST80	68	3198		
WRST83	3368	347		
WRST88	31	888		
WRST86	28	3278		
WRST81	334	3458		

## APPENDIX C-4

### DUAL SCREEN MODE

Name: Dual Screen Mode Support

Description:

This component provides support to the application programmer for using the dual screen capability of the TS 2068. The services include opening/closing the second display file (moving the machine stack, OS RAM routines and BASIC structures), position control, clear screen and scroll screen services, and display of characters. In addition, services are provided to control which display file is active at the screen and which is the target of the current screen operation, as well as a Copy Service and an Exchange Service to transfer screen data/attributes within or between the two display files. "Get" services are provided to return the current display position, the character code for a specified display position, or the attribute byte for a specified display position.

For ease of use from BASIC, status is returned in the BC register pair, usually zero for successful completion and designated non-zero values for other conditions. The interface from BASIC is by means of PCKE'ing the input parameters to designated RAM locations prior to invoking the service via the USR function. See page 4 of the code listing for the memory addresses to be used from BASIC with the support code loaded starting at Chunk 7 (E000H/3734H).

Attribute and other display controls such as Inverse are taken from the standard TS2063 System Variables (see Usage Section.) The system variable VIMODE is used to determine which display file is the target of the requested service.

#### DECLARED SERVICES

Name: SETMODE (SETMODE from BASIC - parameter to VIMODE)

Input:

MDC:	0	=	Normal (Display File 1 only)
	128(80H)	=	DF1 Active (DF2 Open)
	1	=	DF2 Active
	4	=	Screen Operations to DF1
	5	=	Screen Operations to DF2

From Machine Code: Mode Parameter in A

From BASIC: Mode Parameter in VIMODE

Description:

Mode values of 0, 128 or 1 will cause an update of the video mode hardware and the opening or closing of the second display file as needed. This involves determining if there is enough free RAM to open the second display file and if so, moving the BASIC structures and machine language variables area up and the UDG (User-Defined Graphics) area down to make space for the machine stack and DS RAM routines at the top of memory. The second display file is cleared to zeroes. The affected system and internal variables are updated or initialized (see Usage Section). When returning to Mode 0 the structures are returned to their normal locations. In these modes the designated Display File is active at the screen and is the target of all screen operations.

The mode values of 4 and 5 do not affect the hardware, but are used to redirect screen operations to the desired display file. This permits building of a display file prior to activating it at the screen.

The screen position is set to "home" (Line 0/Col.0) whenever SETMDGE (SETMD3) is executed.

NOTE: All screen operations of the TS 2058 System BASIC Interpreter including program entry, system messages,

LIST, PRINT, PLDT, DRAW, etc. work only in Display File 1.

Name: CLRSCN (CLRSCB from BASIC - parameters to CLSCTL)

Input: Line Count (1-24)  
Starting Line Number (0-23)

From Machine Code: Line Count in Register B  
Starting Line in Register C

From BASIC: Starting Line Number in CLSCTL  
Line Count in CLSCTL + 1

Description:

Clears to background color (PAPER) the designated number of lines, beginning with the Starting Line Number. Line 0 with a count of 24 clears the entire screen. Upon return, the cursor position is at the beginning of the first line cleared.

Output: BC = 0 for successful completion  
BC = 1 invalid parameters  
(Line Number + Line Count < 1 or > 24)

Name: SETCUR (SETCUE from BASIC - parameters to LINCOL)

Input: Line Number (0-23)  
Column Number (0-31)

From Machine Code: Line Number in Register B  
Column Number in Register C

From BASIC: Column Number in LINCOL  
Line Number in LINCOL + 1

Description:

Converts the requested position to internal format, determines display file address, and stores the values for use by the next display character operation. Note that once established, the position is updated automatically when a character is displayed so that it is only necessary to set the position when sequential display is not desired.

NOTE: The screen position used and maintained by these service routines is independent of that used by the System ROM (S POSN).

Output: BC = 0 for successful completion  
BC = 1 for invalid parameters (Line Number > 23,  
Column Number > Line Length-1)



-----

Name: WRCHR (WRCHB from BASIC - parameter to DATAB)

Input: Character code for character to be displayed

20H TO 7FH - Std. TS2068 Character Set  
80H TO BFH - Std. Graphics Set  
90H TO A4H - User-Defined Graphics Set

From Machine Code: Register A

From BASIC: in DATAB

Description:

Displays character at current cursor position, applying current attributes and mask. Moves cursor position on to next sequential position. If character would start a new line after BDTLM (see Usage section) and the scroll count (variable SCRLCT) decrements to zero, the character will not be displayed and return will be made with BC = 3 (screen full). If the scroll count does not decrement to zero, the screen will be scrolled up one line using the information in SCRLCT and the new line started at the vacated line.

Output: BC = 0 for successful completion  
BC = 1 invalid character code  
BC = 3 for screen full

-----

Name: WRSTRG (WRSTRB from BASIC - String Identifier in PARAMS)

Input: Character Code String

From machine code: Address of string in HL  
Count in BC

From BASIC: String Variable Identifier in  
System Variable PARAMS - 23747 (SCC3H)

Description:

Displays the characters from the string, beginning at the current cursor location and continuing sequentially until the count expires, or "Screen Full" is detected (see WRCHR description and Usage Section on Automatic Scrolling). For the Screen Full condition, the remaining count is stored in the internal variable STRGCT for access by the user.

NOTE: Characters within the string which are outside of the supported range (32 through 164 (20H-A4H)) will be ignored. E.g., BASIC Token codes and control codes embedded in an INPUT string will not be displayed or decoded.

From BASIC, PDKE the code for the string variable identifier into PARAMS prior to invoking WRSTRG, e.g.

```
0005 LET @s="-----string-----"
0010 PDKE 23747,CODE "s"
0015 IFUSR(WRSTRB)<>0 THEN -----
      (continues)
```

Output: BC = 0 Successful  
BC = 2 BASIC - String not found  
BC = 3 Screen Full - Remaining Count in STRGCT  
(HL=Current Address in String)

-----

Name: SCRDL (SCRDLB from BASIC - parameters to SCRCTL)

Input: Line Count (1-23)  
Starting Line Number (1-23)

From Machine Code: Line Count in B

Starting Line in C

From BASIC: Starting Line in SCRCTL

Line Count in SCRCTL + 1

Description:

Scrolls the designated number of lines up 1 position, starting at the specified line number and inserts a blank line at the bottom of the scrolled area. Line 1 with a count of 23 will scroll the entire screen up 1 line. Upon return, the cursor position is at the beginning of the inserted blank line.

Note: See Usage Section on 'automatic' scrolling.

Output: BC = 0 Successful  
BC = 1 Invalid Parameters  
(Line Number + Line Count < 1 or > 24)

Name: GTCHAR (GTCHARB from BASIC - parameters to GETCTL)

Input: Line/Column position as for SETCUR

Free Machine Code: Line Number in B  
Column Number in C

From BASIC: Column Number in GETCTL  
Line Number in GETCTL + 1

Description:

Returns in register C of the BC register pair the character code for the character at the designated screen position. If no match against the character set (including the standard and user-defined graphics) is found, zero is returned. (Character code of zero also returned in A.)

Note: Positions "printed" using the OVER technique will return zero if they do not match against any single character.

Output: BC = 0 for no find  
BC = 1 invalid parameters  
BC = character code (20h-A6h)

Name: GETATT (GETATB from BASIC - parameters to GETCTL)

Input: As for GTCHAR

Description:

Returns in register C of the BC register pair the attribute byte for the character at the designated screen position.

Output: BC = 1 for invalid parameters  
BC = attribute byte

Bit 7	-	PLASH
Bit 6	-	BRIGHT
Bit 5		
Bit 4	-	PAPER
Bit 3	/	
Bit 2		
Bit 1	-	INK
Bit 0	/	

Name: GETCUR (GETCUB from BASIC)

Input: None

Description:

Returns in the BC register pair and in the BASIC parameter location LINCOL, the current print position (where the next character would be displayed).

Output: B = Line number (0-23)  
C = Column number (0-31)

BASIC: LINCOL - Column number  
LINCOL + 1 - Line number

NOTE: If the last character was printed at Col.31 of Line 23 (last position on the screen), then Col.0/Line 23 will be returned.

-----  
Name: COPYSC (COPYSB from BASIC - string identifier in PARAMS)

Input:

Source Line (0-23)  
Destination Line (0-23)  
Source Display File (1 or 2)  
Destination Display File (1 or 2)  
Line Count (1-24)

Free Machine Code:   Source Line in B  
                          Destination Line in C  
                          Source DF in D  
                          Dest.DF in E  
                          Line Count in A

Free BASIC: Parameters in string variable, separated by commas.  
String Identifier in System Variable PARAMS - 23747  
(SCC3M)

Description:

Copies the designated portion of the source display file to the designated portion of the destination display file. The source and destination may be in the same display file. During a multi-line copy, source and destination operands are accessed in a top to bottom fashion; therefore, in certain cases of operand overlap the operation may be destructive, i.e. the source data may be modified before it is copied. To get the desired result, it may be necessary to copy individual lines.

From BASIC, the following example would copy lines 8 thru 20 from DF1 to lines 0 thru 12 in DF2:

```
LET os="8,0,1,2,13"           8=Starting Line  
                              0=Destination Line  
                              1=Source DF  
                              2=Dest.DF  
                              13=Line Count  
  
POKE 23747,CCODE "o"        (String Identifier to PARAMS)  
LET cc=USR (COPYSB)  
IF cc<>0 THEN.....
```

Output: BC=0 Successful  
          BC=1 Invalid Parameters (DF<>1 or 2; Line No.<>0-23;  
  Line Count<>1-24  
          BC=2 BASIC - String Not Found

-----  
Name: EXCHSC (EXCHSB from BASIC)

Input:

Source "1" Display File (1 or 2)  
Source "1" Line (0-23)  
Source "2" Display File (1 or 2)  
Source "2" Line (0-23)  
Line Count (1-24)

Free Machine Code:   Source "1" DF in B  
                          Source "1" Line in C  
                          Source "2" DF in D  
                          Source "2" Line in E  
                          Line Count in A

Free BASIC: Parameters in string variable, separated by commas, in the order Source "1" Line, Source "2" Line, Source "1" DF, Source "2" DF, Line Count. String Identifier in System Variable PARAMS - 23747 (SCC3M)

Description:

Exchanges the specified number of lines between Source "1" and Source "2". Both Sources may be in the same Display File. The exchange is done by XOR'ing the two sources together three times. The operation is done a line at a time, accessing the two sources in a top to bottom fashion, e.g. to exchange lines 8-12 in DF1 with lines 16-20 in DF2 would first exchange line 8 (DF1) with line 16 (DF2), then line 9 with line 17 and proceed in this fashion through exchange of line 12 with line 20.

From BASIC, the following example would exchange lines 8 thru 4 in DF1 with lines 8-12 in DF2:

```
LET os="0,8,1,2,5"           0=Source "1" Line  
                              8=Source "2" Line  
                              1=Source "1" DF  
                              2=Source "2" DF  
                              5=Line Count  
  
POKE 23747,CCODE "o"        (String Identifier to PARAMS)  
LET cc=USR (EXCHSB)  
IF cc<>0 THEN.....
```

Output:       BC=0 Successful  
          BC=1 Invalid Parameters (DF<>1 or 2; Line No.<>0-23;  
  Line Count <>1-24  
          BC=2 BASIC - String Not Found

# UASBBI

## Memory Usage:

This package of machine code routines includes the following internal variables:

Name	Size	Description
SCRCTL	2	Scroll Control LSB = Starting line number MSB = Number of lines to be scrolled
BOTLN	1	Bottom Line - Line number (0-23) after which test for scroll will be made.
SCRLCT	1	Scroll Count- Number of times + 1 that automatic scroll will be done. When decremented to zero, data will not be displayed and a condition code will be returned to user.
CHTBL	2	Character Table (Base Address=100H)
GRtbl	2	Std.Graphics Character Table (Base=100H)
LINLEN	1	Line Length
CURPOS	2	Current Position (Internal Format) LSB = Column Position MSB = Line Position
DFADDR	2	Current Display File Address
MASKB	1	Working Byte - (P FLAG Shifted Right 1) (bit 0 = OVER ) (bit 2 = INVERSE) (bit 4 = INK Complement of PAPER (bit 6 = PAPER Complement of INK
ATTBYT	1	Working Byte - (Copy of ATTR P)
GTINDX	1	'Get' Index - Used by GETCHAR
STRGCT	2	String Count - Contains remaining byte count when bC=3 (Screen Full) is returned from the Write String service WASTRG (WRSTOE).
ATTMSK	1	Working Byte - (Copy of MASK 0)

Initial values set via SETMODE (SETHOB) when the second display file is first opened are as follows:

Variable Name	Value
SCRCTL	17D1H
BOTLN	17H
SCRLCT	1H
CHTBL	3DDDH
GRtbl	(Internal to Module)
LINLEN	2DH
CURPOS	1B21H
DFADDR	4D00H/6D00H
GTINDX	80H
STRGCT	0H

The following are the variables used for passing parameters in BASIC. The 0 indicates those initialized by SETMODE when the second display file is first opened:

Variable Name	Size	Value
0 DATAR	1	0H
0 LINCL	2	0H
0 CLRCTL	2	1BDDH
0 GETCTL	2	0H
YIMODE	1	

In addition, YIMODE, PARAMS and other system variables must be available to these routines. At minimum, chunks 0-3 and chunk 7 must be enabled in the Memory Bank.

## Location:

This package can be incorporated into a machine code program within the memory range from chunk 4 through 7, taking into consideration the reweaving of certain structures when the second display file is open.

NOTE: Machine code above RAMTOP is not moved.

#### Registers:

Other than as documented for output values, no claims are made as to preservation of any register contents except for the IV Register which must always contain the value SC3AH for access to the standard system variables.

#### Automatic Scrolling:

As initialized, test for scrolling will be made when the print position goes to the next line following the bottom line on screen (BDTLM=23=24th line). Condition Code 3 (Screen Pull) will be returned since SCRLCT will decrement to zero. If SCRLCT is set to some larger value, then the parameters in SCRCTL will be used to automatically scroll the designated number of lines beginning at the specified line number. As initialized, this will scroll the entire screen up. By performing a PGKE or setting the variables BDTLM and SCRCTL to the desired values, automatic scrolling can be done using smaller sections of the screen. When working from BASIC it is recommended that BDTLM be set to Line 21 (15H) and SCRCTL+1 be set to 21 (15H) to avoid conflict with the Edit Line which uses the bottom two lines of the screen. Note that once SCRLCT expires, it will be set to 1. If a different value is desired, it must be reinitialized by the user after receiving the "Screen Pull" condition.

By setting the SCRCTL variable and invoking the SCROLL (SCRLB) routine any portion of the screen may be scrolled at any time.

#### Attribute Control:

Attribute and masking (Inverse/Over) control information will be taken from the system variables ATTR\_P, MASK\_P and P\_FLAG as defined below. These variables contain the "permanent" attribute controls set via the BASIC commands PAPER, INK, BRIGHT, PLASH, INVERSE, and OVER, or by directly writing to the specified locations.

NAME ----	ADDRESS -----	CONTENTS -----
ATTR_P	23693	Bit 7 - PLASH 6 - BRIGHT 5 4 - PAPER 3/ 2 1 - INK 0/
MASK_P	23694	SAME FORMAT AS ATTR_P. USED FOR "TRANSPARENT" DISPLAY: For each bit that is set to 1, the corresponding information will be taken from the current screen position instead of from ATTR_P.
P_FLAG	2369T	BIT 7 PAPER=COMPLEMENT OF INK 5 INK=COMPLEMENT OF PAPER 3 - INVERSE 1 - OVER (New Characters ADR'd with Old)

#### Using System RDM Screen Services:

The following technique can be used to build screens using the BASIC commands such as PRINT, PLOT, CIRCLE, and DRAW or other System RDM routines and get them into the second display file:

1. Set Mode B0 (opens 2nd DP)
2. Build Screen in DP1 using System or ADL Services
3. Copy DP1 to DP2 using CDPISC (CDPTSB)
4. Set Mode 1 (DP2 to Screen)
5. Set Mode 4 (DP2 to Screen/Operations to DP1)
6. Build Screen in DP1 using System or ADL Services
7. Set Mode B0 (DP1 to Screen)
8. Go to Step 3

The switching of the screen from one display file to the other is transparent to the viewer where the files contain the same data.

# M A R K I N G

When the second display file is active at the screen, any system message such as SCROLL?, error reports, or use of the INPUT command will not be visible and the system may appear to be "hung". Indiscriminately pressing keys may be filling the "invisible" Edit Line with "garbage" thus further aggravating the situation. The DM E88 facility can be used to intercept some of these situations and set the view mode to use Display File 1 at the screen in order that the message can be seen and responded to. Otherwise, it is necessary to key in and execute from the Edit Line without being able to see it.

-----

6DL - 43C 084 DUAL SCREEN MODE SUPPORT CB240/11 version 10.36.14 I6-00r-84 12167130  
VERSION LEVEL CONTROL 1001.58C

```

1 NAME 6DL - 43C 084 DUAL SCREEN MODE SUPPORT
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0010	SCRII	ECU	24	1 24 LINES
1701	SCINI7	ECU	1701M	1 ICRQLL CONTROL INITIALIZATION
1000	CLINIT	ECU	1000M	1 CLEAR ICRQLL C7L INIT.
1C00	CHRIE7	ECU	1C00M	1 RCM CHAR.TABLE-100M
01E0	GRPHI7	ECU	((GRPI7)-100M)	1 I70.GRAPHICS CHARI.
0E0E	CMNGV10	ECU	0E0EM	1 ROUTINE IN RCM EXTENSION
1720	RECLEM	ECU	1720M	1 ROUTINE IN RCM RCM
30P9	ININT	ECU	30P9M	1 ROUTINE IN RCM RCM
3193	PP2A	ECU	3193M	1 ROUTINE IN RCM RCM
00PP	HREXP7	ECU	00PPM	1 RCM RCM EXT. SELECT POST (BIT 7)
00P4	QXMSPT	ECU	00P4M	1 DOCK HORIZONTAL SELECT POST
5C48	VARS	ECU	5C48M	1 IYIEM VARIABLE
5C50	CH_ADD	ECU	5C50M	1
5C65	ITRMO	ECU	5C65M	1
5C78	UDG	ECU	5C78M	1
5C70	COOBDI	ECU	5C70M	1
5C80	AT7R_P	ECU	5C80M	1
5C8E	HASK_P	ECU	5C8EM	1
5C91	P_FLAG	ECU	5C91M	1
5C82	RAMTOP	ECU	5C82M	1
5C84	PRAMT	ECU	5C84M	1
5CC2	VIOHOD	ECU	5CC2M	1
5CCI	PAGAM9	ECU	5CC3M	1 (LOCATION 23747)
6840	DRIVES	ECU	6840M	1
12C0	INSERTSI	ECU	7800M-DRIVEI	1
0840	MOVEII	ECU	DRIVES-4000M	1
77C0	DEI77	ECU	DRIVES-4000M	1
77C0	PIX	ECU	DEI77-4000M	1
1000	PIX70L	ECU	1000M	1

```

02          SUBTTL INPUTS AND ENTRIES
03
04
05
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09
10          ***PARAMETER INPUTS AND ENTRY POINTS FROM BASIC**
11
12
13          WITH
14          POCULO LOADED
15          AT 87544CE000H3
16          GOCIMAL ADDRESS
17          -----
18
19 0000" 20          9A  DATA          DEPH  20H      1 DATA BYTE FROM BASIC          ST344
20 0001" C1 004A"    9T  WRCHRB1       JP    WRCHNO  1 ENTRY TO WRITE CHARACTER          ST348
21 0004" C3 008B"    99  WRSTRB1       JP    WRSTRQ  1 ENTRY TO WRITE STRING          ST348
22          1 (SEE PARAMS ABOVE)
23 0007" 0000        100 LINCBL        DEPH  0          1 SET CURSOR PARAMETERS
24          101          1 LINCBL=CCL. (0-31)          ST351
25          102          1 LINCBL=LINE CO-23)          ST352
26 0009" C3 0117"    103  SETCUR1       JP    SETCBO  1 ENTRY TO SET CURSOR POSN.          ST353
27 000C" 1000        104  CLRCTL        DEPH  1000H    1 CLEAR SCREEN CONTROLS
28          105          1 CLRCTL=STARTING LINE NO.          ST354
29          106          1 CLRCTL=LINE COUNT          ST355
30 000E" C5 0504"    107  CLRSCL1       JP    CLRSBO  1 ENTRY TO CLEAR SCREEN          ST358
31 0011" 0000        108  SPARE0       DEPH  0          1 NOT USED (ALIGNMENT ONLY)
32 0013" 0000        109  SPARE1       DEPH  0
33 0015" 0000        110  SPARE2       DEPH  0
34 0017" 0000        111  SPARE3       DEPH  0
35 0019" 0000        112  GETCTL        DEPH  0          1 "GET" CONTROL FROM BASIC          ST359
36          113          1 FORMAT IS AS FOR LINCBL
37 001B" C5 028C"    114  OTCHRB1       JP    GTCMB0  1 ENTRY TO GET CHARACTER          ST371
38 001E" C3 020C"    115  GETATB1       JP    GETAB0  1 ENTRY TO GET ATTRIBUTE          ST374
39 0021" C3 02P1"    116  GETCUR1       JP    GETCBO  1 ENTRY TO GET CURSOR POSN.          ST377
40 0024" 00          117  VIMCOB        DEPH  0          1 VIDEO MODE CONTROL          ST380
41 0021" C3 050B"    118  SETHDB1       JP    STMDB0  1 ENTRY TO SET VIDEO MODE          ST381
42 002B" 1701        119  SCRCTL        DEPH  1705H    1 SCRCL=STARTING LINE NO.          ST384
43          120          1 SCRCTL=LINE COUNT          ST385
44 002A" C3 0127"    121  SCRLB1       JP    SCRLB0  1 ENTRY TO SCROLL          ST388
45          122          1
46          123          1 ICOPY50
47          124          1 (SEE NEXT PAGE)
48          125          1 ENTRY TO COPY FROM OP          ST400
49          126          1 (SEE NEXT PAGE)
50          127          1 EXCH50
51          128          1 (SEE NEXT PAGE)
52          129          1
53
54          SUBTTL OTHER VARIABLES
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61 0020" 17          130  BOTLW        DEPH  17H      1 BOTTOM LINE (LINE AFTER
62          131          1 WHICH SCREEN IS CONSIDERED
63          132          1 FULL. AUTOMATIC SCROLL
64          133          1 WILL BE DONE IF SCRLCT
65          134          1 DOES NOT DECREMENT TO 0.
66 0020" 01          135  SCRLCT        DEPH  1          1 SCROLL COUNT - 1 PLUS NO.          ST400
67          136          1 OF TIMES AUTOMATIC SCROLL
68          137          1 WILL BE DONE.
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81 002P" 3C00        147  CNTBL        DEPH  CMBEST  1 ADDRESS OF CHARACTER TABLE          ST395
82          148          1 (DEFAULTS TO TS2008 SYSTEM
83          149          1 ROM)
84 0031" 0000"        150  GRBL        DEPH  GRPHST  1 ADDRESS OF STD.GRAPHICS TOL.          ST395
85 0033" 20          151  LINLEN        DEPH  20H      1 LINE LENGTH (32)          ST398
86 0034" 1025        152  CURPOS        DEPH  1025H    1 CURRENT POSITION (INTERNAL          ST398
87          153          1 FORMAT)
88 0036" 4000        154  DPA000        DEPH  A000H    1 CURRENT DISPLAY FILE ADDR.          ST398
89 0038" 00          155  MASA0        DEPH  0          1 MASK TO BE APPLIED TO
90          156          1 DISPLAY CHARACTERS
91 0039" 00          157  ATTB07       DEPH  0H      1 CURRENT ATTRIBUTES          ST401
92          158          1
93 003A" 00          159  GTIN0R        DEPH  00H      1 INCR FOR ADJUSTING          ST402
94          160          1 CH.CODE IN GTCHAR
95 003B" 0000        161  STRGCT        DEPH  0          1 REMAINING COUNT FROM WRSTRG
96 003D" 0000        162          1 WHEN "SCREEN PULL"          ST403
97 003P" 00          163  BPA004        DEPH  0          1
98          164          1 ATTRIBUTE MASA          ST407
99          165          1
100 0040" C8 050B"    166  CDPT50        JP    CDPT50  1 BASIC ENTRY TO COPY OP          ST408
101 0043" C5 0A05"    167  EXCH50        JP    EXCH50  1 BASIC ENTRY TO EXCHANGE OP          ST411
102
103          SUBTTL WRITE CHARACTER
104
105
106
107
108
109
110 0046"          171          1 WRCHNO:
111          172          1 HERE FROM BASIC ENTRY TO DISPLAY THE
112          173          1 CHARACTER WHOSE CODE IS IN "DATA0"
113 0048" 3A 0000"    174          1 LB 0.(DATA0)
114          175          1
115 0049"          176          1 WRCHAB:
116          177          1 ENTRY WITH CODE IN A
117 0049" CD 0042"    177          1 CALL LDATYR          1 GET ATTRIBUTE AND MASA CONTROLS
118          178          1
119          179          1 ENTRY TO USE ATTRIBUTE VALUES IN
120          180          1 INTERNAL VARIABLES WITHOUT RELOADING
121 004C" C0 0050"    181  WRCH01: CALL LDOPSN          1 LOAD REGISTERS FOR CURRENT POSITION
122          182          1 OC=CURSOR POSITION FROM "CURPCS"
123          183          1 HL=DISPLAY FILE ADDRESS FROM "DPA000"
124          184          1 A=CODE FOR CHAR. TO BE DISPLAYED
125          185          1 TEST VALID RANGE
126          186          1 < 20H
127          187          1
128          188          1 > 40H
129          189          1 SAVE CURSOR POSITION
130 005A" P0 00        190          1 TEST IF ASCII PRINTABLE (20H-7FH)
131 005C" 00 15        191          1 YES
132 005B" P0 70        192          1 TEST IF STD.GRAPHICS(00H-0FH)
133 0060" 30 07        193          1
134 0062" 0C 40 0C70  194          1 NO-DEPENDED GRAPHICS(90H-A4H)

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0088 05 193 DBC 0 ; ADJUST ADDRESS-100H
0089 04 TO 198 SUB TOM ; ADJUST FOR INDEX INTO TABLE
0090 18 0C 199 JR WRCM11 ; ADDRESS OF GRAPHICS TABLE
0091 0C 48 0051 19A LD BC,(CGRTO) ; ADJUST FOR INDEX INTO TABLE
0092 04 00 199 SUB 60H
0093 1A 04 201 WRCM12 LD BC,(CMTOL) ; CHARACTER TABLE
0094 0C 40 002P 202 WRCM13 LD 00,HL ; DE=POSH,IN DISPLAY FILE
0095 00 203 LD M,0 ; CODE IS INDEX INTO TABLE
0096 20 00 204 LD L,6
0097 29 205 ADD HL,HL
0098 29 206 ADD HL,HL
0099 29 207 ADD HL,HL
0100 09 208 ADD HL,BC
0101 C1 209 POP BC
0102 00 210 OR 00,HL
0103 79 211 LD A,C
0104 50 212 DBC A
0105 1A 0033 213 LD A,(CLINLEN)
0106 20 05 214 JR NZ,WRCM14
0107 3C 215 INC A
0108 03 216 DEC B
0109 4P 217 LD C,A
0110 10 01 218 JR WRCM15
0111 3C 219 INC A
0112 09 220 CP C
0113 03 221 PUSH 00
0114 CC 00CA 222 CALL 2,TVPULO
0115 01 223 POP 00
0116 224
0117 225 WRCM2 PUSH BC
0118 05 226 PUSH HL
0119 1A 005A 227 LD A,(CHASKE)
0120 04 PP 228 LD R,-1
0121 1P 229 RRA
0122 30 01 230 JR C,WRCM5
0123 04 231 INC B
0124 1P 232 WRCM3 RRA
0125 1P 233 RRA
0126 9P 234 SBC A,A
0127 4P 235 LD C,A
0128 50 0A 236 LD A,R
0129 0A 237 AND A
0130 0A 238 OR 00,HL
0131 00 239 WRCM5 EX AP,AP
0132 1A 240 LD A,(DB)
0133 00 241 AND R
0134 0A 242 ROR (HL)
0135 0A 243 XOR C
0136 12 244 LD (DB),6
0137 00 245 OR AP,AP
0138 14 246 INC 0
0139 23 247 INC HL
0140 5C 248 WRC 0
0141 20 PA 249 JR NZ,WRCM5
0142 15 250 WRCM7 CDB 0
0143 00 251 OR 00,HL
0144 00 252 CALL UPDATY
0145 00 253 POP HL
0146 00 254 POP BC
0147 00 255 POP C
0148 25 256 INC HL
0149 00 0055 257 WRCM8 CALL STPOSH
0150 00 258 GCJORDY LO C,0
0151 00 259 BRRAET LO A,0
0152 00 260 RET
0153 261
0154 00 262 INVPAR LO C,1
0155 10 PP 263 JR ERRRET
0156 264
0157 30 10 265 TVPULC LO A,SCRSI
0158 90 266 SUB B
0159 57 267 LD 0,A
0160 3A 0020 268 LD A,(CDBTN) ; GET BOTTOM LINE
0161 0A 269 CP 0
0162 02 0020 270 JP NC,UPDPOSH
0163 271
0164 5A 0020 272 LD A,(SCRLCT)
0165 30 273 DEC A
0166 20 00 274 JR NZ,TVPUL1
0167 50 01 275 LD A,1
0168 32 0020 276 LD (SCRLCT),6
0169 00 277 POP BC
0170 00 278 POP BC
0171 00 00 279 LD C,3
0172 10 00 280 BRRET
0173 32 0020 281 TVPUL1 LD (SCRLCT),A
0174 00 282 LD BC,(SCRLCT)
0175 103
0176 283 JP SCALO
0177 284
0178 285 ;
0179 286 SUBTL WRITE STRING
0180 287
0181 288 ;
0182 289 ;
0183 290 ;
0184 291 ;
0185 292 WRCM9 CALL GOTSTRG
0186 00 0000 293 RET 2
0187 00 294
0188 295
0189 296 ;
0190 297 ;
0191 298 ;
0192 299 ;
0193 300 WRCM10 CALL LOATYR
0194 70 301 WRCM11 LD A,(HL)
0195 00 302 OR HL
0196 00 303 PUSH BC
0197 00 304 WRCM12 CALL WRCM10
0198 00 305 LD A,C
0199 00 306 OR 0
0200 20 09 307 JR NZ,WRCM13
0201 00 308
0202 C1 309 WRCM13 POP 0C

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0100* E1
0101* 23
0102* 0E
0103* 78
0104* E1
0109* 20 EE
0107* C9

0508* T9
0109* P8 01
010E* 28 P2
0100* E1
010E* 22 00SE*
0111* E1
0112* 0E 05
011A* 06 00
011A* C9

POSITION CONTROL 1005.3RC

0317*
0317* 00 40 0007*
0318*
031E* CC 040C*
031E* CC 0A10*
0321* CD 0629*
0324* C3 008P*

0327*
0327* EC 40 0020*
0328*

032E* T8
032C* AT
0320* CA 00CA*
0330* E1
0331* PE 01
0333* DA 00CA*
0334* PE 19
033E* D2 00CA*
0338* CO 0141*
033E* C3 008P*

0341* C5
0342* 3E 18
034A* 91
0348* A7
0346* 3A 0033*
0349* 3C
034A* AP
034E* CE 065A*
034E* C1
034P* E1
0350* C5
0351* T0
0352* 87
0351* 28 3C
0355* 07
0356* 07
0357* 07
0358* AP
0359* 1E 0E
035E* 91
035E* 8E
0350* 18 4A
035P* TE
0360* 06 00
0361* C3
0365* A7
0364* 0E 0E
0366* TE
0367* C1
0368* 0P
036E* 0P
036A* 0P
0368* AP
036C* 0A 00
0368* E8
036P* 21 PF80
0372* 19
0375* 88
0374* E9
0375* E0 80
0377* 81
0378* 24
0379* C1
037A* 00
0378* 20 89
0370* C1
0378* 78
037P* AT
0380* 28 04
0382* 2E 00
0384* 18 C5
038A* C1
0387* 01
0388* C9
03E9* A8
038A* 26 00
038C* 29
0380* 29
0388* 29
038P* 29
0390* 29

510 POP HL
511 INC HL
512 DEC BC
513 LO A,B
514 CR C
513 JR NZ,WRSTLP
516 RET
517
51E NRSTRS LO A,C
51E CP I
520 JR 3,WRSTR3
521 POP HL
522 LO C3TRGCTI,HL
523 POP HL
524 LO C,3
525 NRSTR2 LO B,0
526 RET
527

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531 SETC801
532
533 LO BC,CL3NCGL
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3UE7TL POSITION CONTROL
1
SETC801
1
LD BC,CL3NCGL
1
SETCUP1
CALL TSTAP
CALL CONVPF
CALL UPDPOSN
JP GOODRET
1
3UE7TL 3CROLL
1
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3CRL001
1
LD BC,C3CRCYLI
1
3CROLL1
LD A,B
AND 8
JP I,INVPAR
BCD 8,C
CP 1
JP C,INVPAR
CP 25
JP NC,3MVPAR
CALL SCRL0
JP GOODRET
1
3CPLO PUSH EC
LD A,ICRS2
3UE C
LD B,A
LD B,CLINLENI
INC B
LD C,A
CALL LNRW
POP BC
PUSH HL
PUSH EC
LD A,L
AND A
JR I,STELK
RLCB
RLCA
RLCA
LD C,A
LD A,B
3UE C
CP E
JR C,GRBLX
LD A,B
LD B,0
PUSH PC
LD B,A
LD C,E
LD A,B
3C
PUSH BC
RRCA
RRCA
LD C,A
LD B,0
BX 08,HL
LD HL,-20H
ADD HL,DB
BX 08,HL
PUSH HL
LDI
POP HL
INC H
POP BC
DEC C
JR NZ,LODPO
POP JC
LD A,R
AND A
JR 2,SCRLYT
LD L,0
JR TEITAD
POP BC
POP DE
PUSH BC
LD L,R
LD M,0
ADD HL,HL
ADD HL,HL
ADD HL,HL
ADD HL,HL
ADD HL,HL

1 HERE FROM B433C ENTRY. PARAMETERS
1 3N L3NCCL
1 ENTRY WITH LINE/COL. IN BC REG.
1 TEST PARAMETER1 FOR VALIDITY
1 CONVERT TO INTERNAL FORMAT
1 CALCULATE AND STORE POSITION
1 RETURN BC=0
1 HERE FROM B433C ENTRY TO 3CROLL
1 SCREEN
1 GET CONTROL 3MPO.
1 HERE WITH CONTROL3 3N BC
1 E=NO. OF LINES
1 C=STARTING LINE NO.
1 TEST VAL303TY
1
1 ERROR 3P COUNT=0
1 ERROR 3P 0=C<1
1 ERROR IF E<C>24
1 DO ICROLL
1 RETURN BC=0
1 GET STARTING LINE IN INTERNAL FORMAT
1 COL. 0
1
1 HAVE ITAPTING AORS.
1 SAVE ORIG.BC FOR EXIT
1 TEST IF A7 ITA77 OP BLCKX
1 YES
1 GET NO. OF LINES THIS BLOCK
1 TEST AGAINST TOTAL LINES
1 TOTAL GREATER THAN THIS BLOCK
1 REMAINING COUNT
1 B=LINES THIS BLOCK
1 C=ICAN COUNT
1 SAVE ICAN COUNT
1 CALCULATE NO. OF BYTES
1 BC=520# OF LINES
1 GET AORS. OF PREV. LINE
1 TO 08
1 DISPLAY FILE AORI.
1 00 MOVE
1 NEXT SCAN LINE
1 SCAN COUNT
1 MORE ICANS
1 RESIDUAL LINE COUNT
1 DONE
1 START OF NEXT BLOCK
1 00 REMAINING LINE(S)
1 ORIG.AC
1 ITARTING AORS. TO DE
1 SAVE GRIG.BC
1 NO. OF LINES SCROLLED
1
1 52=NO. OF LINES=
1 NO. OF ATTRIBUTES BYTES
1 TO 88 ICROLLED
1
1

```

[illegible]

235

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028E" 20 0A
02C0" 80 38 0031"
02C4" 04 10
02C6" 3E 90
02CE" 18 AT
02CA" PE 90
02CC" 20 08
02CE" RD 38 3CTB
0202" 15
0203" 06 15
0205" 3E 45
0207" 18 9E
0209" 48
020A" AP
020B" C9

020C" 80 4E 0015"

0280" C0 060C"
02E3" C0 0610"
02E6" C0 0A20"
02E9" CC 0492"
02EC" 78
02ED" 48
02EF" 26 00
02F0" C9

02F5"
02F1"
02F5" 80 4E 003A"
02F9" C0 0E10"
02FE" 1A 0055"
02FB" 85
02FC" 20 0E
02FE" 08 00
0300" 78
0301" 5C
0302" 47
0303" PE 18
0303" SR 02
0307" 84 17
0309" EC 45 0007"
0300" C9

030E"
030E" 36 0024"
0311"
0315" 47
0312" 47
0313" 2E 15
0313" PE 80
0317" 2E 11
0319" PE 81
031E" 2E 00
0310" PE 84
031P" CA 05BA"
0322" PE 01
0324" CA 07BA"
0327" C3 08C4"
0324" 3A 5CC2"
0320" 47
032E" 20 3A
0330" 80
0335" C0 000P"

0334" 21 1701
0337" 22 002E"
033A" 3E 17
033C" 32 0020"
033P" 5E 01
0345" 32 002E"
0344" 5E 20
0346" 52 0033"
0349" 21 1800
034C" 22 00DC"
034P" AP
0350" 32 003B"
0351" 52 003C"
0356" 21 5C00
0355" 22 002P"
035C" 21 05E0"
033P" 22 0051"

0362" 32 000B"
0363" 52 0007"
036E" 32 000B"
036E" 52 0019"
036E" 32 0014"

0375" 21 12C0
0374" 15 0B40
0377" 19
0378" EC 5E 5C65
037C" 19
0370" 8A 05B3"
0380" 85 38 5CB2
0384" AT
0385" BC 52
03ET" C2 0353"
038A" 78
0385" CC 03P3"
03E8" CG 03C0"

452      JR      N2,GTCH3      I TRY GRAPHICS
453      LO      0E,(GRTBL)      I STD GRAPHICS TABLE
454      LO      8,56             I NO. OF ENTRIES
455      LO      A,90M           I INDEX
456      JR      GTCH1           I
457      CP      90M             I TEST IF STD GRAPHICS
458      JR      N2,GTCH6         I DONE IF NOT
459      LO      0E,(UOG)         I TRY USER-DEFINED GRAPHICS AREA
460      DEC      0               I ADJUST ADDRESS-100M
461      LO      E,21             I NO OF ENTRIES
462      LO      A,0A3M           I INDEX
463      JR      GTCH1           I
464      LO      C,8              I HERE WHEN NO MATCH ANYWHERE
465      XOR      A               I SET EC AND A=ZERO
466      SET
467
468      I GET ATTRIBUTE BYTE
469      I HERE FROM BASIC ENTRY TO GET ATTRIBUTE
470
471      GETARDI LO BC,(GBTCTL)
472      I
473      I HERE WITH LIN/COL IN BC
474
475      GETATT: CALL TSPHAR      I TEST PARAMETERS
476      CALL CCWPHM             I CONVERT TO INTERNAL FORMAT
477      CALL CALCPDS            I CALCULATE OF POSITION
478      CALL CALCATT            I CALCULATE ATTRIBUTE POSITION
479      LO      A,(ML)          I ATTRIBUTE BYTE TO A
480      LO      C,A             I PASS BACK IN BC
481      LO      E,0
482      RET
483
484      I GET CURSOR POSITION
485      I HERE FROM BASIC ENTRY
486
487      GETCURI: LO EC,(CURPOS)   I GET INTERNAL POSITION
488      CALL CCWPHM             I CONVERT TO USER FORMAT
489      LO      A,(LINLEN)
490      CP      C               I TEST IF END OF LINE(=32)
491      JR      NI,GETCS         I NO
492      LO      C,0             I HERE POSN, START OF NEXT LINE
493      LO      A,E
494      SMC      A              I BUMP TO NEXT LINE
495      LO      E,A
496      CP      24              I TEST IF OFF SCREEN
497      JR      C,GETCI         I NO
498      LO      E,25            I POSN. IS AT BOTTOM LINE
499      GETCI: LO (LINCOL),BC    I STORE FOR BASIC PROGRAM
500      RET                    I VALUES ALSO IN BC
501
502      I
503      SUE7TL VIDEO MODE CONTROL
504
505      I
506      STNG00: I HERE FROM BASIC ENTRY WITH MODE IN
507      I "VIMODE"
508      LD      4,(VIMODE)
509
510      I
511      SETMODE: I ENTRY WITH MODE IN 4
512      E
513      LO      E,4             I MODE TO 0
514      AND      4
515      JR      2,SETM00        I TEST FOR VALIDITY
516      CP      80M             I
517      JR      2,SETMODE       I
518      CP      1
519      JR      2,SETM00        I
520      CP      4
521      JP      2,PRPV           I HANDLE 4 AND 3
522      CP      8
523      JP      2,PPPV           I
524      JP      INVPAR          I INVALID PARAMETERS
525      LO      4,(VIDMOD)      I TEST CURRENT MODE
526      AND      4
527      JR      NI,SETM02       I OP2 OPEN
528      OR      0               I CURRENT MODE=0 - TEST IF NEW MODE=0
529      JP      2,GO00REY       I NO ACTION
530
531      I
532      SETNGI: I INITIAL OPENING OF 2ND OP
533      LO      ML,SCINST       I INITIALIZE VARIABLES
534      LO      (SCRCTL),ML      I SCROLL CONTROL
535      LO      A,25             I
536      LO      (BOTLN),A        I BOTTOM LINE
537      LO      A,1             I
538      LO      (SCRCLT),A       I SCROLL COUNT
539      LO      A,32             I
540      LO      (LIMLEN),A       I LINE LENGTH
541      LO      ML,CLIN17        I
542      LO      (CLRCTL),ML      I CLEAR SCREEN CONTROL
543      XOR      A
544      LO      (STRECT),A       I WRITPG REM LINING COUNT
545      LO      (STRECT+1),A      I
546      LO      ML,CHRSET        I STD.CHAR.SET
547      LO      (CHYBL),ML       I
548      LO      ML,GRPMST        I STD. GRAPHICS SET
549      LO      (GRTBL),ML       I
550      I
551      I INITIALIZE BASIC INPUTS
552      LO      (DATA8),A         I DATA BYTE
553      LO      (LINCOL),A        I COLUMN
554      LO      (LINCOL+1),A      I LINE
555      LO      (GETCTL),A        I "GET" COLUMN
556      LO      (GETCTL+1),A      I "GET" LINE
557      I
558      I TEST IF ENOUGH MEMORY
559      LO      ML,INSTR752
560      LO      0E,MOVBS2
561      ADD      ML,0B           I ML=TOTAL ROOM NEEDED
562      LO      0B,(STREND)
563      ADD      ML,DE           I ADD MEMORY IN USE
564      JP      C,EATNRM         I NOT ENOUGH MEMORY TO OPEN OP2
565      LO      0E,CRAHTOP
566      AND      A
567      SEC
568      LO      ML,0B
569      JP      NC,EXTNRM        I NOT ENOUGH MEMORY
570      LO      A,R             I MODE TO A
571      ENDBRT I UNABLE ROM BRT
572      CALL GETVAL             I MAX VALUE TO 51 VIDMOD VALUE TO C

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0430* 01      879  C0P0B1 PDP      HL      ; RESTORE CH_000
0437* 32 0C00  880  LD      CCH_ADDS,HL
043A* C3 00C4* 881  JP      INVPAR      ; RETURN 0C=1 PDB INVALID
                                ; PARAMETER(S)
                                ;
043D*          882  ;
                                ;
043D*          883  C0PTSC1 ;
                                ;
043D* 07      884  BND      0
043E* CA 00C4* 885  JP      S,INVPAR
043F* 08 19    886  CP      33
0440* 03 00C6* 887  JP      NC,INVPAR
0440* 03      888  PUSH    AP
0447* C0 05A8* 889  CALL    CTADRS
044A* 0A 00C4* 890  JP      C,INVPAR
044B* 03      891  PUSH    HL
044B* 42      892  LD      0,C
044C* 73      893  LD      0,0
044D* C0 05A8* 894  CALL    CTADRS
044E* 0A 00C4* 895  JP      C,INVPAR
044F* 01      896  PDP     08
0457* 08      897  04      02,HL
0458* F1      898  PDP     AP
0459* 67      899  LD      0,A
045A* 05      900  PUSH    HL
045B* 05      901  PUSH    08
045C* C3      902  PUSH    0C
045D* 70      903  C0PT51 LD      A,L
045E* 07      904  RLCA
045F* 07      905  RLCA
0460* 07      906  RLCA
0461* 4P      907  LD      C,A
0461* 3E 08    908  LD      0,0
0462* 91      909  SUB     C
0463* 4P      910  LD      C,A
0464* C3      911  PUSH    0C
0465* 0E      912  CP      0
0466* 5E 71    913  JR      C,C0PT55
0467* 7E      914  LD      0,0
0468* 07      915  RLCA
046C* 01      916  RLCA
046D* 07      917  RLCA
046E* 4P      918  LD      C,A
046F* 3E 08    919  LD      0,0
0471* 91      920  SUB     C
0472* C1      921  PDP     0C
0473* 6P      922  LD      C,A
047A* 08      923  CP      0
0475* 3E 79    924  JR      C,C0PT51
                                ;
                                ;
0477* 7E      925  ;
                                ;
0478* 0A 08    926  C0PT52 LD      A,0
047A* C3      927  LD      0,0
047B* 47      928  C0PT53 PUSH    0C
047C* 0E 08    929  LD      0,A
047D* 7E      930  LD      0,C
047E* C3      931  C0PT54 LD      A,0
047F* 4P      932  PUSH    0C
0480* EA 07    933  AND     7
0482* 0P      934  RRCA
0483* 0P      935  RRCA
0484* 4P      936  RRCA
0485* 0A 00    937  LD      C,A
0486* 47      938  LD      0,0
0487* 20 02    939  AND     0
0488* 0A 01    940  JB      02,C0PT41
0489* 05      941  LD      0,1
048A* EC 00    942  C0PT41 PUSH    HL
048B* 05      943  PUSH    0E
048C* 01      944  PDP     0E
048D* 01      945  PDP     HL
048E* 14      946  INC     D
048F* 24      947  INC     H
0490* C1      948  PDP     R6
0491* 00      949  DEC     C
0492* 20 08    950  JB      02,C0PT54
0493* 7B      951  LD      A,0
0494* C1      952  PDP     0C
0495* 4P      953  LD      C,A
0496* 7B      954  LD      0,A
0497* 20 08    955  AND     1
0498* C1      956  JB      2,C0PT47
0499* 4P      957  PUSH    0C
049A* 7B      958  LD      A,C
049B* 4P      959  LD      Y
049C* 7B      960  AND     Y
049D* 4P      961  AND     1
049E* 1E 21    962  JB      2,C0PT47
049F* C3      963  PUSH    0C
04A0* 79      964  LD      A,C
04A1* EA 07    965  AND     Y
04A2* 0P      966  RRCA
04A3* 0P      967  RRCA
04A4* 0P      968  RRCA
04A5* 4P      969  LD      C,A
04A6* 0P      970  LD      0,0
04A7* 08 00    971  LD      0,0
04A8* 47      972  AND     A
04A9* 20 01    973  JB      02,C0PT41
04AA* 0A 01    974  LD      0,1
04AB* 25      975  DEC     H
04AC* 09      976  ADD     HL,0C
04AD* 5E 70    977  LD      A,70H
04AE* 44      978  AND     H
04AF* 07      979  LD      H,A
04B0* 08      980  PUSH    HL
04B1* 15      981  DEC     D
04B2* EA      982  EX      DE,HL
04B3* 09      983  ADD     HL,0C
04B4* 5E 70    984  LD      A,70H
04B5* 44      985  AND     H
04B6* 07      986  LD      H,A
04B7* 01      987  PDP     DE,HL
04B8* 08      988  PDP     0C
04B9* C1      989  JR      C0PT51
04CA* 1E 98    990  C0PT47 PDP     0C
04CB* C1      991  LD      L,0
04CC* 0E      ;
                                ;
                                ; NO. OP LINES
                                ; REMAINING LINES
                                ; SAVE REMAINING LINE COUNT
                                ; NO. OP LINES
                                ; SCAN COUNT
                                ; NO. OP LINES
                                ; SAVE SCAN COUNT
                                ;
                                ; BC=SI = NO. OP LINES
                                ; (C=0 IF 206 PDB 9 LINES)
                                ; TEST IF 0
                                ;
                                ; SET TO 256
                                ; SAVE ADDRESSES
                                ;
                                ; COPY "BC" BYTES
                                ; RESTORE ADDRESSES
                                ;
                                ; ADJUST FOR NEXT SCAN LINE
                                ; (>100H)
                                ; SCAN COUNT
                                ; DECREMENT SCAN COUNT
                                ; NEXT SCAN ROW
                                ; SAVE # OF LINES JUST MOVED
                                ; RESIDUAL LINE COUNT
                                ;
                                ; DONE
                                ; B=REMAINING LINE COUNT
                                ;
                                ; BC = NO. OP BYTES JUST MOVED
                                ; ADJUST ADDRESSES TO START
                                ; OP NEXT LINE IN EACH FILE
                                ;
                                ; SAVE ADJUSTED SOURCE ADDRS
                                ;
                                ; DEST. ADDRS. TO HL
                                ;
                                ; SOURCE ADDRS. TO DE
                                ; HL=SOURCE DE=DEST.
                                ;
                                ; B=REMAINING LINE COUNT
                                ; ORIG.LINE COUNT
                                ; CALCULATE # OF ATTRIBUTE BYTES

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04C4* 2A GO          992      LO      M,B
04C5* 19            993      R00    HL,HL
04C6* 19            994      R00    HL,HL
04C7* 29            995      R00    HL,HL
04C8* 29            996      R00    HL,HL
04C9* 19            997      R00    HL,HL
04CA* 19            998      LO      A,M
04CB* 44            999      LO      C,L
04CC* 40            1000     POP     HL
04CD* E1            1001     CRLL    CRLCMT
04CE* CO 0A92*      1002     ER      GE,HL
04CF* E1            1003     POP     HL
04D0* CO 0A92*      1004     CRLL    CALCMT
04D1* E0 E0         1005     LOIA    A,M
04D2* C3 00E0*      1006     JP      GO00ART
04D3* 1A P4         1007     I
04D4* A1            1008     COPT33 LO 4,C
04D5* YE            1009     LO      R,E
04D6* 07            1010     KLCA    C,R
04D7* 01            1011     RLCH    C
04D8* 07            1012     ALCA    C,R
04D9* 4P            1013     LO      C
04DA* EE OE         1014     LO      A,E
04DB* 91            1015     JUE    C
04DC* 4P            1016     LO      C,R
04DD* A6            1017     CP      E
04DE* EE OE         1018     JE      C,COPT37
04DF* C1            1019     POP     6G
04E0* 7E            1020     COPT3A LO R,R
04E1* 91            1021     JUV    C
04E2* 4T            1022     LO      6,A
04E3* CE            1023     PUSH    EC
04E4* 19            1024     LO      A,C
04E5* 1E EE         1025     JR      COPT33
04E6* 79            1026     COPT37 LO A,C
04E7* C1            1027     POP     EC
04E8* 4P            1028     LO      C,A
04E9* 1A P4         1029     JR      COPT3A
04EA* 1A P4         1030     I
04EB* 1A P4         1031     EUATTL  EACHRNGE SCAREM
04EC* 1A P4         1032     I
04ED* 1A P4         1033     I
04EE* 1A P4         1034     I
04EF* 1A P4         1035     I
04F0* 1A P4         1036     I
04F1* 1A P4         1037     EACHNO
04F2* 1A P4         1038     I
04F3* 1A P4         1039     I
04F4* 1A P4         1040     LO      HL,(CM,ADD)
04F5* 1A P4         1041     PUSH    HL
04F6* 1A P4         1042     CRLL    GET3TNG
04F7* 1A P4         1043     JR      HL,EXCH01
04F8* 1A P4         1044     POP     HL
04F9* 1A P4         1045     LO      (CM,ADD),HL
04FA* 1A P4         1046     RET
04FB* 1A P4         1047     I
04FC* 1A P4         1048     EXCH01 LO (CM,ADD),HL
04FD* 1A P4         1049     CALL    GETNG
04FE* 1A P4         1050     LO      (COPTMK),A
04FF* 1A P4         1051     CALL    GETNG
0500* 1A P4         1052     LO      (COPTMK+2),A
0501* 1A P4         1053     CALL    GETNG
0502* 1A P4         1054     LO      (COPTMK+3),A
0503* 1A P4         1055     CALL    GETNG
0504* 1A P4         1056     LO      (COPTMK+4),A
0505* 1A P4         1057     CRLL    GETNG
0506* 1A P4         1058     POP     HL
0507* 1A P4         1059     LO      (CM,ADD),HL
0508* 1A P4         1060     LO      0G,(COPTMK)
0509* 1A P4         1061     LO      0G,(COPTMK+2)
0510* 1A P4         1062     I
0511* 1A P4         1063     I
0512* 1A P4         1064     EACHMSC:
0513* 1A P4         1065     I
0514* 1A P4         1066     I
0515* 1A P4         1067     I
0516* 1A P4         1068     I
0517* 1A P4         1069     I
0518* 1A P4         1070     I
0519* 1A P4         1071     I
0520* 1A P4         1072     I
0521* 1A P4         1073     I
0522* 1A P4         1074     I
0523* 1A P4         1075     I
0524* 1A P4         1076     I
0525* 1A P4         1077     I
0526* 1A P4         1078     I
0527* 1A P4         1079     I
0528* 1A P4         1080     I
0529* 1A P4         1081     I
0530* 1A P4         1082     I
0531* 1A P4         1083     I
0532* 1A P4         1084     I
0533* 1A P4         1085     I
0534* 1A P4         1086     I
0535* 1A P4         1087     I
0536* 1A P4         1088     I
0537* 1A P4         1089     I
0538* 1A P4         1090     I
0539* 1A P4         1091     I
0540* 1A P4         1092     I
0541* 1A P4         1093     I
0542* 1A P4         1094     I
0543* 1A P4         1095     I
0544* 1A P4         1096     I
0545* 1A P4         1097     I
0546* 1A P4         1098     I
0547* 1A P4         1099     I
0548* 1A P4         1100     I
0549* 1A P4         1101     I
0550* 1A P4         1102     I
0551* 1A P4         1103     I
0552* 1A P4         1104     I
0553* 1A P4         1105     I
0554* 1A P4         1106     I
0555* 1A P4         1107     I
0556* 1A P4         1108     I
0557* 1A P4         1109     I
0558* 1A P4         1110     I
0559* 1A P4         1111     I
0560* 1A P4         1112     I
0561* 1A P4         1113     I
0562* 1A P4         1114     I
0563* 1A P4         1115     I
0564* 1A P4         1116     I
0565* 1A P4         1117     I
0566* 1A P4         1118     I
0567* 1A P4         1119     I
0568* 1A P4         1120     I
0569* 1A P4         1121     I
0570* 1A P4         1122     I
0571* 1A P4         1123     I
0572* 1A P4         1124     I
0573* 1A P4         1125     I
0574* 1A P4         1126     I
0575* 1A P4         1127     I
0576* 1A P4         1128     I
0577* 1A P4         1129     I
0578* 1A P4         1130     I
0579* 1A P4         1131     I
0580* 1A P4         1132     I
0581* 1A P4         1133     I
0582* 1A P4         1134     I
0583* 1A P4         1135     I
0584* 1A P4         1136     I
0585* 1A P4         1137     I
0586* 1A P4         1138     I
0587* 1A P4         1139     I
0588* 1A P4         1140     I
0589* 1A P4         1141     I
0590* 1A P4         1142     I
0591* 1A P4         1143     I
0592* 1A P4         1144     I
0593* 1A P4         1145     I
0594* 1A P4         1146     I
0595* 1A P4         1147     I
0596* 1A P4         1148     I
0597* 1A P4         1149     I
0598* 1A P4         1150     I
0599* 1A P4         1151     I
0600* 1A P4         1152     I
0601* 1A P4         1153     I
0602* 1A P4         1154     I
0603* 1A P4         1155     I
0604* 1A P4         1156     I
0605* 1A P4         1157     I
0606* 1A P4         1158     I
0607* 1A P4         1159     I
0608* 1A P4         1160     I
0609* 1A P4         1161     I
0610* 1A P4         1162     I
0611* 1A P4         1163     I
0612* 1A P4         1164     I
0613* 1A P4         1165     I
0614* 1A P4         1166     I
0615* 1A P4         1167     I
0616* 1A P4         1168     I
0617* 1A P4         1169     I
0618* 1A P4         1170     I
0619* 1A P4         1171     I
0620* 1A P4         1172     I
0621* 1A P4         1173     I
0622* 1A P4         1174     I
0623* 1A P4         1175     I
0624* 1A P4         1176     I
0625* 1A P4         1177     I
0626* 1A P4         1178     I
0627* 1A P4         1179     I
0628* 1A P4         1180     I
0629* 1A P4         1181     I
0630* 1A P4         1182     I
0631* 1A P4         1183     I
0632* 1A P4         1184     I
0633* 1A P4         1185     I
0634* 1A P4         1186     I
0635* 1A P4         1187     I
0636* 1A P4         1188     I
0637* 1A P4         1189     I
0638* 1A P4         1190     I
0639* 1A P4         1191     I
0640* 1A P4         1192     I
0641* 1A P4         1193     I
0642* 1A P4         1194     I
0643* 1A P4         1195     I
0644* 1A P4         1196     I
0645* 1A P4         1197     I
0646* 1A P4         1198     I
0647* 1A P4         1199     I
0648* 1A P4         1200     I
0649* 1A P4         1201     I
0650* 1A P4         1202     I
0651* 1A P4         1203     I
0652* 1A P4         1204     I
0653* 1A P4         1205     I
0654* 1A P4         1206     I
0655* 1A P4         1207     I
0656* 1A P4         1208     I
0657* 1A P4         1209     I
0658* 1A P4         1210     I
0659* 1A P4         1211     I
0660* 1A P4         1212     I
0661* 1A P4         1213     I
0662* 1A P4         1214     I
0663* 1A P4         1215     I
0664* 1A P4         1216     I
0665* 1A P4         1217     I
0666* 1A P4         1218     I
0667* 1A P4         1219     I
0668* 1A P4         1220     I
0669* 1A P4         1221     I
0670* 1A P4         1222     I
0671* 1A P4         1223     I
0672* 1A P4         1224     I
0673* 1A P4         1225     I
0674* 1A P4         1226     I
0675* 1A P4         1227     I
0676* 1A P4         1228     I
0677* 1A P4         1229     I
0678* 1A P4         1230     I
0679* 1A P4         1231     I
0680* 1A P4         1232     I
0681* 1A P4         1233     I
0682* 1A P4         1234     I
0683* 1A P4         1235     I
0684* 1A P4         1236     I
0685* 1A P4         1237     I
0686* 1A P4         1238     I
0687* 1A P4         1239     I
0688* 1A P4         1240     I
0689* 1A P4         1241     I
0690* 1A P4         1242     I
0691* 1A P4         1243     I
0692* 1A P4         1244     I
0693* 1A P4         1245     I
0694* 1A P4         1246     I
0695* 1A P4         1247     I
0696* 1A P4         1248     I
0697* 1A P4         1249     I
0698* 1A P4         1250     I
0699* 1A P4         1251     I
0700* 1A P4         1252     I
0701* 1A P4         1253     I
0702* 1A P4         1254     I
0703* 1A P4         1255     I
0704* 1A P4         1256     I
0705* 1A P4         1257     I
0706* 1A P4         1258     I
0707* 1A P4         1259     I
0708* 1A P4         1260     I
0709* 1A P4         1261     I
0710* 1A P4         1262     I
0711* 1A P4         1263     I
0712* 1A P4         1264     I
0713* 1A P4         1265     I
0714* 1A P4         1266     I
0715* 1A P4         1267     I
0716* 1A P4         1268     I
0717* 1A P4         1269     I
0718* 1A P4         1270     I
0719* 1A P4         1271     I
0720* 1A P4         1272     I
0721* 1A P4         1273     I
0722* 1A P4         1274     I
0723* 1A P4         1275     I
0724* 1A P4         1276     I
0725* 1A P4         1277     I
0726* 1A P4         1278     I
0727* 1A P4         1279     I
0728* 1A P4         1280     I
0729* 1A P4         1281     I
0730* 1A P4         1282     I
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0742* 1A P4         1294     I
0743* 1A P4         1295     I
0744* 1A P4         1296     I
0745* 1A P4         1297     I
0746* 1A P4         1298     I
0747* 1A P4         1299     I
0748* 1A P4         1300     I
0749* 1A P4         1301     I
0750* 1A P4         1302     I

```

0554" 01 0020	1105	LD	0C,52	1 NO,OP 07705 IN SCAN ROW FOR
0557" CD 0595"	1104			1 A L2N0 ON THE SCREEN
0558" C1	1105	CALL	0ACHLP	1 EXCHANGE 07705
0558" 01	1106	POP	0C	1 SCAN COUNT IN 0
0558" 01	1107	POP	00	
0558" 01	1108	POP	ML	1 ADDRESSES
0558" 14	1209	ZMC	0	1 ADJUST TO NEXT SCAN ROW
0558" 24	1110	ENC	M	
0558" 10 P0	1111	0JN2	0RCM1	1 DO NEXT ROW
0561" P1	1112	POP	AP	1 L2N0 COUNT IN A
0562" 15	1113	00C	0	1 ADJUST ADDRESSES BACK 1 SCAN ROW
0563" 25	1114	00C	M	
0564" 50	1115	00C	0	1 ADJUST LINE COUNT
0565" 20 22	1116	JR	2,0XCHMT	1 DONE
0567" P5	1117	PUSH	AP	1 REMAINING LINE COUNT
0568" 01 0020	1218	LD	5C,52	1 ADJUST ADDRESSES TO START
0568" 05	1119	ADD	ML,0C	1 OP NEXT LINE
0568" 58 70	1120	LD	A,75H	
0568" 64	1121	AND	M	
0568" 67	1122	LD	M,A	
0570" 00	1123	EX	00,ML	
0571" 09	1124	ADD	ML,0C	
0572" 12 70	1125	LD	A,70H	
0574" A4	1126	AND	M	
0575" 67	1127	LD	M,A	
0576" 00	1128	OR	00,ML	
0577" 10 06	1129	JR	0XCHMT	1 DO NEXT LINE
0578" P1	1130	POP	AP	1 A=0R1C, LINE COUNT
0578" 0P	1131	LD	L,A	1 LINE COUNT
0578" 20 00	1132	LD	M,0	
0578" 20	1133	ADD	ML,ML	1 R OP ATTR20UT0 07705 7C EXCHANGE
0578" 20	1134	ADD	ML,ML	
0578" 25	1135	ADD	ML,ML	
0580" 20	1136	ADD	ML,ML	
0581" 20	1137	ADD	ML,ML	1 (524ND, OP LINE5)
0582" 44	1138	LD	0,M	
0583" 40	1139	LD	C,L	1 COUNT TO RC
0584" E1	1140	POP	ML	1 SOURCE 1 ADDR.
0585" CD 0692"	1141	CALL	CALCAT7	1 GET ADDR. OP ATTRIBUTE FILE
0588" E0	1142	EX	00,ML	1 SOURCE 1 ATTRIBUTE ADDR. IN 0E
0589" E1	1143	POP	ML	1 SOURCE 2 ADDR.
058A" CD 0692"	1144	CALL	CALCAT7	1 SOURCE 2 ATTRIBUTE ADDR. IN ML
0590" CD 0593"	1145	CALL	EXCHLP	1 EXCHANGE ATTRIBUTE 07705
0590" C5 000P"	1146	JP	GDJCHMT	1 RETURN R=0
	1147			
	1148			
	1149			1 EXCHANGE LOOP
	1150			
	1151			1 EXCHANGES DATA BETWEEN TWO
	1152			1 MEMORY AREAS, ADDRESSES IN
	1153			1 0E AND ML, 0770 COUNT IN RC
	1154			
0595" C5	1155	EXCHLP	PUSH	1 SAVE COUNT
0596" 7E	1156	LD	A,(ML)	1 DATA FROM SOURCE 2
0596" 4P	1157	LD	C,A	1 TO WRG.REG.
0596" 1A	1158	LD	A,(0E)	1 DATA FROM SOURCE 1
0597" A9	1159	XOR	C	1 EXCHANGE VIA XOR
0598" P5	1160	PUSH	AP	1 SAVE INTERMEDIATE RESULT
0599" A9	1161	XOR	C	1 A NOW CONTAINS DATA FOR SOURCE 2
059A" 4P	1162	LD	C,A	1 C=NEW DATA FOR SOURCE 2
059B" P1	1163	POP	AP	1 INTERMEDIATE RESULT
059C" A9	1164	XOR	C	1 A=NEW DATA FOR SOURCE 1
059D" 12	1165	LD	(0E),A	1 WRITE NEW DATA
059E" 71	1166	LD	(ML),C	
059F" 15	1167	INC	00	1 ADJUST TO NEXT 0770
05A0" 23	1168	INC	ML	
05A1" C1	1169	POP	5C	
05A2" 0R	1170	DEC	0C	1 DECREMENT COUNT
05A3" TR	1171	LD	A,R	1 TEST IF DONE
05A4" R1	1172	OR	C	
05A5" 20 EC	1173	JR	M2,EXCHLP	
05A7" C9	1174	RET		
	1175			
	1176			
	1177			1 SUB-RTN. TO GET
	1178			1 OP ADDR. OF LINE IN 0 (0-25)
	1179			1 FOR OP IN 0 (1 OR 2)
	1180			1 RETURNS CARRY IF INVALID PARAMETER
05A8" 70	1181	GTADDR	LD	A,0
05A9" P0 10	1182	CP	24	
05A8" 50 1E	1183	JR	MC,GA0R0	1 TEST VALID LINE NO.
05A0" 0P	1184	RRCA		
05A8" 0P	1185	RRCA		
05A8" 0P	1186	RRCA		
05A8" 06 E0	1187	AND	0E3H	
05B2" 0P	1188	LD	L,A	
05B3" 7R	1189	LD	A,0	
05B4" E6 10	1190	AND	10H	
05B6" P6 40	1191	OR	40H	
05B8" 07	1192	LD	M,A	
05B9" TA	1193	LD	A,0	
05BA" AT	1194	AND	A	
05B8" 20 0E	1195	JR	1,GAERR	1 ERROR 2P NOT 1 OR 2
05B8" PE 03	1196	CP	5	
05B8" 50 0A	1197	JR	MC,GAERR	
05C1" C0 47	1198	SET	0,A	1 TEST WHICH OP
05C3" 20 04	1199	JR	M2,GTADDR1	1 OP1
05C3" TC	1200	LD	A,M	
05C3" P6 20	1201	OR	20H	1 OP2
05C8" 67	1202	LD	M,A	
05C9" AT	1203	GTADDR1	AND	1 RETURN NO CARRY
05CA" C9	1204	RET		
	1205			
05C8" ST	1206	GAERR	SCP	1 SET CARRY
05CC" C9	1207	RET		
	1208			



```

1210          SUETTL  INTERNAL SUB-ROUTINE.
1211          :
1212          :
1213          :
1214          : INPUT: V1000 MODE IN A
1215          : OUTPUT: M/W SETTING IN R
1216          : V10000 SETTING IN C
1217          : RETURNS M1 STATUS IF MODE INVALID
1218          :
1219          :
1220          : VALID VALUES:  INPUT      M/W      V10000
1221          :                   -----
1222          :                   =0 DISPLAY FILE ACTIVE 6T SCREEN
1223          :
1224          :                   01 ONLY      0      0      0
1225          :                   0100 012     00      0      50
1226          :                   01 & 012H    1      1      01
1227          :                   HIGH RES. GR. 2      2      2
1228          :                   04/00-COLUMN 06 - SE 06 - SE 4R - 7E
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063A*
063B* 3E 18
063C* 50
063D* 57
063E* 0F
063F* 0F
0640* 0F
0641* E6 E0
0643* 6F
0644* TA
0645* E6 18
0647* P6 40
0649* 6F
064A* 3A SCC2
064B* C8 47
064C* C8
0650* 3E 20
0652* 04
0653* 67
0654* CR

0655*
0656* E0 43 0034*
0659* 22 0036*
065C* C9

065D*
065E* E0 48 0034*
0661* 2A 0036*
0664* C9

0665*

0666*
0667* C0 0691*
0668* 3A 0039*
0669* 3F
066C* 3A 003F*
066D* 57
0670* 3A 0038*
0673* 47
0674* TE
0675* A0
0676* A2
0677* A0
0678* C0 40
067A* 28 08
067C* E6 F8
067E* CE 6F
0680* 20 01
0682* P6 07
0684* C0 70
0686* 2R 08
0688* 86 C7
068A* CE 57
068C* 20 02
068E* P6 38
0693* 77
0695* C9

0696* TC
0697* 0F
0698* 0F
0699* 0F
069A* E6 03
069B* P6 5E
069C* C8 6C
069E* 2E 01
069F* P6 10
06A0* 67
06A1* C9

06A2*
06A3* P5
06A4* 3A 3C80
06A5* 31 0025*
06A7* 3A 5C88
06A8* 32 003F*
06A9* 3A 5C91
06B1* 0F
06B3* 32 0036*

06B4* P1
06B7* C9

06B8* 3A SCC3
06B9* E6 1F
06BD* P6 40
06BF* 57
06C0* 2A 5C48
06C3* TE
06C4* E6 TP
06C6* 28 13
06C8* 04
06C9* 04
06CC* CC 1720
06CF* E0
06D0* 01
06D1* 18 P0
06D3* 23
06D4* 4E

1325 I
1326 I
1327 LNOU2
1328
1329 LO A,SCRS2
1330 SUB 0
1331 LO 0,A
1332 RBCA
1333 RBCA
1334 RBCA
1335 AND 080H
1336 LO L,A
1337 LO A,D
1338 AND 18H
1339 OR 40H
1340 LO M,A
1341 LO A,(VIDMOD)
1342 BIT 0,A
1343 RBT 2
1344 LO A,20H
1345 OR M
1346 LO M,A
1347 RBT
1348
1349 STPOSN1
1350 LO ((CURPOS),BC
1351 LO ((OPADRS),HL
1352 RBT
1353
1354 LOPDSN1
1355 LO BC,((CURPOS)
1356 LO HL,((OPADRS)
1357 RBT
1358
1359 I
1360 I
1361 I
1362 UPDAT1
1363
1364
1365 I
1366 CALL CALCATY
1367 LO A,(ATTB97)
1368 LO E,A
1369 LO A,(ATTMSA)
1370 LO D,A
1371 LO A,(MASAB)
1372 LO 0,A
1373 LO A,(ML)
1374 XOR B
1375 AND D
1376 ADR B
1377 BIT 4,B
1378 JR I,UPDAT1
1379 AND 0F8H
1380 BIT 5,B
1381 JR M2,UPDAT1
1382 OR 7
1383 UPDAT1 BIT 6,B
1384 JR 7,UPDAT2
1385 AND 0C7H
1386 BIT 1,A
1387 JR M2,UPDAT1
1388 OR 30H
1389 UPDAT2 LO (ML),A
1390 RBT
1391
1392 I
1393 I
1394 I
1395 I
1396 CALCATY LO A,M
1397 RBCA
1398 RBCA
1399 RBCA
1400 AND 03H
1401 OR 50H
1402 BIT 5,M
1403 JR 2,CALC01
1404 OR 10H
1405 CALC01 LO M,A
1406 RBT
1407
1408 LOATTR
1409
1410 PUSH AF
1411 LO A,(ATTB_P)
1412 LO (ATTBTT),A
1413 LO A,(MASA_P)
1414 LO (ATTMSA),A
1415 LO A,(P_PLAC)
1416 RBCA
1417 LO (MASAB),A
1418 POP AF
1419 RBT
1420
1421 GETSTRG LO A,(PARAM1)
1422 AND 1FH
1423 OR 40H
1424 LO 0,A
1425 LO HL,(VARS)
1426 GETSTR1 LO 6,(ML)
1427 AND 07FH
1428 JR 1,NOSTRG
1429 CP 0
1430 JB 1,GETSTR1
1431 PUSH DE
1432 CALL RECLEM
1433 EA DE,HL
1434 POP DE
1435 JR GETSTR1
1436 GETSTR2 INC HL
1437 LO C,(ML)

I GET DISPLAY FILE ADDRESS
I FOR STAGE OF LINE 2N 0

I TEST IF UP1
I SET TO OP2

I STORE CURSOR POSITION

I LOAD CURSOR POSITION

I UPDATE ATTRIBUTE BYTE FOR CHARACTER
I JUST PRINTED
I HL = ANT SCAN OF CHAR. IN OF

I ADPS. OF ATTRIBUTE BYTE TO HL
I
I CURRENT ATTB. VALUE TO E
I
I MASK TO 0
I
I FLAG TO 0
I BYTE FROM ATTRIBUTE FILE

I NEW ATTB. FOR DI OLD PQA 1
I SET INA COMPLEMENT OF PAPERT
I
I SET INK TO BLACA
I IS PAPER 4-7
I YES - USE BLACA INA
I NO - USE WHITE INA
I SET PAPER COMPLEMENT OF INAT
I
I SET PAPER TO BLACA
I IS INA 4-7
I YES - USE BLACK PAPER
I NO - USE WHITE PAPER
I TO ATTRIBUTE FILE

I SUB-RTN. TO CALCULATE ADAS. OF
I ATTRIBUTE BYTE FROM ADAS. CP
I ANT SCAN 80H IN OF

I UPPER BYTE OF ADDRESS

I TEST WHICH OP
I
I OPI
I ADAS. OF ATTRIBUTE IN OP2

I LOAD INTERNAL ATTRIBUTE VARIABLES
I FROM SYSTEM VARIABLES
I SAVE A
I
I
I
I
I SHIFT ODD BITS TO EVEN
I

I GET STRING ID
I MASK OFF UPPER BITS
I STRING VARIABLE IDENTIFIER
I SAVE IN D
I FIND STAGING
I
I TEST IF END OF VARS AREA
I NO FIND - RETURN BC=2
I TEST IF MATCH
I FOUND STRING
I SAVE STRING ID
I RETURN ADPS. OF NEXT VAR. IN DE
I ADPS. TO HL
I RESTORE STRING ID
I LOCK AGAIN
I GET LENGTH

```

0605*	23	1438	INC	ML	
0606*	46	1439	L0	9,(ML)	
0607*	23	1440	INC	ML	1 P1007 TEXT CODE
0609*	7R	1441	L0	6,0	1 7007 IP NULL STRING
0609*	01	1442	09	C	
0604*	C9	1443	097		1 RETURN (2 IP NULL STRING/42 IP 407)
		1444	1		
0606*	09 02	1445	W0079C	L0	C,2
0609*	06 00	1446		L0	9,6
060P*	C9	1447		007	
		1448	1		1 (2 AND 6C=2 FOR NO PINS)
		1449	1		
		1481			0U077L G04PHIC8 CH69.097
		1482	1		
		1483	1		1P200L P6770RNS FOR 870.G04PHIC8 007
		1484	1		1 0=06CRGROUND(PAPER) 1=PRECODEDUND(IN41
		1488	1		
0600*	00	1486	0RP07	defb	00000000b
0601*	00	1487		defb	6000C300R
0602*	00	1488		defb	00000000b
0603*	00	1489		defb	0000C300R
0604*	00	1490		defb	0000C300b
0605*	00	1491		defb	00000000b
0606*	00	1492		defb	0000C300b
0607*	00	1493		defb	00000000b
		1494	1		
0606*	0P	1498		defb	00001111b
060R*	0P	1499		defb	00001111b
0604*	0P	1497		defb	00001111b
0606*	0P	1499		defb	000C1111b
060C*	40	1499		defb	00000000b
060D*	00	1470		defb	00000000b
0608*	00	1471		defb	0000C000b
060P*	00	1472		defb	C000C000R
		1473	1		
		1474			
0600*	P0	1478		defb	11110000R
0601*	P0	1470		defb	11110000b
0602*	P0	1477		defb	11110000b
0603*	P0	1479		befb	11110000b
0604*	00	1479		defb	000C0000b
0608*	00	1490		defb	000C0000b
0606*	40	1481		defb	00000000b
0607*	00	1482		defb	60000000R
		1483			
0608*	PP	1494		defb	11111111b
0609*	PP	1495		befb	11111111b
0606*	PP	1486		defb	11111111b
0600*	PF	1487		defb	11111111b
060C*	00	1488		defb	00000000b
060D*	0C	1489		defb	R0000000b
0608*	00	1480		befb	00000000b
060P*	00	1491		defb	60000000b
		1492			
		1498	1		
0700*	04	1494		defb	00000000b
0761*	00	1498		defb	000C3000b
0762*	40	1496		befb	600C0000b
0703*	00	1497		befb	000C0000b
0764*	0P	148R		befb	00091111b
0709*	0P	1499		defb	R0001111R
0706*	0P	1800		defb	00001111b
0701*	0P	1801		befb	60001111b
		1802	1		
0706*	0P	1805		defb	000C1111b
0769*	0P	1804		defb	00001111b
07R4*	0P	1805		defb	00001111R
0766*	0P	1806		defb	000C1111b
070C*	0P	1807		defb	00001111b
0100*	0P	1806		defb	000C1111b
0709*	0P	1809		defb	000C1111b
070P*	0P	1810		defb	00001111b
		1811			
		1812			
0710*	P0	1815		defb	11110300R
0711*	P0	1814		defb	11110000R
0712*	P0	1815		defb	11110000R
0713*	P0	1816		defb	11110000b
0714*	0P	1817		defb	000C1111b
0715*	0P	1819		defb	00001111b
0116*	0P	1819		defb	00001111b
0717*	0P	1820		defb	000C1111b
		1821	1		
0710*	PP	1822		defb	11111111R
0719*	PP	1825		defb	11111111b
0714*	PP	1824		defb	11111111b
071R*	PP	1825		defb	11111111b
071C*	0P	1826		defb	000C1111b
071D*	0P	1821		defb	000C1111b
0119*	0P	182R		defb	000C1111b
011P*	0P	1829		defb	00001111b
		1830	1		
0720*	00	1831		defb	000C0000b
0721*	00	1832		defb	000C0000b
0722*	00	1835		defb	00000000b
0723*	00	1834		defb	000C0000b
0724*	P0	1835		defb	11110000b
0725*	P0	1834		defb	11110000b
0126*	P0	1831		defb	11110000b
0721*	P0	1839		befb	11110000R
		1839	1		
0720*	0P	1840		defb	00001111b
0729*	0P	1841		defb	00001111b
0724*	0P	1842		defb	00001111b
0726*	0P	1845		defb	00001111b
012C*	P0	1844		defb	11110000b
0720*	P0	1845		defb	11110000b
0729*	P0	1846		defb	11110000b
072P*	P0	1847		defb	11110000b

AOL - D3C 004 DUAL SCREEN MODE SUPPORT Cdl80/11 version 10.50.14  
GRAPHICS CHAR. SET 1GB3.SRC

10-MAR-84 1214T130

NO ERROR DETECTED

Cross reference listing (M00P version 4.71)

Symbol	Defn (0 = definition 8 = Write (blank) = read)						
6778V7	1370	557	1367	14120			
677M1x	1640	1168	16140				
6770_P	650	1411					
607LN	1350	268	7320				
C6LC61	1401	14030					
C6LC67	425	546	677	1001	1004	1345	3346
		1566	13960				
C6LCP0	569	676	13170				
CMNGV1	520	772					
CM0507	490	147	742				
CMY6L	1470	201	550	7450			
CM_000	610	835	8400	8570	862	8750	
		8600	1046	10450	10460	10590	
CL3M57	460	737					
CLUCYL	1040	481	7560				
CL0550	107	4800					
CL05C0	441	493	4970				
CL05C1	5100	567					
CL05C2	5190						
CL05C3	5220	576					
CL05C4	5240	543					
CL05C5	5330						
CL05C7	510	5700					
CL05C8	40	1070					
CL05C9	50	4830					
CL0107	565	5680					
CONVPM	537	580	675	686	12960		
CO0005	640	7020					
COP001	8790						
CO0000	867	870	8700				
COP701	436	8460					
COPY41	545	5670					
COPY42	972	8740					
COPY50	166	8340					
COPY11	8960	169					
COPY32	5310						
COPY13	5540	1025					
COPY34	5360	956					
COPY35	516	10060					
COPY36	10200	1025					
CO0Y57	927	1310	10260				
COPY50	45	1660					
CO0Y5C	43	960	9050				
COPY6K	8430	8480	8530	8520	8540	950	859
		10300	10520	10540	10560	1060	1065
CO0YAT	862	8900					
CU0005	1520	485	13560	1355			
06740	560	174	7400				
00577	750	76					
004005	1540	13510	1356				
00M107	500	12010	12600				
001V01	720	73	74				
074001	8140						
074002	812	9210					
000L0x	765	12500					
000LMD	775	12670					
000067	2500	263	206	706			
0xCH6	10990	1129					
0xCH61	1045	10460					
0xCH1	11000	1111					
00CHLP	1105	1145	11050	1173			
0xCH16	147	10370					
0xCH10	44	1670					
EXC45C	44	10640					
0xCHAT	1116	11300					
0xTN0M	709	743	7050				
05x	760						
05070L	780						
0P26	550	872					
000V	710	726	8000				
060006	1105	1195	1107	12660			
067600	115	6710					
06767	41	1150					
067AT7	39	6740					
067C1	600	695	6070				
067C06	116	6930					
067CYL	1120	504	671	7510	7520		
067CU0	45	1160					
067CU0	39	6840					
067M1	8710						
067M2	865	8740					
067M0	847	849	551	953	855	8620	876
		1040	1051	1053	1051	1057	
067570	292	837	1042	14210			
067V4L	766	12200					
000000	2500	339	162	494	726	704	819
		622	1006	1146			
000Lx	305	4450					
000A17	550	150	744				
00P17	50	14060					
0070L	1500	190	653	7450			
070001	1100	12030					
070005	882	897	1086	1001	11010		
07CH1	5050	856	601				
07CH2	1900	846					
07CH23	405	6080					
07CH3	801	6120					
07CH31	8140	820					
07CH32	815	835	6370				
07CH4	807	810	619	6410			
07CH5	802	8570					
07CH6	850	8440					
07CH40	30	5960					
07CH00	114	5010					
07CH00	41	1140					
07IN0x	1590	9530	601	630	650		

GYSY01	1A1A	1433					
GYSYR2	1A30	143A					
NR00PY	570	1259	1261R	1270	12720		
ININT	540	071					
IN30BL	5000						
IN50RT	730	754					
INVPAR	106	10R	2620	535	350	360	407
		490	492	721	009	001	000
		590	095	090	1079	1001	1007
		1092	1203				
LOATYR	177	500	497	14000			
LOPDSM	101	15540					
LINCOL	1000	533	97R	749R	750R		
LINLON	1510	213	66R	302	007	7300	777
		120A	129R	1310			
LNDU	371	505	1317	1327R			
LODP	3090	449					
LODP0	3510	400					
LODP1	5900						
MA50R	1550	227	1371	1417R			
MA50_P	000	1415					
MDVRS1	740	75	755				
NCS70G	1420	14400					
P404M0	710	1421					
PARERR	12R20	12R7					
PR4MT	690						
P_PLAC	070	1413					
RAHYOP	000	760					
ROCLEM	030	1432					
SC1W17	470	729					
SC4CTL	1190	202	340	730R			
SC4LO	20A	301	3040				
SC4LO	40	1220					
SC4LO0	122	34R0					
SC4LCY	1400	272	27R0	201R	73A0		
SC4LO7	A12	4150	470				
SC4OLL	50	5500					
SC4S1	440	245	363	495	700	1300	5329
S070	1231	12510					
S07C00	103	3510					
S07CUR	40	1030					
S07CUR	50	5550					
S07M21	770	7720					
S07M00	712	714	710	7220			
S07M01	720R						
S07M02	72A	7640					
S07M03	7700	010	024				
S07M00	42	1150					
S07M00	42	7000					
SPARR0	1000						
SP40R1	1090						
SP40R2	1100						
SP40R3	1110						
SP40R4	1430						
ST0L0	377	4510					
ST0L00	4340	440					
ST0L01	4350						
ST00V0	600	757					
ST00R0	110	7040					
ST0P0M	257	1310	19490				
ST0CC7	1010	322R	740R	7A10			
ST07A0	3730	41A	475				
ST0D1	1230	12400					
ST0P40	31A	507	07A	12790			
ST0P01	1201	12040					
TVPUL1	27A	2010					
TVPUL0	212	2030					
U0G	030	15A	159				
UP0RT1	1370	1301	13030				
UP0AT2	130A	1307	13090				
UP0AT0	7770						
UP0ATY	232	13A20					
UP0PC5	270	150	509	703	1307R		
VARS	600	1423					
V10M00	700	722	770R	00R	1301		
V10M00	1170	706					
WRCH0	97	1720					
WRCH01	1010	30A					
WRCH11	103	1500					
WRCH12	101	2010					
WRCH13	107	290	202R				
WRCH14	214	2150					
WRCH13	210	2200					
WRCH2	2230						
WRCH3	250	2320					
WRCH5	2350	249					
WRCH7	2500						
WRCH4R	30	1700					
WRCH00	40	97R					
WRCH07	2570						
WRSTLP	3010	315					
WRST00	90	2920					
WRST03	3090	320					
WRST00	40	900					
WRST06	30	3000					
WRST01	307	3100					
WRST02	523R						

## APPENDIX C-5

### SPRITE GRAPHICS SUPPORT

Name: Sprite Graphics Support

Description: This component provides support for sprite graphics. A sprite is a graphical object which can be created and manipulated by a set of services to be described below. A sprite can be up to 256 x 256 characters in size, however at most 32 x 24 can be displayed. Each sprite is identified by a sprite-id, a number greater or equal to zero, and has the following properties:

**definition** - a pointer to a width x height character bitmap, where width and height are user defined. A 1 indicates foreground color, a 0 indicates background color.

**position** - the row, col location on the screen of the upper left corner of the sprite.

**color** - the screen attribute (foreground color). A value of 0 indicates black, 1 indicates blue, etc.

**size** - the width and height of the sprite (in characters).

The sprites graphics package assumes that the system variables area of memory is not used by an application. The sprite support services can be invoked from machine code and BASIC. There is a BASIC interface routine, which takes as its parameter a BASIC string variable. The BASIC interface and sprite services code is loaded at SPRCODE, where SPRCODE = 0E000H. The name of this variable is specified by poking in the starting character at address SPRCODE-4. For example, if POKE SPRCODE-4, CODE "C" is executed, the variable C\$ will be used to pass commands to the interface routine. This must be done before using any sprite services. The interface routine is invoked by LET <variable> = USR SPRCODE. The status code returned by the last sprite service executed is assigned to <variable>.

The BASIC interface routine causes the report "A: Invalid argument" to be produced whenever an illegal command or invalid argument to a command is found. If too few or too many arguments are given for some command, the report "Q: Parameter error" is produced. If the command string variable cannot be found, the report "C: Nonsense in BASIC" is produced. If there is not enough memory for the number of sprites specified, the report "4: Out of memory" is produced (see InitSprites). There exists a variable called COMMAND, at address SPRCODE-9, which contains the number of the last sprite command to be executed. Thus if an error occurs in a sprite command, this variable can be PEEKed to find out which command in the command string caused the error. The first command in the command string is command zero.

The machine code routines all return status codes in the BC register pair. If the value in C is 0, then no error occurred. In this case B contains a value indicating further status information. If an error occurred, C will contain an odd value (thus bit 0 is set).

The BASIC interface string variable contains sprite graphics commands, separated by spaces. The commands have the following syntax:

"<command letter><parameter list>"

where <command letter> is a single upper or lower case letter identifying the command:

I = Init\_Sprites  
S = Init\_Screen  
C = Create\_Sprite  
W = Set\_Autowrap\_Mode  
P = Put\_Sprite  
E = Erase\_Sprite  
M = Move\_Sprite  
L = Change\_Sprite\_Location  
A = Change\_Sprite\_Attribute  
O = Overlap?  
H = Horizontal\_Scroll  
V = Vertical\_Scroll

and where <parameter list> is a list of numbers separated by commas. The string assigned to C\$ can be a string expression, i.e., "<subscr>"+STR\$(<expr>)+"<subscr>". The semantics of these commands will be described in detail below.

-----

APPLICATION SERVICES

-----

Name: Init\_Sprites

Machine code interface:

Inputs: A = max-sprites, 0..255

Outputs: BC = 0000h - OK  
          = 0001h - not enough memory

Entry: SPRSVC = SPRCDE + 219H

BASIC interface:

Inouts: LET C\$ = "1<max-sprites>"

Description: This service initializes the data structures used by the sprites services. Memory immediately below the sprites code is used for global variables and memory immediately below this is used to store sprite information. Each sprite takes up 8 bytes of data (not including its definition, which is stored elsewhere). Thus the total amount of memory used by the sprites package is:

8 bytes \* max-sprites + 29 byte globals area + 2746 byte code area

If there is not enough memory between RAMTOP and the global variables area for the number of sprites specified, 0001h is returned. Thus, RAMTOP should be set to

SPRCDE - 29 - 8 max-sprites

before calling InitSprites. This service must be called before any of the others.

BASIC example: "132" initializes memory for 32 sprites.

-----

Name: Init\_Screen

Machine code interface:

Inputs: A = screen\_height, 0..24  
          0 = background\_color, 0..7  
          E = border\_color, 0..7

Outputs: BC = 0000h - OK  
          = 000Th - illegal screenht  
          = 0005h - illegal color

Entry: SPRSVC + 200H

BASIC interface:

Inouts: LET C\$ =  
          "5<screen\_ht>,<background\_color>,<border\_color>"

Description: This service clears the top screen\_ht lines of the screen and sets their color to the background\_color. The bottom 24-screen\_ht lines are cleared and set to the border\_color. The top screen\_ht lines are used for displaying sprites. The remaining lines can be used for text display. The border is set to the border\_color.

BASIC example: "522,5,1" clears the top 22 lines and sets their color to cyan, clears the bottom two lines and sets their color to blue, and sets the border color to blue.

-----



-----

Name: Create\_Sprite

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1  
B = width, 0..255  
C = height, 0..255  
D = color, 0..7  
HL = definition address, 0..64K-1

Outputs: BC = 0000h - OK (sprite created)  
          = 0003h - illegal sprite-id

Entry: SPRSVC + 5FH

BASIC interface:

Inputs: LET Cs =  
          "C<sprite-id>,<width>,<height>,<color>,<addr>"

Description: This service creates a sprite with the specified width, height, color, and definition address. Initially, the position of the sprite is (0, 0). If there was already a sprite with the specified id, it is destroyed. In all further operations the new sprite is identified by its sprite-id.

BASIC example: "C0,2,2,2,32768" creates sprite 0 which is 2 x 2 characters in size, is red in color, and whose definition is located at 32768. The initial position of the sprite is (0, 0).

-----

Name: Set\_Autorep\_Mode

Machine code interface:

Inputs: A = mode: 0 - off, not 0 - on

Outputs: BC = 0000h - OK

Entry: SPRSVC + B0H

BASIC interface:

Inputs: LET Cs = "W(mode)"

Description: This service turns the Autorep mode on and off. When the Autorep mode is on, all changes to a sprite's location are made so that the new location is on the screen (wraparound).

BASIC example: "W1" causes Autorep mode to be turned on.

-----

Name: Put\_Sprite

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1  
D = row, 0..255  
E = column, 0..255

Outputs: BC = 0000h - OK, nothing overwritten  
          C100h - OK, something overwritten  
          0003h - illegal sprite-id  
D = row  
E = column

Entry: SPRSVC + EEH

BASIC interface:

Inputs: LET Cs = "P<sprite-id>,<row>,<column>"

Description: This service writes a sprite on the screen. The row and column described above define the location at which the upper left corner of the sprite is written. If the Autorep mode is set, the row and column values are modified to make sure that the sprite location is a legal screen location. The new row and column values are returned. If the Autorep mode is not set, the position is not changed. However, only those parts of the sprite corresponding to legal screen positions will be displayed. If writing the sprite causes something on the screen to be overwritten, the appropriate code is returned.

BASIC example: "P0,11,0" puts sprite 0 at row 11, column 0. Anything already at that location is overwritten. If anything is overwritten, the interface routine returns 10h.

-----  
Name: Erase\_Sprite

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1

Outputs: BC = 0000h - OK (sprite erased)  
          0003h - illegal sprite-id

Entry: SPRSVC + 190H

BASIC interface:

Inputs: LET C\$ = "E(sprite-id)"

Description: This service erases a sprite. The screen becomes blank where the sprite used to be. The sprite still exists and can be written elsewhere.

BASIC example: "EO" erases sprite 0. The screen locations taken up by the sprite become blank.

-----  
Name: Move\_Sprite

Machine code interface:

Inputs: S = sprite-id, 0..max-sprites - 1  
          D = relative vertical motion, -128..127  
          E = relative horizontal motion, -128..127

Outputs: BC = 0000h - OK, nothing overwritten  
          0100h - OK, something overwritten  
          0003h - illegal sprite-id  
          D = row  
          E = column

Entry: SPRSVC + 200H

BASIC interface:

Inputs: LET C\$ = "M(sprite-id),<vert>,<hor>"

Description: This service moves a sprite. The sprite is erased at its current location and written at a new location defined by:

new row = old row + relative vertical motion  
new col = old col + relative horizontal motion

The location of the sprite is updated to reflect the motion. As with the "P" command, if the sprite overwrites anything, 10h is returned. If Autowrap mode is set, then the location is automatically wrapped to fit onto the screen (i.e., (24, 0) becomes (0, 0)). The new row and column values are returned. If Autowrap mode is not set, only those parts of the sprite which correspond to legal screen locations will be displayed.

BASIC example: "M0,2,0" changes the position of the sprite from (11, 0) to (13, 0). This operation is equivalent to "EO 0,13,0".

-----  
Name: Change\_Sprite\_Location

Machine code interface:

Inputs: A = sprite-id, 0..max-sprites - 1  
          D = new row, 0..255  
          E = new column, 0..255

Outputs: BC = 0000h - OK (location changed)  
          0003h - illegal sprite-id

Entry: SPRSVC + 237H

BASIC interface:

Inputs: LET C\$ = "L(sprite-id),<new-row>,<new-col>"

Description: This service updates the position property of a sprite in the same fashion as for Put\_Sprite. The sprite is not updated on the screen until a Move\_Sprite, Put\_Sprite, or Erase\_Sprite is executed.

BASIC example: "L0,13,0" will set the location of sprite 0 to (13, 0).

-----

Name: Chengx\_Sprite\_Attribute

Machine code interface:

Inputs: A = sprite-id, 0..max-sprite - 1  
D = color, 0..7

Outputs: BC = 0000h - OK (color changed)  
0003h - illegal sprite-id  
0005h - illegal color

Entry: SPRSVC + 240H

BASIC interface:

Inputs: LET C\$ = "A(sprite-id),<color>"

Description: This service updates the color property of a sprite. The sprite on the screen is not updated until a Put\_Sprite, Move\_Sprite, or Erase\_Sprite is executed.

BASIC example: "A0,2" causes the color property of sprite 0 to be set to red.

-----

Name: Overlap?

Machine code interface:

Inputs: D = sprite-id-1, 0..max-sprite - 1  
E = sprite-id-2, 0..max-sprite - 1

Outputs: BC = 0000h - OK, no overlap  
0200h - OK, overlap  
0003h - illegal sprite-id

Entry: SPRSVC + 268H

BASIC interface:

Inputs: LET C\$ = "D(sprite-id-1),<sprite-id-2>"

Description: This service is used to detect if two sprites overlap.

BASIC example: "D0,1" causes the interface routine to return TBS if sprites 0 and 1 overlap on the screen.

-----

Name: Vertical\_Scroll

Machine code interface:

Inputs: A = direction: positive number - down,  
negative number - up

Outputs: BC = 0000h - OK

Entry: SPRSVC + 558H

BASIC interface:

Inputs: LET C\$ = "V<direction>"

Description: This service is used to scroll the entire screen in the vertical direction by one row. The direction of scroll is up if direction is less than 0, otherwise the scroll is down. The position property of all sprites is updated by one row to reflect the effect of the scroll. If Autowrap code is not set, then 1 is added to the row value. If Autowrap code is set, then 1 is added to the row value, and this new value is wrapped to fit on the screen.

BASIC example: "V1" causes the screen to be scrolled down one row.

-----

Name: Horizontal\_Scroll

Machine code interface:

Inputs: A = direction: positive number - right,  
negative number - left

Outputs: BC = 0000h - OK

Entry: SPRVC + 3BDH

BASIC interface:

Inputs: LET C\$ = "N<direction>"

Description: This service is used to scroll the entire screen in the horizontal direction by one column. The direction of scroll is to the left if direction is less than 0, otherwise the scroll is to the right. The position property of all sprites is updated by one column to reflect the effect of the scroll. If Autoursp mode is not set, then 1 is added to the column value. If Autoursp mode is set, then 1 is added to the column value, and this new value is erased to fit on the screen.

BASIC example: "M1" causes the screen to scroll right one column.

CHDINT CR180/11 version 10.36.14  
Commons Interpreter module CHDINT.SRC

13-Feb-84 16:18:38

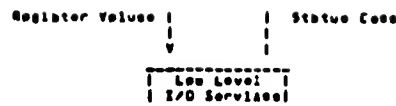
=0000

```
1          SETC CHDINT, ABS, LOC=0E000H
2          NAME CHDINT
3          SUBTTL Commons interpreter module
4
5
6 |-----|
7 | Commons interpreter module |
8 |
9 | Inputs: none.
10 |
11 | Outputs: a Parameter Block (see below).
12 |
13 | Description: this module scrolls a sprites command string and, for
14 | each recognized command, produces a parameter block
15 | describing the command. This parameter block is passed
16 | to the interface handler module. To parse a command
17 | string first it must be found. The global variable
18 | variable NAME contains the name of the string variable
19 | containing the command string. The RCE routine @iND_h
20 | is used to find the variable in the VARS area of memory.
21 | Note: symbols in all upper case are external to this
22 | module and are defined under imports below.
23 |
24 |
25 | Following is an architecture overview showing the relation
26 | between the modules of the sprite services component of
27 | the application development library:
28 |
29 |
30 |-----|
31 | command |
32 | string  |
33 |-----|
34 | BASIC |-----| Command |
35 | program |-----| Interpreter |
36 |-----|
37 | Status |
38 | Code   |
39 |
40 | Parameter Block |
41 |
42 |-----|
43 | I/P |
44 | Handler |
45 |-----|
46 | Register Values |
47 |
48 |-----|
49 | Register Values |
50 |-----|
51 | Status |
52 | Code   |
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99 |-----|
100 | Register Values |
```

```

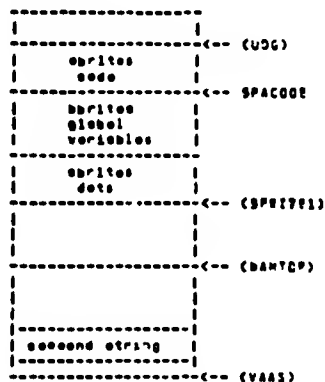
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```



Note: the interface between the I/O handler module and the Sprites Services module is the same as the interface between an assembly language program and the Sprites Services module.

Following is the layout in memory of the code and data used by the sprits services component of the application development library:



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```

```

#0003      197  ebitest      equ      color+1      !definition address
#0007      199  eflags      equ      coltest+2      !local write flags
#000C      199  eflag      equ      0              ! bit 0 - allocated?
#0001      199  everwrite    equ      1              ! bit 1 - write overwrite something
#0002      199  lms         equ      2              ! bit 2 - write in lower half screen
162
163
164
165
166
167  ! Locals
168
169  ! state needs for cursor
170  s0      equ      0
171  s1      equ      1
172  s2      equ      2
173  e_end    equ      3
174  e_error   equ      0FFH
175
176
177  ! =====
178  ! CH0INT
179  !
180  ! Input: none.
181  ! Output: cursor position block.
182  !
183  ! Global Read: none.
184  !
185  ! Global Write: none
186  !
187  !
188  !
189  !
190  ! Procedures Called: find_string
191  !
192  !
193  !
194  !
195  !
196  !
197  !
198  !
199  !
200  !
201  ! Procedures Called By: none.
202  !
203  !
204  ! Description: this is the main routine of the command interpreter.
205  ! The command string is located (using #INC_W) and is
206  ! recognised by the following notation:
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```

E059 7E      261      ld      o, (hl)      ;get next char
E03A CC 8059 262      call  w_or_1_aloha
E030 50 38   263      jr      ns, not_aloha   ;jump if not under or lower case letter
E030 C0 80CE 264      call  letter
E042 10 13   265      jr      ah10101      ;otherwise handle letter
E044 0E 10   266      not_aloha
E046 2E 05   267      jr      na, not_einua   ;jump if not minus sign
E04E CC E1E0 268      call  minus
E040 1E 06   269      jr      ah10101      ;otherwise handle minus
E040 C0 5009 270      not_minus
E050 3E 05   271      jr      o, not_diglt   ;jump if not digit
E052 C0 E121 272      call  number
E050 1E 00   273      jr      ah10101      ;otherwise handle number
E057 0E 2C   274      not_diglt
E059 2E 05   275      jr      na, not_aseee   ;jump if not some
E05E C0 810E 276      call  seeee
E05E 1E C7   277      jr      ah10101      ;otherwise handle some
E060 0E 10   278      not_aseee
E062 2E 05   279      jr      ns, bad_ohr   ;not a space either, so must be illegal
E064 C0 E164 280      call  seeee
E067 1E 0E   281      jr      ah10101      ;handle space
E069 0E 0F   282      bad_ohr
E049 1E 06   283      jr      b, s_error   ;set b = s_error
E060 0E 0F   284      endul
E06E 67      285      oush
E06F EC 51   286      end
E071 E1      287      sbe
E072 2E 02   288      pop
E074 0A 00   289      jr      A, do_sed
E074 75      290      ld      b, s0
E077 32 0FEC 291      ld      o, A
E076 C1      292      (num_soreel, /
E07E C5      293      bs
E07C 1E      294      do
E070 C0 80010 295      call  l_P_HANDLER
E080 C5      296      oush
E081 09      297      ba
E082 C1      298      osh
E083 09      299      na
E084 21 0F07 300      ld      hl, seeeee
E087 0A      301      ins
E080 E1      302      (hl
E089 01      303      osh
E080 C1      304      do
E080 1E 0F   305      pop
E080 09      306      jr      ah10101
E080 C0      307      endu0
E08F 09      308      osh
E090 C1      309      ba
E091 C9      310      osh
E091 C9      311      bc
E091 C9      312      ret
E092 ED 48 8C80 340
E096 C5      341      find_string
E097 1A 8C3A 342      ld      b, (CH_0001)
E096 08      343      oush
E098 01 0F0C 344      ld      o, (FLAG01)
E09E 0C 43 5C50 345      leave FLAG0
E0A2 3E 00   346      ld      b, name
E0A4 32 8C36 347      ld      CCM_0001, b
E0A7 C0 2C70 348      ld      o, 80H
E0AA 30 02   349      ld      (FLAG0), o
E0AC CP      350      test runtime flag, reset others
E0AD 09      351      find_string
E0AE C1      352      jr      no, f_ok
E0AF 70      353      pop
E0B0 32 8C5A 354      ba
E0B3 C1      355      ld      a, b
E0B4 00 43 5C80 356      (FLAG0), o
E0B8 C9      357      osh
E0B8 C9      358      bc
E0B8 C9      359      ret
E092 ED 48 8C80 360
E096 C5      361      find_string
E097 1A 8C3A 362      ld      b, (CH_0001)
E096 08      363      oush
E098 01 0F0C 364      ld      o, (FLAG01)
E09E 0C 43 5C50 365      leave FLAG0
E0A2 3E 00   366      ld      b, name
E0A4 32 8C36 367      ld      CCM_0001, b
E0A7 C0 2C70 368      ld      o, 80H
E0AA 30 02   369      ld      (FLAG0), o
E0AC CP      370      test runtime flag, reset others
E0AD 09      371      find_string
E0AE C1      372      jr      no, f_ok
E0AF 70      373      pop
E0B0 32 8C5A 374      ba
E0B3 C1      375      ld      a, b
E0B4 00 43 5C80 376      (FLAG0), o
E0B8 C9      377      osh
E0B8 C9      378      bc
E0B8 C9      379      ret

```

```

360
361 ;*****
362 ;
363 ; U_OR_L_ALPHA
364 ;
365 ; Input: current character in 4.
366 ;
367 ; Output: set carry flag if current letter is an upper or lower case
368 ; letter. Otherwise, reset the carry flag.
369 ;
370 ; Global Read: none.
371 ;
372 ; Global Written: none.
373 ;
374 ; Procedures Called: none.
375 ;
376 ; Procedures Called By: cadint
377 ;
378 ; Description: this routine checks if the current character is an upper
379 ; or lower case letter. If so, the carry flag is set.
380 ; otherwise the carry flag is reset. If the character is a
381 ; lower case letter, it is converted to an upper case letter.
382 ;
383 ;*****
384
385
386 U_OR_L_ALPHA    cc      "4"          ;check if upper case letter
387                ccf
388                ret                ;less than "4", so return
389                cc      "Z"+1
390                cc      0          ;is upper case letter
391                cc      "a"        ;check if lower case letter
392                ccf
393                ret                ;less than "a", so return
394                cc      "a"+1
395                ret                ;greater than "a", so return
396                sub      "a"-4
397                ccf
398                ret                ;set carry flag
399
400
401 ;*****
402 ;
403 ; LETTER
404 ;
405 ; Input: current character in 4. The character is an upper case letter.
406 ; h1 = current string pointer.
407 ; 6 = error state
408 ;
409 ; Output: avc_code in the parameter block.
410 ; 6 = error state
411 ;
412 ; Global Read: none.
413 ;
414 ; Global Written: error
415 ;
416 ; Procedures Called: none.
417 ;
418 ; Procedures Called By: cadint
419 ;
420 ; Description: this routine translates the current character, which is
421 ; known to be a letter, into a code. If this code = err,
422 ; then the letter is not a legal command character. In this
423 ; case state is set to a_error. Otherwise, the code is the
424 ; service code appropriate to the command letter and is put
425 ; into avc_code in the parameter block. State is set to sl.
426 ;
427 ;*****
428
429
430 err            ccw      0FFH
431
432
433 letter        push    sf
434                ld      b, 0
435                cc      0
436                jr      2, l_ok0    ;state = 00, so ok
437                pop     sf
438                ld      b, a_error  ;otherwise, state is illegal
439                ret
440
441 l_ok0         pop     sf
442                push    de
443                push    hl
444                ld      hl, l_table ;hl -> letter state table
445                sub     "A"
446                ld      d, 0
447                ld      e, 0
448                add     hl, de      ;hl -> desired table entry
449                ld      e, (hl)
450                or      err
451                jr      no, l_ok1   ;code is ok, so jump
452                ld      b, a_error  ;else code is illegal
453                ld      hl, l_exit
454                ld      hl, (avc_code), a ;otherwise put code in param block
455                ld      b, sl
456                ld      hl, l_exit
457                ld      hl, a
458                ld      hl, a
459                ret
460
461 l_table       dwb      7          ;Change_Sprite_Attribute
462                dwb      err
463                dwb      1          ;Create_Sprite
464                dwb      err
465                dwb      4          ;Erase_Sprite
466                dwb      err
467                dwb      err
468                dwb      10         ;Horizontal_Scroll

```



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80P5 0D
80P6 0P
80P7 0A
80P8 0A
80P9 0S
8100 0A
8101 0S
8102 0S
8103 PP
8104 0P
8105 0S
8106 0P
8107 PP
8108 0S
8109 0Z
810A PP
810B 0P
810C PP

465      defb 0          12Init_Serites
466      defb 0FF
467      defb 0FF
468      defb 0
469      defb 5          1Change_Serite_Location
470      defb 0FF
471      defb 0
472      defb 5          1Read_Serite
473      defb 0FF
474      defb 0
475      defb 5          1Serial_7
476      defb 0FF
477      defb 0
478      defb 0FF
479      defb 5          1Init_Screen
480      defb 0FF
481      defb 0FF
482      defb 11
483      defb 1          1Serial_Scroll
484      defb 0FF
485      defb 0FF
486      defb 0FF
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572

;=====
;
; MINUS
;
; Inputs: h1 = string pointer.
;         b = error state.
;
; Outputs: b = error state.
;
;
; Global Read: gflags
;
; Global Written: gflags
;
; Procedures Called: none.
;
; Procedures Called By: endint
;
; Description: if the state is not 01, sets state to 0_error. Otherwise,
; toggles bit neg of gflags and state in state 01.
;=====
;
; minus      anc    h1          ;advance string pointer
;            ld     0, d1
;            co     6
;            jr     0, 0_0k     ;state = 01, so ok
;            ld     0, 0_error   ;otherwise state (- 0_error
;
; 0_0k      ret     0x0h        h1
;            ld     0, 0_0flag   h1, 0_0flag
;            ld     0, (h1)
;
;            rrc
;            ccf
;            rlc
;            ld     (h1), 0
;            mov    h1
;            ret
;
;=====
;
; NUMBER
;
; Inputs: current string pointer in HL.
;         b = error state.
;
; Outputs: b parameter in the parameter block.
;         b = error state.
;
;
; Global Read: CH_ADD
;
; Global Written: CH_ADD
;
; Global Read: gflags
;
; Global Written: 0x0000
;
; Procedures Called: MINUS
;
; Procedures Called By: endint
;
; Description: this routine converts the digit string starting at the
; current value of the string pointer and ending with the
; first non-digit character into an unsigned two byte number.
; If the neg flag is set the 2's complement is taken. The
; number is put into the current zero slot in the zero block.
; The value of 0x0000 is incremented. State becomes 02.
;=====
;
; number      push    0x00
;            push    0x00
;            ld     0x, (CH_ADD)   ;move CH_ADD
;            push    0x00
;            ld     (CH_ADD), h1   ;set CH_ADD to point to digit string
;            call    MINUS        ;convert string to number. Number
;            ; ends up on calculator stack.
;
;            push    h1
;            call    PP25C        ;put number in 0x
;            ld     h1, gflags
;            bit    neg, (h1)
;            jr     0, 0x00

```

```

0139 70
0130 20
0135 07
013C 75
0130 20
0130 40
013P 05
0100 C6 06
0142 00 71 00
0145 0C 25
0107 0C 70 60
0146 0C 23
014C 01
0100 01
0100 EC 55 5C50
0152 01
0153 C1
0154 0C
0155 06 02
0157 C9

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```

0150 50 01
0150 00
0156 20 03
0150 06 00
015P C9
0160 25
0161 06 01
0165 C9

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```

0164 51 00
0166 00
0167 20 03
0169 06 05
0161 C9
016C 25
0160 C9

```

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375      ld      a, b      ;here to convert to 2's complement
376      cpl
375      ld      b, a
376      ld      a, a
377      cpl
378      ld      a, a
379      bnc     00
380      rsc     reg, (hl)   ;put number into current core slot
381      ld      (ix), a
382      bnc     ix
383      ld      (ix), b
384      bnc     ix
385      pop     hl
386      pop     de
387      ld      (CH_600), de ;restore CH_600
388      do
389      pop     de
390      pop     bc
391      bnc     0
392      ld      b, a2
393      ret
394
395
396
397 ;=====
398 ;
399 ; COMMA
400 ;
401 ; Input: current string pointer in HL.
402 ;      a = parser state.
403 ;
404 ; Output: b = parser state.
405 ;
406 ; Globals Read: none.
407 ;
408 ; Globals Written: none.
409 ;
410 ; Procedures Called: none.
411 ;
412 ; Description: this routine recognizes a comma. If state is a2, then
413 ; the comma is skipped and state becomes a1. Otherwise
414 ; state becomes s_error.
415 ;
416 ;=====
417
418
419
420
421
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423
424
425
426
427
428
429
430
431 ; SPACE
432 ;
433 ; Input: current string pointer in HL.
434 ;      b = parser state.
435 ;
436 ; Output: b = parser state.
437 ;
438 ; Globals Read: none.
439 ;
440 ; Globals Written: none.
441 ;
442 ; Procedures Called: none.
443 ;
444 ; Description: this routine recognizes a space. If state is a0, then
445 ; the space is skipped and state remains a0. Otherwise
446 ; state becomes a_end.
447 ;
448 ;=====
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CMOINT CR200/II version 10.3a.14 11-Feb-84 161214Z  
 CooooH Interpreter oOHu CMOINT.SOC

A	Reserver	ALLCC	0003	AUTOWS	3001	S	Reserver	EACGCC	80FE
EPBSCH	8045	C	Reserved	CHSPDO	1C50	CMOINT	8030	COL	0301
COLOR	0004	COMHP	E159	COMHPN	0007	C80K	E160	C	Reserved
OIGIT	3003	004CM0	E076	E	Reserved	EMOW0	EG80	ENOWI	E040
ESS	00PP	EXXOR	000E	PIN30N	0070	0IN015	E092	PLGCS	SCSP
PP2EC	3160	P10X	E04E	GPL4GS	0PPE	GL00AL	0010	"	Reserved
MEIGHT	0003	ININT	50P9	ISPSM4	3301 EX	L	Reserver	LETTER	E0C9
LMS	0802	LXEXIT	E0EP	L80K0	E005	L80X1	ECE4	LETTEL	E0P5
N	Reserved	MAXSCQ	0020	MAXS80	301E	MAX8SP	00P4	MINUS	E100
M80X	E116	NAME	0PFC	NEG	0000	NOT94L	E044	NOTPCO	E060
NOT901	E857	NOT9MI	E040	NUM8E8	F121	NUM8P4	DPEC	OK	E01C
OVERW8	0001	PARMS	00E9	PAPMI	00E0	PARM2	00EP	PARM3	0PPI
PARM4	0PP3	PARMS	00P5	PEITMA	C005	POS	E140	PSW	Reserved
80W	0000	SCPPTC	0PE3	SCREEN	0PP9	SPL4GS	0007	SP	Reserved
SPACE	E164	SPCCCO	E000	SP0ITE	CPPE	SVC8CO	0PE8	S8END	0003
SE8RD	00PP	S8OK	E16C	SO	0500	S1	0001	S2	0002
UE0RIL	E085	WHILE0	E01C	WHILE1	E027	WID7W	0E02		

NA PPP0P6 detected

# Cross reference listing (M88P version 4.7)

Symbol	Refs (0 = definition 1 = write <blank> = read)
ALLOC	159P
AUTOWS	1310
EPCKGC	1350 136
E40_CM	279 2820
CH_000	1030 341 3409 3578 563 5658 5878
CMOINT	2260
CCL	1530 154
COLOR	1560 157
COMHP	276 6190
COMHPN	1360 137 2298 300
C_0K	621 6240
OIGIT	1000 270
OD_CMO	240 288 290P
EMOW0	248 3060
ENOWI	255 2E40
888	430R 449 462 464 466 467 470
	471 474 477 478 480 481
	484 485 486
ERROR	105R 251 551
PINO_M	1080 549
0INO_5	233 3410
PL4GS	1020 343 34EX 5558
PP28C	1070 569
P_0X	350 353P
GPL4GS	1290 132 250 519 570
GLJ6AL	1250
MEIGHT	1550 156
ININT	1060 566
I_0_M4	1110 295
LETTER	264 4330
LMS	161P
L_EXIT	452 4550
L_0K0	436 4400
L_0K1	450 4550
L_7PEL	443 461P
MAX_CO	1210
MAX_RO	1200
MAX_SP	1320 135
MINUS	24E 512P
N_0K	515 51E0
NAME	1280 123 2278 345
NEG	1300 251 571 5E0
NOT_PL	263 2440
NOT_CO	275 2780
NOT_0I	271 2740
NOT_0I	267 2700
NUMBER	272 5610
NUM_P4	1440 145 2918
OK	250 2530
OVERW8	1400
PARM1	1450 146 245
PARM2	146P 147
PARM3	1470 14E
PARM4	1480 149
PARM5	1490
PARM5	1370 138 145
PEITMA	1570 158
POS	572 - 580P
80W	1520 153
SO	168P 243 246 289 454 655
S1	1670 454 515 625
S2	1700 591 615
SCR47C	1380
SCREEN	133P 135
SPL4GS	1580
SPACE	280 6550
SPRC00	1220 127 223
SP0ITE	127P 148
SVC_CO	1430 144 4538
S_8M0	1710 255 656
S_8M0	1720 2E2 438 451 516 622
S_0X	655 6580
U_08_LL	262 3860
WHILE0	2440 305
WHILE1	249P 265 273 277 281 385
WID7W	1540 155

```

1      MORG IPMANDL64
2      SUB77L interface handler module
3
4      ;=====
5      ;
6      ; Interface handler module
7      ;
8      ; Input: a parameter block.
9      ;
10     ; Output: a status code in EC.
11     ;
12     ; Description: this module translates a parameter block into a call to
13     ; the appropriate write service. The number of parameters
14     ; specified in the param block is checked against a table
15     ; entry for the service containing the correct number of
16     ; parameters. If these values do not match, the report
17     ; "CI Parameter error" is produced. Otherwise the interface
18     ; routine appropriate to the service is invoked. This
19     ; interface routine puts the values of the param in the
20     ; param block into the appropriate registers. The write
21     ; service is then invoked. When the service returns, the
22     ; status code is checked and, if an error occurred, an
23     ; appropriate report is produced.
24     ;
25     ;=====
26
27     ; Imports
28
29     ;=====
30     ;
31     ; Imports
32     ;
33     ; Imports
34     ;
35     ; Imports
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37     ; Imports
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97 ; *****
98 ;
99 ; 1_f_handler
100 ;
101 ; Inputs: a parameter block.
102 ;
103 ; Outputs: a status code in EC.
104 ;
105 ; Global Used: none
106 ;
107 ; Global Written: none.
108 ;
109 ; Procedures Called: add0d
110 ;                      jne_hl
111 ;                      jne_lo
112 ;
113 ; Procedures Called By: CM0INT
114 ;
115 ; Description: this is the main routine of this module. It translates
116 ; the values in the parm block into a call to a service.
117 ; The interface to the service services are
118 ; described in a table. For each service the is an entry
119 ; containing the correct number of parameters, the address
120 ; of the interface routine appropriate to that number of
121 ; parameters, and the address of the service. The number
122 ; of parms in the table entry is checked against the value in
123 ; the parm block. If they do not agree, report "c" is produced.
124 ; If they do agree, the interface routine indicated in the
125 ; table entry is called. The service indicated in the table
126 ; entry is then called. If the part of the status code in C
127 ; is non-zero, then an error occurred in executing the service
128 ; routine, and an appropriate report is produced.
129 ;
130 ; *****
131 ; OAC OE16M
132 ;
133 1_f_handler 1d a, (svc_code) ;check num_parms against the
134 ; value in 1_f_table
135 1d b, a
136 oie A
137 oie A
138 add a, d ;a = $Aave_code
139 1d hl, 1_f_table
140 1d e, a
141 1d d, 0
142 add hl, de ;hl -> table entry
143 1d e, (num_parms)
144 co (hl)
145 jr e, ok ;if num_parms correct, then jump
146 rdt EROR ;else produce report "0"
147 defb 25
148 ok 1d hl ;hl -> i/f routine addr
149 1d e, (hl)
150 1d hl
151 1d d, (hl)
152 1d hl
153 co de, hl ;hl = i/f routine addr
154 1d e, (de) ;de -> service addr
155 1d c, e
156 1d aa
157 1d A, (ee)
158 1d b, e
159 1d ix, 0
160 add ix, dc ;ix = service addr
161 push ix
162 1d ix, $serv1 ;ix -> $serv1
163 call jne_hl ;invoke i/f routine
164 pop ix
165 call jne_lo ;invoke write service
166 bit 0, c ;check status code
167 ret d ;if c0 = 0, then no error
168 1d e, e
169 op 1
170 jr no_enough_room ;if c < 1, then not out of room
171 ret EROR ;else produce report "4"
172 defb 3
173 enough_room 1d c
174 ret EROR ;produce report "6"
175 defb 9
176 ret
177
178 ; *****
179 ;
180 ; JMP_ML
181 ;
182 ; Inputs: ML = address to be called.
183 ;
184 ; Outputs: none.
185 ;
186 ; Global Used: none.
187 ;
188 ; Global Written: none.
189 ;
190 ; Procedures Called: none.
191 ;
192 ; Procedures Called By: 1_f_handler
193 ;
194 ; Description: this routine is called to call the routine whose entry
195 ; point is in ML.
196 ;
197 ; *****
198
199
200 jne_hl ja (hl)

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81F5* 00 7E 00
81F6* 00 96 02
81F8* C9

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```

81PC* 00 7E 00
81P* 00 36 02
8202* 00 5F 04
8209* C9

```

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8206* 00 7E 00
8209* 00 46 02
820C* 00 4E 04
820P* 00 56 06
8212* 00 6E 08
8219* 00 66 09
821B* C9

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;=====
;
; I_F2
;
; Inputs: ix -> first parameter
;
; Outputs: a <- value of first parameter
;          d <- value of second parameter
;
; Globals Used: none.
;
; Globals Written: none.
;
; Procedures Called: none.
;
; Procedures Called By: i_f_handler
;
; Description: this is the interface routine for write services with
;              two parameters.
;
;=====
;
; I_F2          ld     a, (ix)
;               ld     d, (ix+2)
;               ret
;
;=====
;
; I_F3
;
; Inputs: ix -> first parameter
;
; Outputs: a <- value of first parameter
;          d <- value of second parameter
;          e <- value of third parameter
;
; Globals Used: none.
;
; Globals Written: none.
;
; Procedures Called: none.
;
; Procedures Called By: i_f_handler
;
; Description: this is the interface routine for write services with
;              three parameters.
;
;=====
;
; I_F3          ld     a, (ix)
;               ld     d, (ix+2)
;               ld     e, (ix+4)
;               ret
;
;=====
;
; I_F4
;
; Inputs: ix -> first parameter
;
; Outputs: a <- value of first parameter
;          b <- value of second parameter
;          c <- value of third parameter
;          d <- value of fourth parameter
;          hl <- value of fifth parameter
;
; Globals Used: none.
;
; Globals Written: none.
;
; Procedures Called: none.
;
; Procedures Called By: i_f_handler
;
; Description: this is the interface routine for write services with
;              five parameters.
;
;=====
;
; I_F4          ld     a, (ix)
;               ld     b, (ix+2)
;               ld     c, (ix+4)
;               ld     d, (ix+6)
;               ld     e, (ix+8)
;               ld     hl, (ix+10)
;               ret
;
;=====
;
; end

```

15-May-84 9:52:10

NO ERRORS DETECTED

15-NOV-04 9:51:21

264



```

73
74 1 Global variables
75
76 sprites      equ     sprdata-2      location of sprites data
77 name         equ     sprites-2      name of common string var.
78 gflags       equ     name-1         global flags
79 max          equ     0              1 bit 0 - sprm < 0
80 autosave     equ     1              1 bit 1 - autosave mode flag
81 max_sprites   equ     gflags-1      number of sprites allocated
82 screen_ht     equ     max_sprites-1  number of lines of screen used by
83             1 sprites
84 bgcolor       equ     screen_ht-1    inherent background color
85 command       equ     bgcolor-1     number of command being executed
86 sarea         equ     command-12     parameter block
87 stretch      equ     sarea-5       parameter block
88
89 1 Data structures
90
91 1 parameter block
92 svc_code      equ     sarea         code identifying service
93 num_sarea     equ     svc_code-1    number of sarea found
94 sarea1        equ     num_sarea-1   first sarea
95 sarea2        equ     sarea1-2
96 sarea3        equ     sarea2-2
97 sarea4        equ     sarea3-2
98 sarea5        equ     sarea4-2     next available sarea
99
100 1 Sprite data offsets
101 pos           equ     0              frame position
102 col           equ     pos+1         column position
103 width        equ     col+1         sprite width
104 height       equ     width+1        sprite height
105 color        equ     height+1       sprite color
106 offset       equ     color+1        definition address
107 flags        equ     offset+2       local sprite flags
108 alloc        equ     0              1 bit 0 - allocated
109 overprite     equ     1              1 bit 1 - sprite overprate something
110 lhc          equ     2              1 bit 2 - sprite in lower half screen
111
112
113
114 ;!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
115 ;
116 ; Init_Sprite
117 ;
118 ; Input: 0 = number-sprites, the number of sprites for which memory is
119 ;         to be allocated.
120 ;
121 ; Output: BC = 0000 - OK
122 ;         0001 - not enough memory
123 ;
124 ; Global Pass: 0x70F
125 ;
126 ; Global Written: screen_ht
127 ;               gflags
128 ;               max_sprites
129 ;               sprites
130 ;               sprites data structure
131 ;
132 ; Procedures Called: none.
133 ;
134 ; Procedures Called By: 1_P_HM010R
135 ;                       user program
136 ;
137 ; Description: This routine allocates memory for sprite data. (max_sprites)
138 ; is set to number-sprites. One sprite requires 8 bytes of
139 ; data. If there is not enough free memory between (HM100)
140 ; and the sprites global variables, then 0 is returned.
141 ; Otherwise, (sprites) is set to point to the start of the
142 ; sprite data area and the sprite data is cleared.
143 ;
144 ;!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
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300 |=====
301 |
302 | CHECKS_ID
303 |
304 | Input: 8 = sprite-id
305 |
306 | Outputs: 00 is out if sprite-id is legal, reset if it is illegal
307 |
308 | Globals Read: sprites
309 |          sprite data structure
310 |
311 | Globals Written: none.
312 |
313 | Procedures Called: scr_addr
314 |
315 | Procedures Called By: out_sprite
316 |          screen_sprite
317 |          ch_lss
318 |          ch_addr
319 |          overlay
320 |
321 | Description: this routine checks (1) that sprite-id is in the range
322 |             00..max_sprites-1) and (2) that the record for that
323 |             sprite-id has been initialized.
324 |
325 |=====
326 |
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337 |
338 |
339 |
340 |
341 |=====
342 |
343 | SET_AUTOWRAP
344 |
345 | Input: 8 = 0 - off
346 |       < 0 - on
347 |
348 | Outputs: 00 = 0000 - OK
349 |
350 | Globals Read: none.
351 |
352 | Globals Written: gflags
353 |
354 | Procedures Called: none.
355 |
356 | Procedures Called By: 1_P_HANDLER
357 |          user program
358 |
359 | Description: this routine sets the autowrap flag according to the
360 |             value of A. If A = 0, the flag is reset; otherwise
361 |             the flag is set.
362 |
363 |
364 |=====
365 |
366 |
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372 |
373 |
374 |
375 |
376 |
377 |
378 |=====
379 |
380 | DISPLAY_CHAR
381 |
382 | Input: 8 = row
383 |       C = column
384 |       DE = location of dot pattern for char to be written
385 |
386 | Outputs: DE = location of next char
387 |
388 | Globals Read: none.
389 |
390 | Globals Written: CHTEL
391 |
392 | Procedures Called: SET_PRINT_POS
393 |          WRITE_CHAR
394 |
395 | Procedures Called By: out_sprite
396 |          screen_sprite
397 |
398 | Description: this routine displays the specified character at the
399 |             specified screen location.
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02P2* 21 5C00
02P3* 22 0040
02P0* C1
02P0* 01
02P4* C0

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02P0* 51 0FF0
02P0* 50
01P0* 00
0500* 30
0301* 00
0102* 32 1P
0104* 00
0305* 30
0506* C9

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0307* P5
0308* D5
0500* 05
0536* P5
0505* C0 E292*
0508* 50 00
0510* 01 0003
0515* C5 0516*
0516* C0 1230*
0518* 50 02
0510* D5
051C* C0 1230
051P* 01
0520* D0 00 03
0525* 30 00 00
0526* 02
0327* 00
0328* D0 70 30

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417      ld      hl, 5C00h
418      ld      (CM70L), hl      ; restore (CM70L) to original value
419      ds_0=1      pop      bx
420      pop      hl
421      ret
422
423      ;=====
424      ;
425      ; VISIBLE
426      ;
427      ; Inputs: 0 = row
428      ;          C = column
429      ;
430      ; Outputs: CF set if BC is a legal screen location, otherwise CF as reset
431      ;
432      ;
433      ; Globals Used: screen_ht
434      ;
435      ; Globals Written: none.
436      ;
437      ; Procedures Called: none.
438      ;
439      ; Procedures Called By: out_write
440      ;                       erase_write
441      ;
442      ; Description: this routine checks that the specified row and column
443      ;               lie within the legal ranges for the screen width and
444      ;               height. If so, the carry flag is set, otherwise it is
445      ;               reset.
446      ;
447      ;=====
448      ;
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0330 00 71 01          299 10 (in=col), 0
0331 30 0F05          300 0, (backcolor)
0332 04 00          301 11111000b
0333 00 54 04          302 (in=col)
0334 32 9C00          303 1d (setto_0), 0      ! set attr to screen background color
                                ! and write foreground color
0335 11 0100          304 10 do, 100h
0336 07          305 and 0
0337 00 92          306 obf hi, do
0338 0E          307 oo do, hi      ! do -> first scan of write times
                                ! minus 100h, acts like (Cn005)
                                ! = row count
0339 30 00          308 10 l, 0
0340 C0          309 push bc
0341 03          310 (col), hi
0342 40          311 10 0, 1      ! c = leftmost column of write
0343 03          312 oo (sp), hi
0344 36 00          313 10 h, 0      ! h = column count
0345 CD 02FE          314 cell vtable      ! is EC a legal screen location?
0346 3E 20          315 jr 0, rtcher      ! yes, so jump
0347 36 0F06          316 10 0, (oflags)      ! is outsize mode set?
0348 CE 4F          317 btt outsize, 0
0349 2E 36          318 jr z, nextchar      ! if not outsize mode then jump
0350 3E 1F          319 1d col_loo      ! loop current column onto screen
0351 00          320 co 0
0352 30 01          321 jr no, chrool
0353 79          322 00 0
0354 06 30          323 sub out_colo
0355 4F          324 10 c, 0
0356 1E 05          325 jr col_loo
0357 09          326 push hi
0358 21 0F09          327 10 hi, screen_ht
0359 70          328 10 0, (hi)      ! save current row onto screen
0360 30          329 dec 0
0361 0E          330 co b
0362 30 07          331 jr nc, contl
0363 0E          332 oo of, of'
0364 7C          333 10 0, b
0365 96          334 sub (hi)
0366 47          335 1d b, 0
0367 00          336 = of, of'
0368 08          337 jr col_loo
0369 00          338 pop hi
0370 16 01          339 10 0, h
0371 01          340 nr 1
0372 20 04          341 jr no, n_store
0373 00 70 00          342 10 (in=mem), 0      ! first time through loops, store
0374 00 71 01          343 10 (in=col), c      ! addresses screen location
0375 C9          344 no_n_store
0376 C6 02FF          345 push bc
0377 C1          346 cell GB7_CM00      ! check if writing the char will
0378 0E 20          347 pop bp      ! overwrite something
0379 00          348 co 0
0380 30 04          349 jr 0, no_overwrite
0381 00 CE 07 CE          350 set overwrite, (in=oflags)      ! save fact that something
                                ! was overwritten
0382 C0 0308          351 no_overwrite cell display_char
0383 0C          352 inc 0      ! increment column location
0384 14          353 inc h      ! increment column count
0385 00 7E 03          354 10 0, (in=col)
0386 0C          355 co h
0387 10 00          356 jr nz, inner1      ! jump if some columns to write
0388 04          357 inc b      ! also increment row position
0389 3C          358 inc l      ! increment row count
0390 0C 7E 03          359 10 0, (in=height)
0391 00          360 oo l
0392 10 09          361 jr no, outer1      ! jump if more rows to write
0393 C1          362 pop bc
0394 0E          363 01 0
0395 01          364 pop hi
0396 01          365 pop do
0397 01          366 pop r1
0398 00 C0 07 4E          367 bit overwrite, (in=oflags)
0399 00 C0 07 EE          368 rep overwrite, (in=oflags)
0400 26 44          369 jr nz, overate      ! write overwrite something
0401 01 0000          370 1d b, 0      ! return 00 in BC, nothing overwritten
0402 C0          371 1d 0
0403 00          372 ret bc, 0100h
0404          373 ret
0405          374
0406          375
0407          376
0408          377
0409          378
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8426* 83
8427* P3
8428* C0 8381*
8428* C8 41
8428* 20 1E
8428* 0C 7E 00
8432* 82
8433* 21 0FF8
8436* C0 4E
8438* 28 08
843A* C0 7F
843C* 28 04
8438* 21 0FF9
8441* 86
8442* 57
8443* 00 7E 01
8446* 83
8447* 5F
8448* P1
8449* P3
844A* C0 8107*
844D* P1
8448* 81
8448* C9

```

```

8490* 00 8282*
8493* 38 04
8493* 01 0003
8496* C9
8499* C0 825F*
849C* 00 72 00
849F* 0C 73 01
8482* 01 0000
8485* C9

```

```

8486* C0 8282*
8489* 38 03
8488* 01 0003
844F* P3
8489* 1E 07
8473* 86
8472* 30 05
8476* P1
8478* 01 0003
8478* C9
8479* P1
847d* C0 825F*
847D* 00 72 04
8480* 01 0000
8483* C9

```

```

73d move_sprite push hl
737 push af
738 call @move_sprite
739 bit 8, a ;if an error occurred, exit
740 jr nz, @_exit
741 ld a, (i+screen) ;update sprite position
742 add a, d
743 ld hl, gflags
744 bit outscreen, (hl)
749 jr s, rce_ok
746 bit 7, a ;is rce negative?
747 jr s, rce_ok ;no, so jump
748 ld hl, screen_ht ;yes, so add it to (screen_ht)
749 add a, (hl)
750 rce_ok ld d, a
751 ld a, (i+scr)
752 add a, a
753 ld a, a
754 pop af
755 push af
756 call @put_sprite
757 @_exit pop af
758 pop hl
759 ret
760
761 ;=====
762 ;
763 ; CH_LOC
764 ;
765 ; Inputs: 4 = sprite-id
766 ; 0 = new rce
767 ; 0 = new col
768 ;
769 ; Outputs: SC = 1000 - OK
770 ; = 1003 - illegal sprite-id
771 ;
772 ; Globals Read: none.
773 ;
774 ; Globals Written: sprite data structure
775 ;
776 ; Procedures Called: check_id
777 ; spr_addr
778 ;
779 ; Procedures Called By: L_P_HANDLER
780 ; user program
781 ;
782 ; Description: this routine updates the rce and coluen in the record for
783 ; the sprite identified by sprite-id.
784 ;
785 ;=====
786 ;
787 ;
788 ch_loc call check_id ;validate sprite-id
789 jr c, ch_l_id_ok ;if id is ok then jump
790 ld bc, 0003 ;else return 03 in SC
791 ret
792 ch_l_id_ok call spr_addr ;ie -> loc of sprite record
793 ld (i+rce), d
794 ld (i+col), a
795 ld bc, 00 ;return 00 in SC
796 ret
797
798 ;=====
799 ;
800 ; CH_d77R
801 ;
802 ; Inputs: 4 = sprite-id
803 ; 0 = new color
804 ;
805 ; Outputs: SC = 0000 - OK
806 ; = 0003 - illegal sprite-id
807 ; = 0005 - illegal color
808 ;
809 ; Globals Read: none.
810 ;
811 ; Globals Written: sprite data structure
812 ;
813 ; Procedures Called: check_id
814 ; spr_addr
815 ;
816 ; Procedures Called By: L_P_HANDLER
817 ; user program
818 ;
819 ; Description: this routine puts a new color into the record for the
820 ; sprite identified by sprite-id.
821 ;
822 ;=====
823 ;
824 ;
825 ;
826 ;
827 ch_addr call check_id ;validate sprite-id
828 jr s, ch_e_id_ok ;if id is ok then jump
829 ld bc, 0003 ;else return 03 in SC
830 ch_e_id_ok push af ;validate color
831 ld a, 7
832 ex d
833 jr nz, ch_e_color_ok ;jump if color ok
834 pop af
835 ld bc, 0005 ;else return 03 in SC
836 ret
837 sh_e_color_ok pop af
838 call spr_addr
839 ld (i+color), d
840 ld bc, 0 ;return 00 in SC
841 ret
842

```













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A	Reserved	ALLOC	0000	ATTASP	SCED	ATTRST	ECEF	AUTDWR	0001
B	Reserved	BACRGC	00P8	BCDOK	E4P8	BCDOK	E507	BLAC00	E52E
BDROCR	SC4E	SCRDRY	00PE	C	Reserved	CMARS	SC36	CMER00	E2E2
CNT6L	E890	CH9ATT	E465	CM9A9C	E4T9	CM9A91	E46E	CM9LCC	E450
CM9L91	E459	CHADU1	E35P	CM9A92	E3P5	CLEAA0	E258	CL500	097P
CL9607	ETCA	CL9T08	EE45	CCL	0001	CCLDA0	0004	CLC000	E2EF
COL9LO	E354	COMMAN	098T	CONY1	E360	CONY2	E405	CAEATF	E27E
C9LOD0	E3EA	D	Reserved	CC9E91	E2PE	DISPLA	E20A	DDW491	E813
ODWNR0	E904	ODWNR5	ET07	E	Reserved	ENDUGM	E253	ERASE0	E361
ERADCH	E406	EXEXIT	E421	ET0D0	E3C0	GETACN	E910	GPLAGS	0PPE
GL0BAL	0010	H	Reserved	HEIGHT	0003	MS9CON	E674	MS9CON	E506
ID9DK	E2E4	INITSC	E4E9	INIT9S	E219	INNEA1	E346	INNE92	E3DE
L	Reserved	LEPT91	E609	LEPT90	E5P0	LEPT95	E5E0	LEPT97	E608
LMS	0002	M	Reserved	MA99CD	0020	MA99AD	0016	MA99SP	0PFA
MDVE95	E426	MB9EIT	E440	NAME	0PPE	NEG	0000	NE9TCH	E30A
NE9T95	E5C9	NO9CLE	E5A9	NO9CLL	E4EE	N99C9E	E38T	NO9STD	E37A
NUN9PA	08EC	NITCHA	E40C	N99SPR	E764	OP9	E304	OUT901	E343
OUT9E2	E3D9	OVEAL9	E464	CVEA9A	0001	QVRW90	E3A0	Q9E917	E4E3
OD109N	E48P	OD9K1	E493	P99MS	0PPE	Q99M1	0PPE	Q99M2	0PPE
Q99N3	0P01	P99M4	0P03	Q99MS	0PPE	Q91TMA	0005	PSW	Reserved
QUT9SP	E30T	Q9E91T	E395	Q91D9G	E310	Q99T0P	SC92	ALC9T9	E430
ADM	0000	Q9W9LO	E365	Q9W9OK	E442	Q91NNN	E44C	AT9OUT	E440
RT9TAB	E456	Q9LJCP	E3P5	Q99E91	E206	SCAATC	08E3	SCRE9N	0P9P
SELECT	I230	SENCTV	0500	SET9AT	0592	SET9AU	E2C9	SET9PR	E9DE
SPLAGS	000T	SH9C9	E4P1	SP	Reserved	SP9C00	E000	SPA1TE	0PPE
SP99AD	E25P	SV99CO	08EE	TVPLAG	SC3C	UC99CN	E590	UC1	E599
UPDC9L	E5E0	UPD99L	E712	UPD9CO	E954	UPD9AD	E6E6	UP91VN	E79E
UP9OUT	E7EF	UP9SCA	E7TE	UA99CN	E73A	URI	E720	VISIEL	E2FE
VS9CON	E950	V59T00	E740	V9SC9C	ET71	W10TN	0002	WR1TE9	E990
WRT9MA	E37A	WRT9SP	E50E	WTS9A	ETS9	T	SC3A	VES9CO	E408

No errors detected

```
1 NAME IOLOM
2 SuETTL Low level I/O module
3
4
5 ;*****
6 ;
7 ; Low Level I/O Module
8 ;
9 ; Inputs: parameters to desired service in appropriate registers.
10 ;
11 ; Outputs: none.
12 ;
13 ; Description: This module provides the following low level I/O services:
14 ;
15 ; Set_Print_Pos - sets the print position
16 ; Write_Char - writes a character to the current print position
17 ; Get_Char - returns the character code of the character at the
18 ; desired position
19 ;
20 ; These services are based on the routines SETCUR, WPCNAR,
21 ; and GTCHAR of the multi-processor support component of the
22 ; Applications Development Library.
23 ;
24 ;*****
25
26
27 ; Imports (Note: all symbols in upper case are imported)
28
29
30 ; From the Name ABN
31
32 ; Variables
33
34 UDG EQU SCEDM
35 ATTA_P EQU SCEDM
36 MASH_P EQU SCEDM
37 P_FLAG EQU SC91M
38 V10M00 EQU SIC2M
39
40 ; Exports
41
42 ; Variables
43
44 ;
45
46 ; Services
47
48 ;
49
50 ; Locals
51
52 ; Constants
53
54
55 =001E
56 =3C00
57 =B33A
58
59
60
61 ; Variables
62
63 ORG @EE90H
64
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78
79 ;*****
80 ;
81 ; WBIT8_CH46
82 ;
83 ; Inputs: A = character code.
84 ;
85 ; Outputs: 6C = 0000 = OK
86 ;           00XX = error; X4 < 00
87 ;
88 ; Globals Read: UDG
89 ;               grtbl
90 ;               chtbl
91 ;               linlen
92 ;               modeb
93 ;
94 ; Globals Written: none.
95 ;
96 ; Procedures Called: ldettr
97 ;                   ldeeen
98 ;                   udeett
99 ;                   stpeen
100 ;
101 ; Procedures Called By: DISPLAY_CH46
102 ;                       V_3(KBL)
103 ;
104 ; Description: this service writes the specified character to the current
105 ;               print position.
106 ;
107 ;*****
108
109
110
111 write_chor      push    of
112                even    de
113                push    hl
114                call    ldettr      ; entry with code in a
115                                     ; get attribute and mode controls
116
117
118
119 arch01          call    ldeeen      ; entry to use attribute values in
120                                     ; internal variables without reloading
121                                     ; load registers for current position
122                                     ; set cursor position from "cursor"
123                                     ; hide/display file address from "afcore"
124                                     ; encode for chor. to be displayed
125
126
127
128                even    bc          ; save cursor position
129                ca       80h        ; test if scroll >intable (20h-7fh)
130                jr       c, Wrch12   ; yes
131                ca       90h        ; test if ext. graphics (80h-8fh)
132                jr       c, Wrch11   ; user-defined graphics(90h-8fh)
133                ld       de, (UDG)   ; user-defined graphics(90h-8fh)
134                b        b          ; adjust address-10h
135                orl      70h        ; adjust for index into table
136                jr       Wrch13
137 arch11          ld       bc, (grtbl) ; address of graphics table
138                orl      40h        ; adjust for index into table
139                jr       Wrch13
140 arch12          ld       bc, (chtbl) ; character table
141 arch13          orl      de, hl     ; de=even in display file
142                ld       hl, 0       ; l = a
143                ld       hl, hl     ; code is index into table
144                add      hl, hl
145                add      hl, hl
146                add      hl, de
147                pop      bc
148                orl      de, hl     ; test if end of line
149                orl      e          ;
150                jr       c, (linlen) ;
151                jr       nz, Wrch14   ;
152                inc      e          ; start of new line
153                dec      e          ; skip line no.
154                ld       c, e       ; linlen=1+start of line
155                jr       Wrch15
156 arch14          ld       e          ; here to put character in display file
157 arch15          push    bc          ; cursor position
158                push    hl          ; display file addr.
159                ld       e, (modeb) ;
160                ld       b, -1      ;
161                rrr      c, arch3    ; if xoring new into old
162                inc      b          ; de-1 if xoring =0 if not
163                rrr      b
164                rrr      e
165                orl      e, e       ;
166                ld       c, e       ; de-1 for inverse =0 if not
167                ld       e, 0       ; scan count
168                and      e
169                orl      de, hl     ;
170                orl      ef, ef     ;
171                ld       e, (de)    ;
172                and      d          ;
173                orl      hl, hl     ;
174                orl      c          ; invert if de-1
175                ld       (de), e     ; update display file
176                orl      ef, ef     ;
177                inc      d          ; next scan
178                inc      hl         ; next byte in char. file
179                dec      e          ; test if more scans
180                jr       nz, arch1   ;
181                dec      d          ; adjust df addr.
182                orl      de, hl     ; df addr. to hl
183                call    udeett      ; update attribute byte

```

```

E2PP  E1
E900  C1
S901  00
E903  23
E903  CC S907
S906  0E 00
S908  06 00
E92A  E1
E90E  D1
E90C  P1
E9ED  C9

164
165
166
167
168 wrchrt      call  rtoeen
169      goodret  ld    a, 0
170      errret   ld    b, 0
171      pop      hl
172      pop      de
173      pop      sf
174      ret
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E94E 20 24      292      JR      nc, gteh4
E94A 3C          293      dec      a           I am-1 for inverse
E94E 4P          294      ld      a, a           I am-1 for match -1 for inverse
E94C 84 07      295      ld      b, 7
E94E 24          296      inc      a           I next even in of
E94A 22          297      inc      hl          I next byte in char.set
E950 24          298      zd      a, (do)
E951 48          299      or      (hl)
E952 09          300      or      a
E952 20 19      301      jr      na, gteh4           I no match
E952 10 P7      302      djnz    gteh2l          I for next even
303
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E937 79          308      ld      a, a           I am-2 if match on inverse
E940 C1          309      pop     cc
E940 C1          310      pop     cc
E940 C1          311      pop     bc
E940 4P          312      ld      a, a           I bcount am-1 if match on inverse
E95C 3A 899B    313      zd      a, (gtinev)       I adjust char.eode
E95P 9C          314      cub      a           I amchar.code
E960 7E 20      315      or      20h          I test if eode
E962 20 02      316      jr      nc, gteh22
E964 9C          317      inc      c           I test if match on inverse eode
E962 20 02      318      jr      nc, gteh22
E967 28 94      319      ld      a, 8fh          I set code for graphics block
E969 4P          320      ld      a, a
E966 96 8D      321      ld      b, 0
E96C 18 2C      322      jr      nc, eodl          I char.eode in a and in bc
323
E98E 81          324      gteh4      pop     hl          I here when no action
E98P 11 8008    325      ld      de, 8           I look at next char. in set
E972 19          326      add      hl, de          I char. set
E973 C1          327      pop     de           I char. in of
E974 C1          328      pop     bc           I char.count
E972 10 8D      329      djnz    gteh2          I look again
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391 |-----|
392 |
393 | UPGDSH
394 |
395 | Inputs: 9 = row in internal format
396 |         C = column in internal format
397 |
398 | Outputs: none
399 |
400 | Globals Read: none.
401 |
402 | Globals Written: none
403 |
404 | Procedures Called: calcpos
405 |                 nlspos
406 |
407 | Procedures Called By: col_print_000
408 |
409 | Description: This routine set the print position to the specified value.
410 |
411 |-----|
412 |
413 |
414 | nlspos          | enter here with bc=line/col.in
415 |                 | internal format
416 |
417 |                 |
418 |                 |
419 |                 | calculate position
420 |                 | store updated position and return
421 |
422 |-----|
423 |
424 | CALCPOS
425 |
426 | Inputs: 8 = row in internal format
427 |         C = column in internal format
428 |
429 | Outputs: HL = address in display file
430 |
431 | Globals Read: linlen
432 |
433 | Globals Written: none.
434 |
435 | Procedures Called: lnsu
436 |
437 | Procedures Called By: pos_shor
438 |                 udsposn
439 |
440 | Description: This routine calculates the position in the display file
441 |               corresponding to the specified row and column position.
442 |
443 |-----|
444 |
445 |
446 | calcpos        | get display file addr.
447 |               | add column offset
448 |               |
449 |               |
450 |               |
451 |               |
452 |               |
453 |               |
454 |               |
455 |
456 |-----|
457 |
458 | LNSU
459 |
460 | Inputs: 8 = row in internal format
461 |
462 | Outputs: HL = address in display file of start of line 8.
463 |
464 | Globals Read: WIDH00
465 |
466 | Globals Written: none.
467 |
468 | Procedures Called: none.
469 |
470 | Procedures Called By: calcpos
471 |
472 | Description: This routine calculates the address of the start of row 8
473 |               in the display file. This is the first byte of the first
474 |               scan line.
475 |
476 |-----|
477 |
478 |
479 | lnsu          | get display file address
480 |               | for start of line in b
481 |               |
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0A14 7C
0A15 0F
0A16 0F
0A17 0F
0A18 06 03
0A1A FA 36
0A1C C6 4C
0A1E 20 01
0A20 F6 20
0A23 67
0A25 C9

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0A14
0A24 P3
0A25 SA 3C80
0A26 S1 E89A
0A27 S0 3C6E
0A28 S1 889C
0A29 SA 3C31
0A2A 0F
0A2B S1 8896
0A2C B1
0A2D C6

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0A3A 00
0A3B 00
0A3C 00
0A3D 00
0A3E 00
0A3F 00
0A40 00
0A41 00

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0A43 0F
0A44 0F
0A45 0F
0A46 00
0A47 06
0A48 00
0A49 00

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0A4A 00
0A4B 00
0A4C 00
0A4D 00
0A4E 00
0A4F 00

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0A52 0F
0A53 0F
0A54 0F
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8b56	00	729	defb 00000000b		
8b57	00	730	defb 00000000b		
8b58	00	731	defb 00000000b		
8b59	00	732	defb 00000000b		
		733			
8b5b	00	734	defb 00000000b	1 code 84	graphics
8b5c	00	735	defb 00000000b		
8b5d	00	736	defb 00000000b		
8b5e	00	737	defb 00000000b		
8b5f	00	738	defb 00000000b		
8b60	00	739	defb 00000000b		
8b61	00	740	defb 00000000b		
		741			
8b62	00	742	defb 00000000b	1 code 85	graphics
8b63	00	743	defb 00000000b		
8b64	00	744	defb 00000000b		
8b65	00	745	defb 00000000b		
8b66	00	746	defb 00000000b		
8b67	00	747	defb 00000000b		
8b68	00	748	defb 00000000b		
8b69	00	749	defb 00000000b		
		750			
8b6a	00	751	defb 11110000b	1 code 86	graphics
8b6b	00	752	defb 11110000b		
8b6c	00	753	defb 11110000b		
8b6d	00	754	defb 11110000b		
8b6e	00	755	defb 11110000b		
8b6f	00	756	defb 11110000b		
8b70	00	757	defb 11110000b		
8b71	00	758	defb 11110000b		
		759			
8b72	00	760	defb 11111111b	1 code 87	graphics
8b73	00	761	defb 11111111b		
8b74	00	762	defb 11111111b		
8b75	00	763	defb 11111111b		
8b76	00	764	defb 11111111b		
8b77	00	765	defb 11111111b		
8b78	00	766	defb 11111111b		
8b79	00	767	defb 11111111b		
		768			
8b7a	00	769	defb 00000000b	1 code 88	graphics
8b7b	00	770	defb 00000000b		
8b7c	00	771	defb 00000000b		
8b7d	00	772	defb 00000000b		
8b7e	00	773	defb 00000000b		
8b7f	00	774	defb 00000000b		
8b80	00	775	defb 00000000b		
8b81	00	776	defb 00000000b		
		777			
8b82	00	778	defb 00000000b	1 code 89	graphics
8b83	00	779	defb 00000000b		
8b84	00	780	defb 00000000b		
8b85	00	781	defb 00000000b		
8b86	00	782	defb 00000000b		
8b87	00	783	defb 00000000b		
8b88	00	784	defb 00000000b		
8b89	00	785	defb 00000000b		
8b8a	00	786	defb 00000000b		
8b8b	00	787	defb 00000000b		
8b8c	00	788	defb 11110000b	1 code 8a	graphics
8b8d	00	789	defb 11110000b		
8b8e	00	790	defb 11110000b		
8b8f	00	791	defb 11110000b		
8b90	00	792	defb 11110000b		
8b91	00	793	defb 11110000b		
8b92	00	794	defb 11110000b		
8b93	00	795	defb 11110000b		
		796			
8b94	00	797	defb 11111111b	1 code 8b	graphics
8b95	00	798	defb 11111111b		
8b96	00	799	defb 11111111b		
8b97	00	800	defb 11111111b		
8b98	00	801	defb 11111111b		
8b99	00	802	defb 11111111b		
8b9a	00	803	defb 11111111b		
8b9b	00	804	defb 11111111b		
8b9c	00	805	defb 00000000b	1 code 8c	graphics
8b9d	00	806	defb 00000000b		
8b9e	00	807	defb 00000000b		
8b9f	00	808	defb 00000000b		
8ba0	00	809	defb 00000000b		
8ba1	00	810	defb 11111111b		
8ba2	00	811	defb 11111111b		
8ba3	00	812	defb 11111111b		
8ba4	00	813	defb 11111111b		
8ba5	00	814			
8ba6	00	815	defb 00000000b	1 code 8d	graphics
8ba7	00	816	defb 00000000b		
8ba8	00	817	defb 00000000b		
8ba9	00	818	defb 00000000b		
8baa	00	819	defb 11111111b		
8bab	00	820	defb 11111111b		
8bac	00	821	defb 11111111b		
8bad	00	822	defb 11111111b		
8bae	00	823			
8baf	00	824	defb 11110000b	1 code 8e	graphics
8bb0	00	825	defb 11110000b		
8bb1	00	826	defb 11110000b		
8bb2	00	827	defb 11110000b		
8bb3	00	828	defb 11110000b		
8bb4	00	829	defb 11110000b		
8bb5	00	830	defb 11110000b		
8bb6	00	831	defb 11110000b		
8bb7	00	832	defb 11111111b	1 code 8f	graphics
8bb8	00	833	defb 11111111b		
8bb9	00	834	defb 11111111b		
8bba	00	835	defb 11111111b		
8bbb	00	836	defb 11111111b		
8bbc	00	837	defb 11111111b		
8bbd	00	838	defb 11111111b		
8bbe	00	839	defb 11111111b		
8bbf	00	840	defb 11111111b		
8bc0	00	841			
8bc1	00	842			
8bc2	00	843	end		

A	Reserved	ATTBYT	893A	ATTMSK	899C	ATTRP	8C8D	S	Reserved
C	Reserved	CALCAT	841A	CALCAL	8B22	COLCPO	89AP	CHRSST	3C00
CHTBL	8998	CDNUPM	89D0	CURPCS	8993	D	Reserved	OPADRS	8897
E	Reserved	ERRSET	890E	GC64+2	899A	GETACH	891P	GDORR8	8904
GRHST	893A	GRPST	8A3A	GR78C	8A92	GTCH1	882F	GTCH2	8834
GTCH23	894A	GTCH3	894E	GTCH31	894E	GTCH32	8969	GTCH4	888E
GTCH5	8989	GTCH6	899E	GTINH0	8998	H	Reserved	L	Reserved
LDSTTR	8A24	LDPDSH	89D0	LINL14	8994	LNEU	898C	M	Reserved
MSK48	8999	MASKRP	8C8E	PSW	Reserved	PRLAG	8C91	SCRS2	0318
SETAP8	890E	SP	Reserved	STPDSH	8807	UDG	8C78	UPDAT7	88E7
UPDAT1	8A04	UPDAT2	8812	UPDPC3	89A8	VIDHDD	8CC2	WACHK7	8803
WRCHD1	88A3	WRCH11	88E8	WRCH12	88C0	WRCH13	88C4	WRCH14	88DA
WRCH15	88D8	WRCH2	8808	WRCH3	88F4	WRCH5	88E8	WRCH7	88FA
WRIT88	8850								

No errors detected

## APPENDIX D

### TS2068 PCB Assembly and Schematic Diagram

The following Appendix contains the PCB Assembly Drawing, the PCB Parts List, and PCB Schematic Diagram (a "fold-out" page located just inside the back cover). The Table below contains some corrections to the Schematic Diagram.

#### \*\*\*TS2068 PCB Schematic Diagram Corrections\*\*\*

Page 34 of the Technical Manual shows pin 9 of the joystick ports grounded as it should be. The traces were left off the TS2068 PCB.

VR1: U3-33 goes to VR1/Q5

Q4: Connect base to R55/R54

Solder dots on horizontal lines below keyboard:

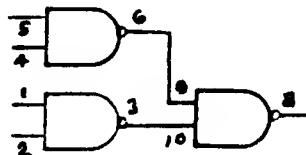
U12-4 to U3-65 (WR)

U12-5 to U3-66 (MREQ)

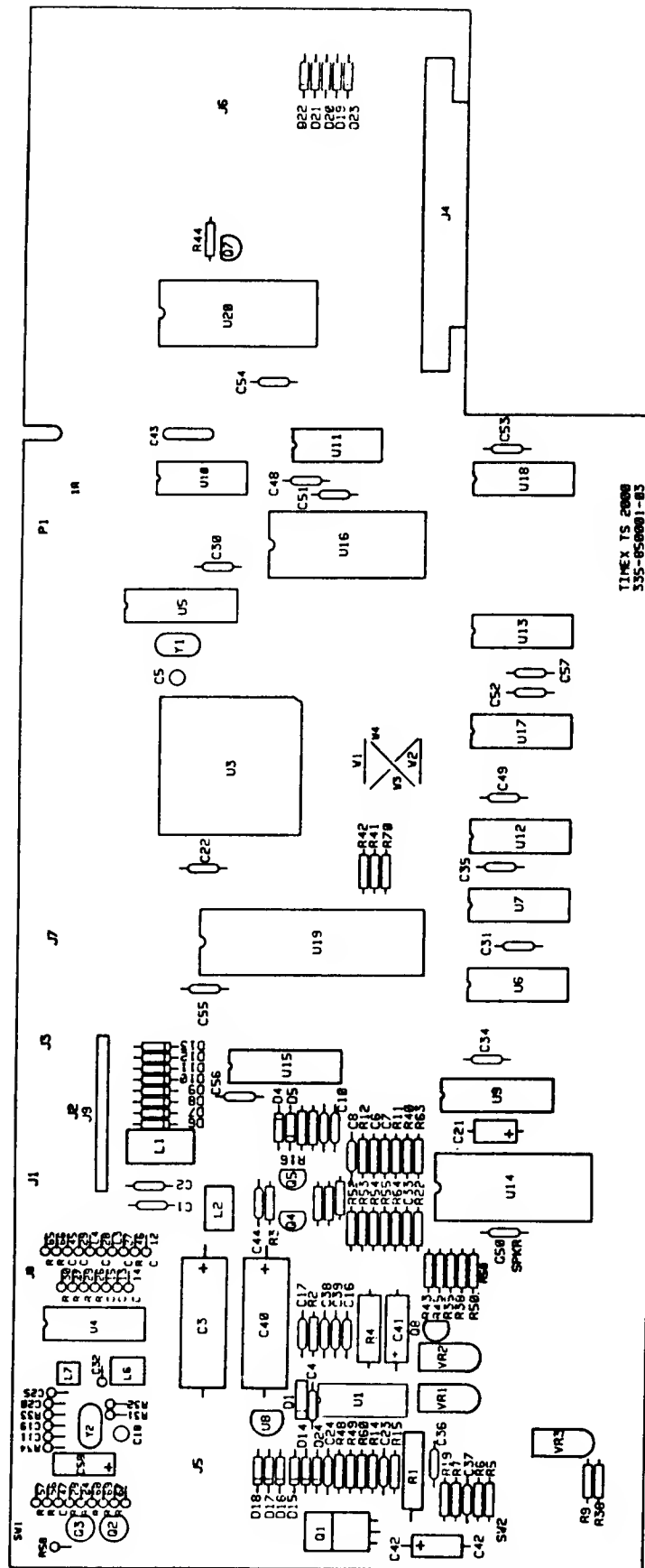
U5: U5-2 to U3-38 (A7R not A7RB)

Pl: Pl-4B +15V (not -15V)

U21:







TS2066 PC BOARD COMPONENT LAYOUT

# APPENDIX D

## TS2068 PARTS LIST

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
P.C.B. (Fabrication and Artwork)			REV 3A
CAP. 0.1 uf, Ceramic, Axial TEMP Z5U	C2,7,9,16,24,30 31,34,35,37,39,43 44,48,49,50,51,52 53,54,55,56,57	23	-20 +80% or GMV
CAP. 0.01 uf, Ceramic, Axial	C11,12,14,33,61 62,68,69	8	-20 +80% or GMV TEMP Z5U
CAP. 0.001 uf, Ceramic, Axial	C8,45,46,47	4	-20 +80% or GMV TEMP Z5U
CAP. 0.047 uf, Ceramic, Axial	C10,15,74,75	4	-20 +80% or GMV TEMP Z5U
CAP. 20pf Ceramic Axial	C23	1	-20 +80% or GMV TEMP Z5U
CAP. 39pf Ceramic Axial	C20	1	NPO
CAP. 43pf Ceramic Axial	C19	1	NPO
CAP. 56pf Ceramic Axial	C25	1	NPO
CAP. 75pf Ceramic Axial	C32	1	NPO
CAP.120pf Ceramic Disc	C59,63,64,65,72 73	6	-20 +80% or GMV TEMP Z5U
CAP.470uf, 25V AL Electro- lytic Axial	C3	1	
CAP. 1 uf, 16V MIN AL Electro- lytic Axial	C21	1	
CAP. 47 uf, 16V MIN AL Elec- trolytic Axial or Radial	C41	1	
CAP. 1000 uf, 12V MIN AL Electrolytic Axial	C40	1	LOW ESR
CAP. 1000 pf, 50V MIN FILM MYLAR	C36	1	+/- 20%
CAP. 100 uf, 10V MIN AL Elec- trolytic Axial	C58,67	2	
CAP. 6-50 pf, TRIMMER	C5,18	2	NPO
CAP. 0.47 uf Ceramic Axial	C60	1	-20 +80% or GMV TEMP Z5U
CAP. 33 uf TANTALUM	C71	1	+/- 20%

APPENDIX D  
TS2068 PARTS LIST  
(continued)

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
CAP. 68 pf Ceramic Axial	C70	1	-20 +80% or GMV TEMP Z5U
CAP. 24 pf Ceramic Axial	C29,27	2	-20 +80% or GMV TEMP Z5U
CAP. 47 pf Ceramic Axial	C28	1	-20 +80% or GMV TEMP Z5U
RES. 300 OHM, 1/4W, +/-5%, CF	R23	1	
RES. 200 OHM, 1/4W, +/-5%, CF	R19,50,54,55	4	
RES. 100 OHM, 1/4W, +/-5%, CF	R58	1	
RES. 240 OHM, 1/4W, +/-5%, CF	R24,28,56,57	4	
RES. 68 OHM, 1/4W, +/-5%, CF	R2	1	
RES. 680 OHM, 1/4W, +/-5%, CF	R13,68	2	
RES. 390 OHM, 1/4W, +/-5%, CF	R74	1	
RES. 1K OHM, 1/4W, +/-5%, CF	R11,33,34,35,36 38,42,62	8	
RES.1.5K OHM, 1/4W, +/-5%, CF	R41	1	
RES.1.8K OHM, 1/4W, +/-5%, CF	R29,30	2	
RES. 620 OHM, 1/4W, +/-5%, CF	R52	1	
RES. 2K OHM, 1/4W, +/-5%, CF	R22	1	
RES. 3K OHM, 1/4W, +/-5%, CF	R32	1	
RES.2.2K OHM, 1/4W, +/-5%, CF	R61	1	
RES. 110 OHM, 1/4W, +/-5%, CF	R53	1	
RES. 510 OHM, 1/4W, +/-5%, CF	R69	1	
RES.5.1K OHM, 1/4W, +/-5%, CF	R31	1	
RES. 10K OHM, 1/4W, +/-5%, CF	R16,40,60,70	4	
RES. 13K OHM, 1/4W, +/-5%, CF	R26,27	2	
RES. 20K OHM, 1/4W, +/-5%, CF	R44,45	2	
RES. 62K OHM, 1/4W, +/-5%, CF	R9,73	2	
RES.100K OHM, 1/4W, +/-5%, CF	R15,49	2	
RES.220K OHM, 1/4W, +/-5%, CF	R43	1	
RES. 75 OHM, 1/4W, +/-5%, CF	R46,67	2	
RES.1.10K OHM 1/4W, +/-1%, MF	R6	1	
RES.3.32K OHM 1/4W, +/-1%, MF	R5	1	
RES. 10K OHM, VARIABLE, LINEAR	VR1,2,3	3	
RES. 330 OHM, 0.5W, +/-5%, CF	R4	1	
RES. 56 OHM, 1/4W, +/-5%, CF	R65,71	2	
RES. 0.110 OHM, 3W, +/-5%, Wire Wound	R1	1	
RES. 20 OHM, 1/4W, +/-5%, CF	R63	1	
RES. 82 OHM, 1/4W, +/-5%, CF	R64	1	
RES. 22 OHM, 1/4W, +/-5%, CF	R66	1	
RES.680K OHM, 1/4W, +/-5%, CF	R14	1	
RES. 47K OHM, 1/4W, +/-5%, CF	R48	1	
RES.390K OHM, 1/4W, +/-5%, CF	R72	1	
RES.6.8K OHM, 1/4W, +/-5%, CF	R12	1	

APPENDIX D  
TS2068 PARTS LIST  
(continued)

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
DIODE 1N4148	CR4,5,6,7,8,9,10 11,12,13,14,15,16 17,18,19,20,21,22 23,24,25,26,27,28	25	
DIODE, Schottky 1N5821 or equivalent	CR1	1	
IC, UA 78S40 NPC, Switching Regulator	U1	1	
IC, SCLD	U3	1	
IC, LM1889N, Video Modulator	U4	1	
IC, 74LS244N	U5	1	
IC, TMS4416-15 (150NS) MOS Dynamic RAM	U6,7	2	
IC, UA 78L12 Regulator	U8	1	
IC, 74LS245	U9,15	2	
IC, 74LS157N	U10,11	2	
IC, TMS4416-20 (200NS) MOS Dynamic RAM	U12,13,17,18	4	
IC, AY-3-8912, Sound Gen. and I/O Port	U14	1	
IC, 23128 Mask ROM (16K X 8)	U16	1	
IC, CPU Z80A	U19	1	
IC, 2364 Mask ROM (8K X 8)	U20	1	
IC, 74LS00	U21	1	
TRAN. PNP D43C1	Q1	1	
TRAN. PNP 2N2907	Q3	1	
TRAN. PNP 2N3904	Q7,8	2	
TRAN. PNP 2N2222	Q5,4,2	3	

APPENDIX D  
TS2068 PARTS LIST  
(continued)

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
EMI Filter(Bifiler) 2.2mh	L1	1	
Inductor 230 uh	L2	1	
Inductor .33uh Axial	L3,4	2	
Inductor .12uh	L6,7	2	
Crystal Oscillator 14.112 MHz	Y1	1	
Crystal Oscillator 3.579545 MHz	Y2	1	
Switch SPDT, Rocker	SW2	1	
Switch Channel Select, SPDT Slide	SW1	1	
Video Jack Insulation Pad		1	Under J7
Jack, Right Angle RCA Video Jack	J7	1	Monitor
Jack, Mini Phone, EAR & MIC	J2,3	2	Tape
Jack, COAX, DC Power, 2 1/2 MM Pin	J1	1	
Jack, Phono	J8	1	Assembled to Shield, R.F.
Connector,Cartridge 2 X 18 Pin 0.1" Space	J4	1	Key between Contact 4&6
Connector,Flex Cable 14 Pin	J9	1	Keyboard
Connector,Joystick 9-Pin Male (D Type)	J5,6	2	Joysticks
Shield, R.F. Button		1	
Shield, R.F. Top		1	
Heat Sink	HS1	1	
Heat Sink Insulation Pad			

APPENDIX D  
TS2068 PARTS LIST  
(continued)

DESCRIPTION	COMPONENT DESIGNATION	QTY PER ASSY	COMMENTS
Socket, IC, 28 Pin		2	
Socket, IC, 40 Pin		1	
Speaker, 45 OHM, Mylar Cone		1	
Jumper Wire	W1, 2, 50	3	
Ferrite Bead	L5,8	2	
PC Board Assembly, Daughter		1	

# APPENDIX E

Expansion Buss Comparison of  
TS2068, Sinclair Spectrum and ZX81

## TS 2068

## SPECTRUM

## ZX-81

BOTTOM TOP

BOTTOM TOP

BOTTOM TOP

GND	1	GND
SPKR/TAPE	2	EAR
+ 15 v	3	A7R
+ 5 v	4	D <sub>7</sub>
N.C.	5	N.C.
PWR GND	7	D <sub>0</sub>
PWR GND	8	D <sub>1</sub>
CLK	9	D <sub>2</sub>
A <sub>0</sub>	10	D <sub>6</sub>
A <sub>1</sub>	11	D <sub>5</sub>
A <sub>2</sub>	12	D <sub>3</sub>
A <sub>3</sub>	13	D <sub>4</sub>
A <sub>15</sub> B	14	INT
A <sub>14</sub> B	15	NMI
A <sub>13</sub> B	16	HALT
A <sub>12</sub>	17	MREQB
A <sub>11</sub>	18	IORQB
A <sub>10</sub>	19	RDB
A <sub>9</sub>	20	WRB
A <sub>8</sub>	21	BUSAK
A <sub>7</sub>	22	WAIT
A <sub>6</sub>	23	BUSRQ
A <sub>5</sub>	24	RESET
A <sub>4</sub>	25	M <sub>I</sub>
N.C.	26	RFSHB
RGB Red	27	EXROM
RGB Grn	28	ROSCS
RGB Blu	29	BE
	30	IO A <sub>8</sub>
VIDEO	31	SOUND
GND	32	GND

A <sub>14</sub>	1	A <sub>15</sub>
A <sub>12</sub>	2	A <sub>13</sub>
5 v	3	D <sub>7</sub>
9 v	4	
0 v	6	D <sub>0</sub>
0 v	7	D <sub>1</sub>
CK	8	D <sub>2</sub>
A <sub>0</sub>	9	D <sub>6</sub>
A <sub>1</sub>	10	D <sub>5</sub>
A <sub>2</sub>	11	D <sub>3</sub>
A <sub>3</sub>	12	D <sub>4</sub>
IORQGE	13	INT
0 v	14	NMI
Video	15	HALT
Y	16	MEMRQ
V	17	IOREQ
U	18	RD
BUSRQ	19	WR
RESET	20	- 5v
A <sub>7</sub>	21	WAIT
A <sub>6</sub>	22	+12 v
A <sub>5</sub>	23	-12 v
A <sub>4</sub>	24	M <sub>I</sub>
ROMCS	25	RFSH
BUSAK	26	A <sub>8</sub>
A <sub>9</sub>	27	A <sub>10</sub>
A <sub>11</sub>	28	

SIDE A

SIDE B

5 v	1	D <sub>7</sub>
9 v	2	RAMCS
0 v	4	D <sub>0</sub>
0 v	5	D <sub>1</sub>
CLK	6	D <sub>2</sub>
A <sub>0</sub>	7	D <sub>6</sub>
A <sub>1</sub>	8	D <sub>5</sub>
A <sub>2</sub>	9	D <sub>3</sub>
A <sub>3</sub>	10	D <sub>4</sub>
A <sub>15</sub>	11	INT
A <sub>14</sub>	12	NMI
A <sub>13</sub>	13	HALT
A <sub>12</sub>	14	MREQ
A <sub>11</sub>	15	IOREQ
A <sub>10</sub>	16	RD
A <sub>9</sub>	17	WR
A <sub>8</sub>	18	BUSAK
A <sub>7</sub>	19	WAIT
A <sub>6</sub>	20	BUSRQ
A <sub>5</sub>	21	RESET
A <sub>4</sub>	22	M <sub>I</sub>
ROMCS	23	RFSH

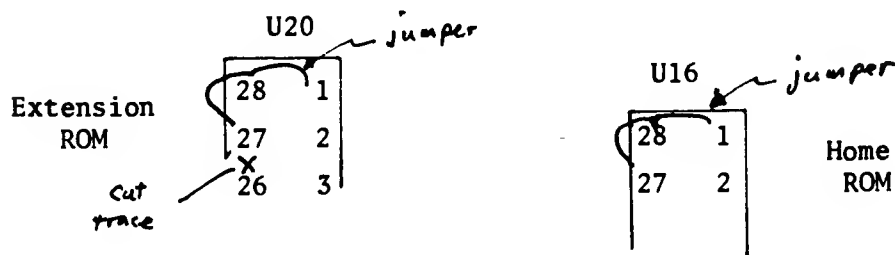
SIDE A

## APPENDIX F

August 1985  
Bob Orrfelt

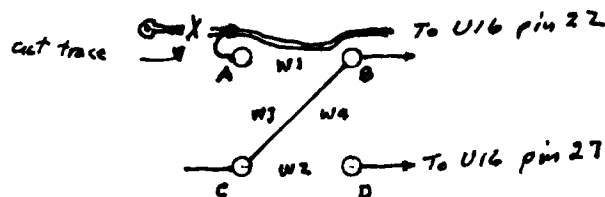
### TS2068 MODIFICATIONS FOR EPROMS

There are a number of errors in the TS2068 Home ROM and the Extension ROM. The errors can be corrected by using EPROMs. The following modifications are necessary:



#### Non-component side of the pcb,

0. Remove ROMs.
1. Cut the trace between U20-26 and U20-27
2. Jumper pins 1 to 28 to 27 on each socket.



#### Component side of pcb.

3. Remove the two zero ohm resistors W1 and W2.
4. Cut the trace just above and to the left of hole A.
5. Add a jumper from hole A to the trace. This connects  $\overline{MREQ}$  to U16 pin 22.
6. Add a jumper from hole C to hole B. This connects  $\overline{ROMCS}$  to U16 pin 20.
7. Use a 27128 (16K) EPROM for U16.
8. Use a 2764 (8K) EPROM for U20.



October 1985  
Bob Orrfelt

# Proposed TS2068 Home ROM Corrections and Improvements

NMI fix.  
006D 2801 JR Z,0070H

DELETE delay timing.  
0351 010100 LD BC,0001H  
0354 0B DEC BC  
0355 79 LD A,C  
0356 B0 OR B  
0357 20FB JR NZ,0354H  
0359 F1 POP AF  
035A 18D2 JR 032EH

Optional turn on message.  
(Last character add 80H)  
1118 Property of Bob  
1128 Orrfelt.....  
1138 .....  
1148 ..

INT -65536 etc. errors.  
33F1 F5 PUSH AF  
33F2 3C INC A  
33F3 B3 OR E  
33F4 B2 OR D  
33F5 C2E435 JP NZ,35E4H  
33F8 C3EF35 JP 35EFH

35E2 181A JR 35FEH  
35E4 F1 POP AF  
35E5 77 LD (HL),A  
35E6 23 INC HL  
35E7 73 LD (HL),E  
35E8 23 INC HL  
35E9 72 LD (HL),D  
35EA 2B DEC HL  
35EB 2B DEC HL  
35EC 2B DEC HL  
35ED D1 POP DE  
35EE C9 RET

35EF F1 POP AF  
35F0 2B DEC HL  
35F1 3691 LD (HL),91H  
35F3 23 INC HL  
35F4 3680 LD (HL),80H  
35F6 3C INC A  
35F7 18ED JR 35E6H

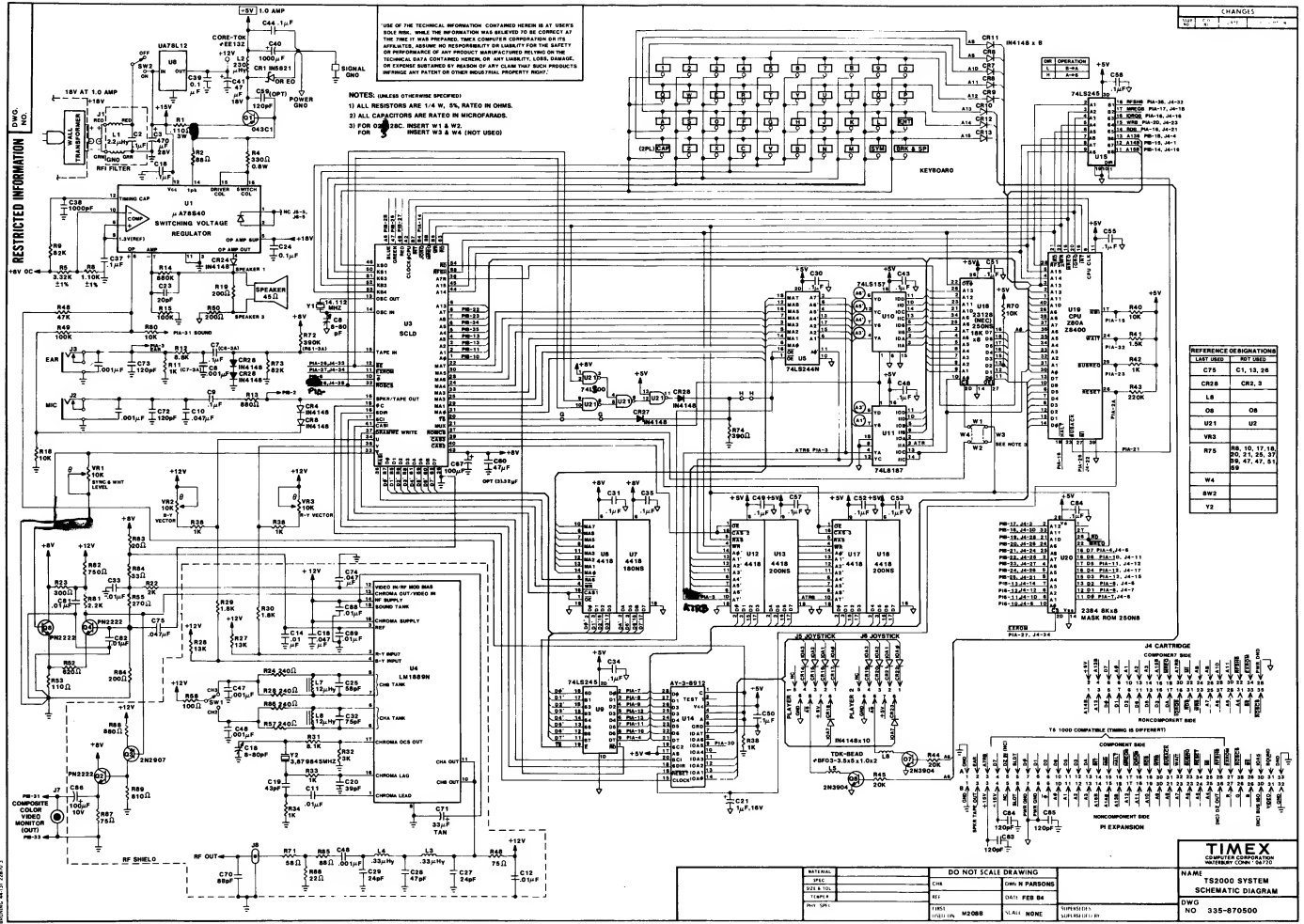
35F9 FFFFFFFF blanks  
35FD FF

USR chunk selection.  
389F E660 AND 60H  
38A1 2818 JR Z,38BEH  
38A3 D640 SUB A,40H  
38A5 FAB738 JP M,38B7H

Fix for Oliger EPROM programmer.  
(see May 85 Syncware, page 14)  
002B 84 DB 84H  
002C 87 DB 87H  
002D 8B DB 8BH  
002E 8D DB 8DH  
002F 92 DB 92H

More for EPROM programmer.  
37B8 D9 EXX  
37B9 212B00 LD HL,002BH  
37BC 85 ADD A,L  
37BD 6F LD L,A  
37BE 6E LD L,(HL)  
37BF 2636 LD H,36H  
  
37C1 D9 EXX  
37C2 AF XOR A  
37C3 C9 RET  
37C4 00 NOP

## NOTES



USE OF THE TECHNICAL INFORMATION CONTAINED HEREIN IS AT USER'S RISK. WHILE THE INFORMATION HAS BEEN MADE TO BE CORRECT AT THE TIME IT WAS PREPARED, TIMES COMPUTER CORPORATION DOES NOT WARRANT, GUARANTEE, OR REPRESENT THAT THIS INFORMATION IS CORRECT, ACCURATE, OR COMPLETE, OR THAT IT WILL BE KEPT UP TO DATE. TIMES COMPUTER CORPORATION SHALL NOT BE LIABLE FOR ANY DAMAGES, INCLUDING REASONABLE ATTORNEY'S FEES, ARISING OUT OF THE USE OF THIS INFORMATION, EVEN IF SUCH DAMAGES ARE FORESEEABLE. THE INFORMATION CONTAINED HEREIN IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESS OR IMPLIED, INCLUDING THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. THE USER ASSUMES ALL LIABILITY FOR ANY DAMAGES, INCLUDING REASONABLE ATTORNEY'S FEES, ARISING OUT OF THE USE OF THIS INFORMATION, EVEN IF SUCH DAMAGES ARE FORESEEABLE.

NOTES: (UNLESS OTHERWISE SPECIFIED)  
1) ALL RESISTORS ARE 1/4 W. 5% TOLERANCE IN OHMS.  
2) ALL CAPACITORS ARE RATED IN MICROFARADS.  
3) FOR CAPACITORS, INSERT W1 & W2 FOR W3 & W4 (NOT USED)

CHANGES	
NO.	DESCRIPTION

REFERENCE DESIGNATIONS	
LIST	REF. DES.
C78	C1, 13, 28
C828	C82, 3
L8	
O8	O8
U81	U2
U83	
U85	U8, 10, 17, 18
U87	U8, 21, 26, 27
U89	U8, 47, 48, 51
W4	
W2	

COMPONENTS	
COMPONENT	REF. DES.
2N2907	Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, Q10, Q11, Q12, Q13, Q14, Q15, Q16, Q17, Q18, Q19, Q20, Q21, Q22, Q23, Q24, Q25, Q26, Q27, Q28, Q29, Q30, Q31, Q32, Q33, Q34, Q35, Q36, Q37, Q38, Q39, Q40, Q41, Q42, Q43, Q44, Q45, Q46, Q47, Q48, Q49, Q50, Q51, Q52, Q53, Q54, Q55, Q56, Q57, Q58, Q59, Q60, Q61, Q62, Q63, Q64, Q65, Q66, Q67, Q68, Q69, Q70, Q71, Q72, Q73, Q74, Q75, Q76, Q77, Q78, Q79, Q80, Q81, Q82, Q83, Q84, Q85, Q86, Q87, Q88, Q89, Q90, Q91, Q92, Q93, Q94, Q95, Q96, Q97, Q98, Q99, Q100, Q101, Q102, Q103, Q104, Q105, Q106, Q107, Q108, Q109, Q110, Q111, Q112, Q113, Q114, Q115, Q116, Q117, Q118, Q119, Q120, Q121, Q122, Q123, Q124, Q125, Q126, Q127, Q128, Q129, Q130, Q131, Q132, Q133, Q134, Q135, Q136, Q137, Q138, Q139, Q140, Q141, Q142, Q143, Q144, Q145, Q146, Q147, Q148, Q149, Q150, Q151, Q152, Q153, Q154, Q155, Q156, Q157, Q158, Q159, Q160, Q161, Q162, Q163, Q164, Q165, Q166, Q167, Q168, Q169, Q170, Q171, Q172, Q173, Q174, Q175, Q176, Q177, Q178, Q179, Q180, Q181, Q182, Q183, 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Q516, Q517, Q518, Q519, Q520, Q521, Q522, Q523, Q524, Q525, Q526, Q527, Q528, Q529, Q530, Q531, Q532, Q533, Q534, Q535, Q536, Q537, Q538, Q539, Q540, Q541, Q542, Q543, Q544, Q545, Q546, Q547, Q548, Q549, Q550, Q551, Q552, Q553, Q554, Q555, Q556, Q557, Q558, Q559, Q560, Q561, Q562, Q563, Q564, Q565, Q566, Q567, Q568, Q569, Q570, Q571, Q572, Q573, Q574, Q575, Q576, Q577, Q578, Q579, Q580, Q581, Q582, Q583, Q584, Q585, Q586, Q587, Q588, Q589, Q590, Q591, Q592, Q593, Q594, Q595, Q596, Q597, Q598, Q599, Q600, Q601, Q602, Q603, Q604, Q605, Q606, Q607, Q608, Q609, Q610, Q611, Q612, Q613, Q614, Q615, Q616, Q617, Q618, Q619, Q620, Q621, Q622, Q623, Q624, Q625, Q626, Q627, Q628, Q629, Q630, Q631, Q632, Q633, Q634, Q635, Q636, Q637, Q638, Q639, Q640, Q641, Q642, Q643, Q644, Q645, Q646, Q647, Q648, Q649, Q650, Q651, Q652, Q653, Q654, Q655, Q656, Q657, Q658, Q659, Q660, Q661, Q662, Q663, Q664, Q665, Q666, Q667, Q668, Q669, Q670, Q671, Q672, Q673, Q674, Q675, Q676, Q677, Q678, Q679, Q680, Q681, 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